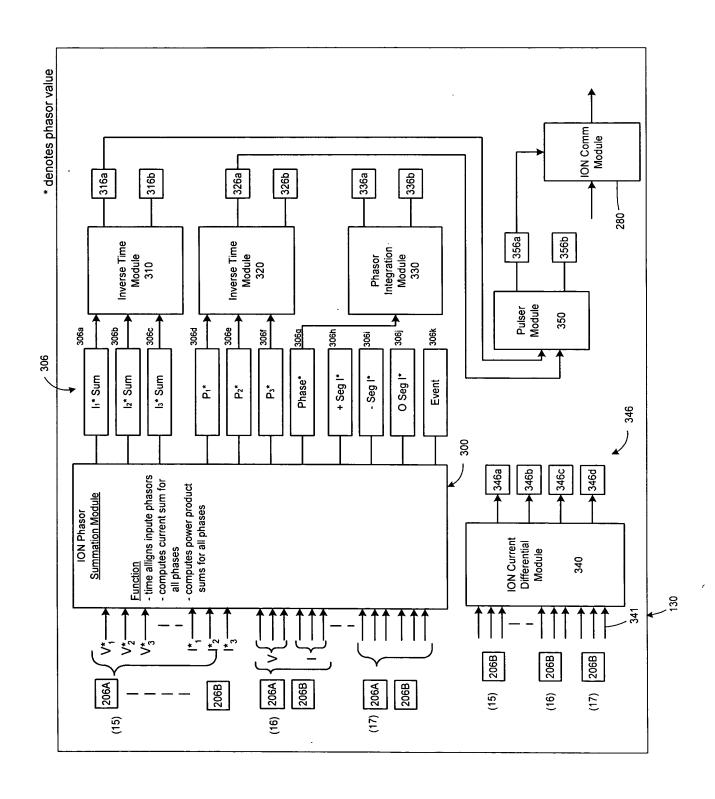
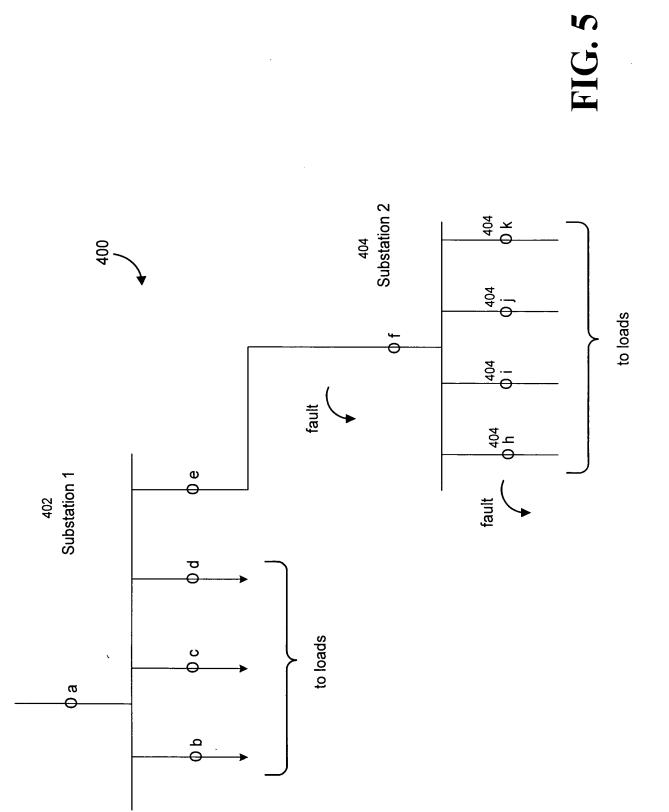


Phasion ION Moudle Diagram

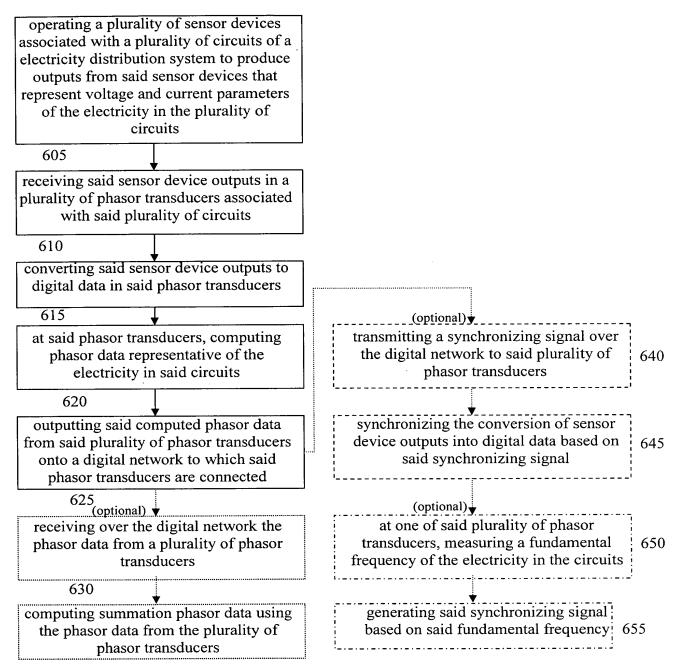
FIG. 4





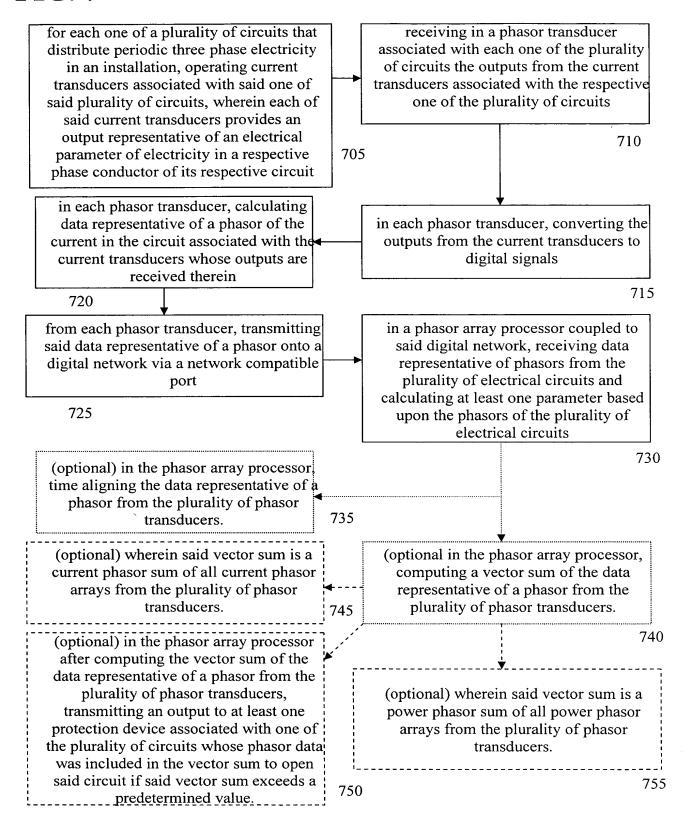
(6/207)

FIG. 6



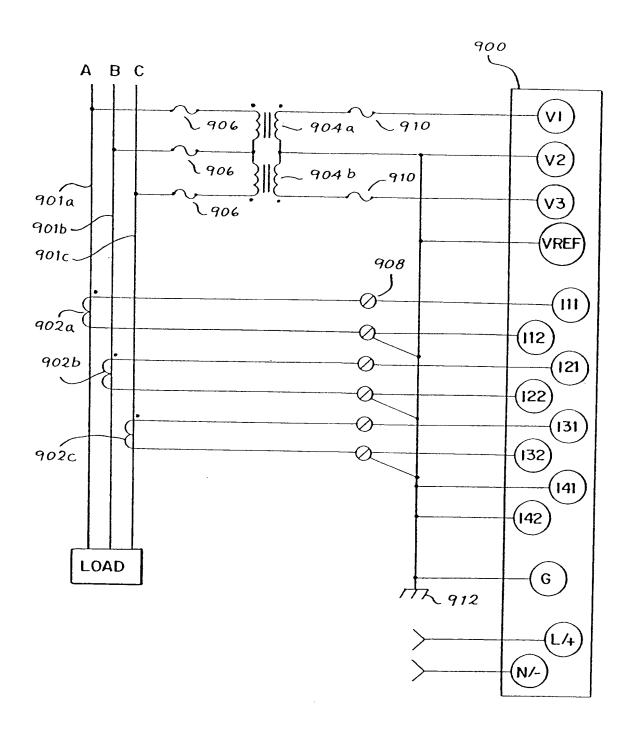
(7/207)

FIG. 7



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FIG. 8



9 17 207

FIG. 9

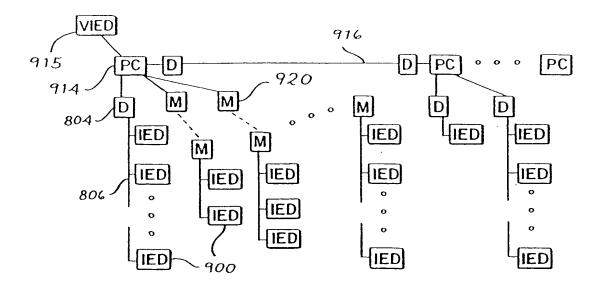
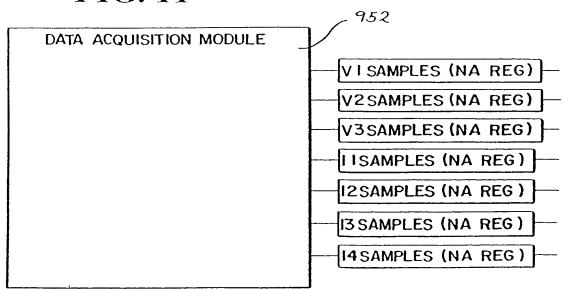
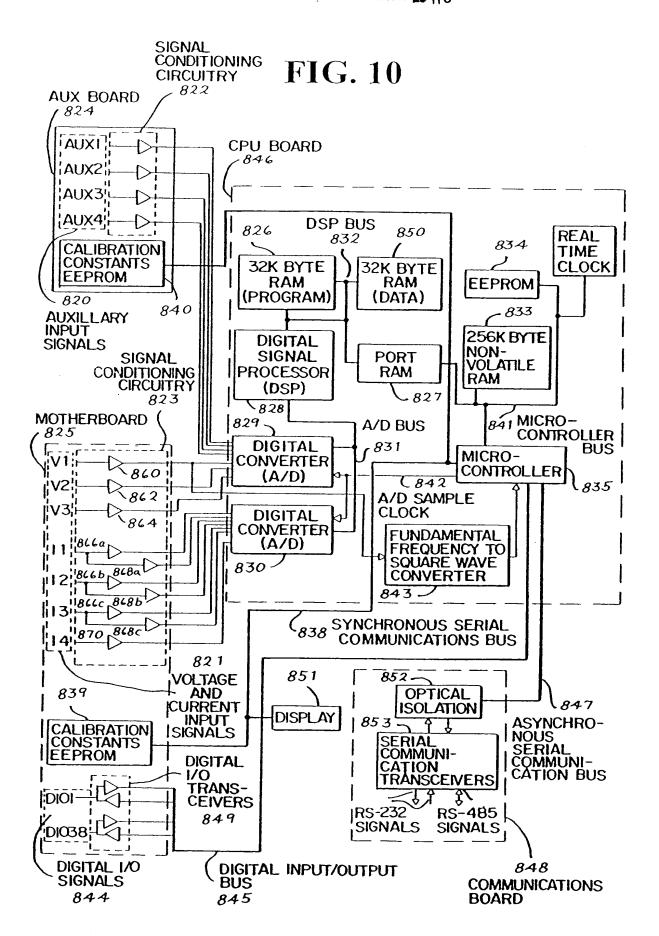


FIG. 11

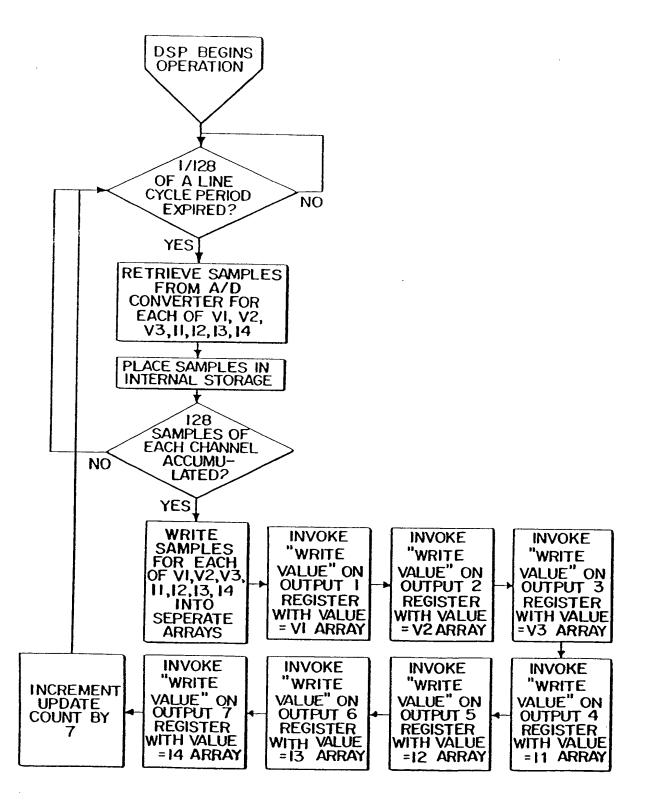




11 8 207

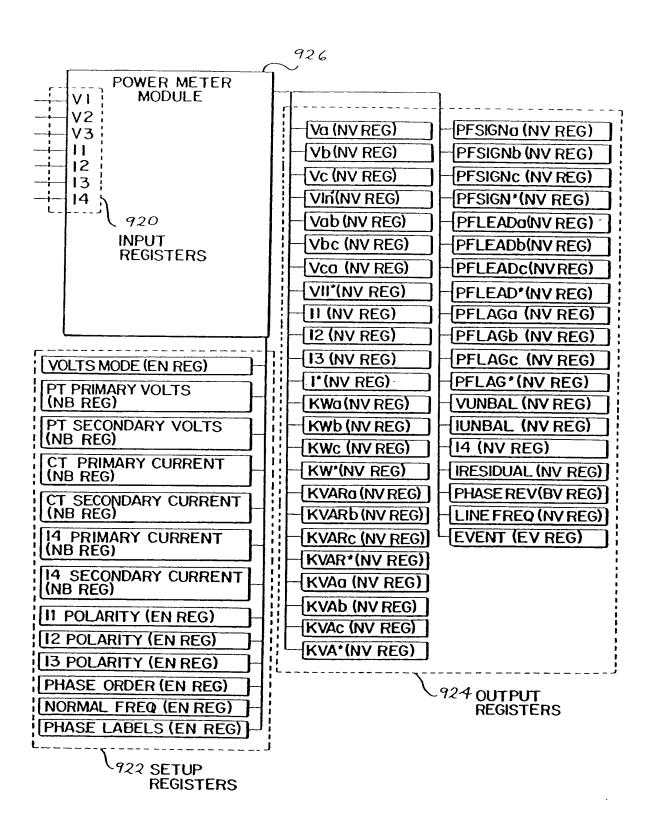


FIG. 11A



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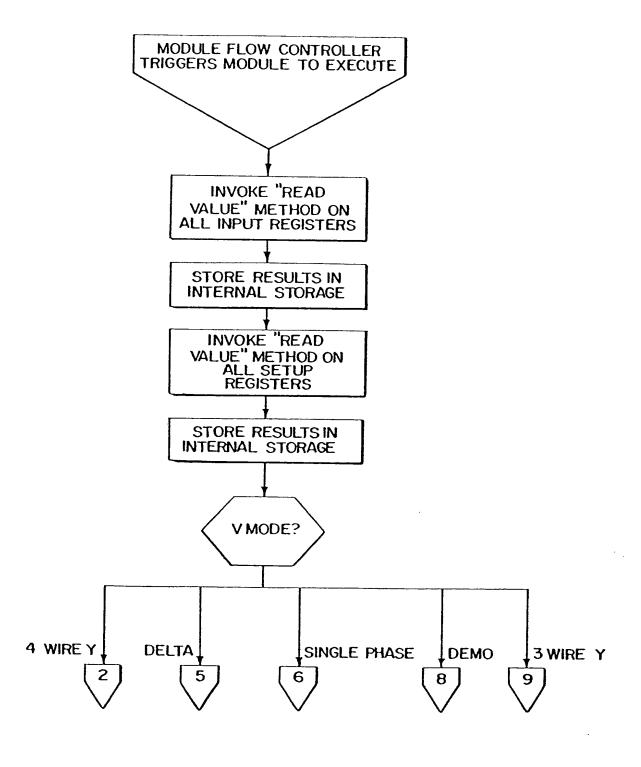
FIG. 12



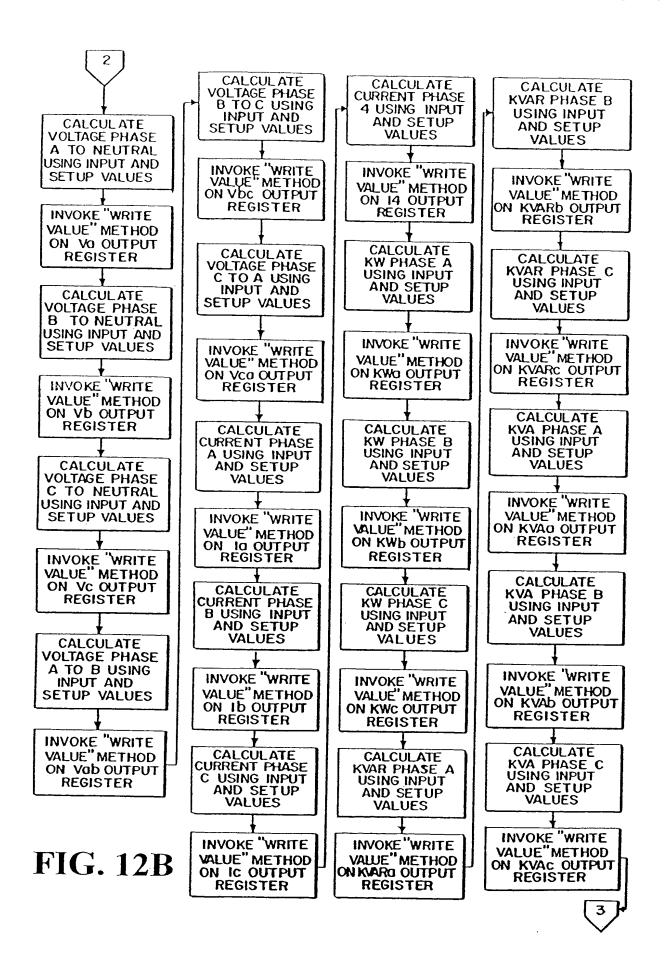
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FIG. 12A

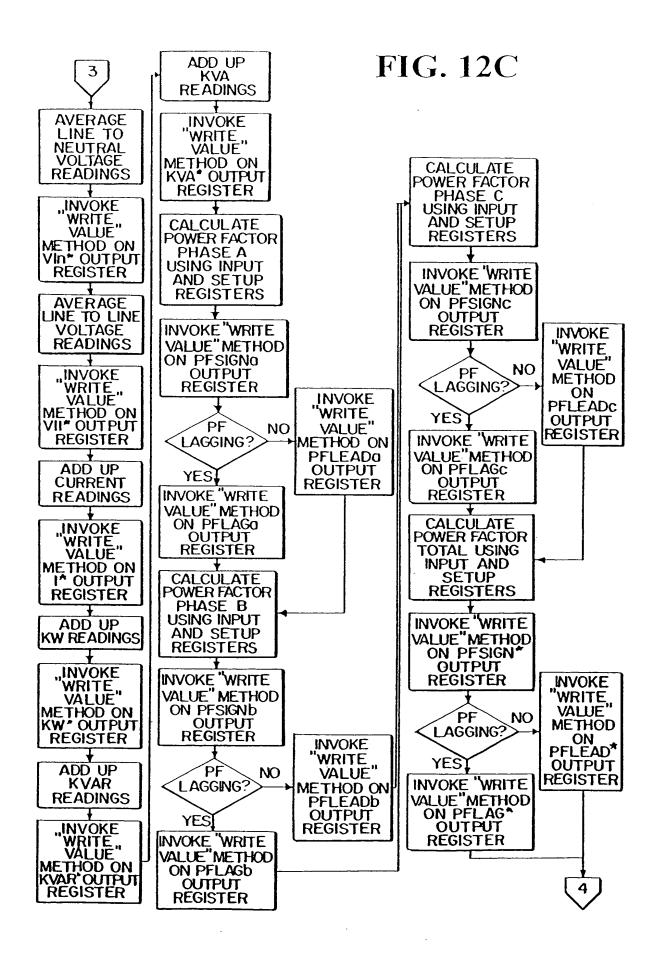
POWER METER MODULE OPERATION (CLIENT PORTION)



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FIG. 12D

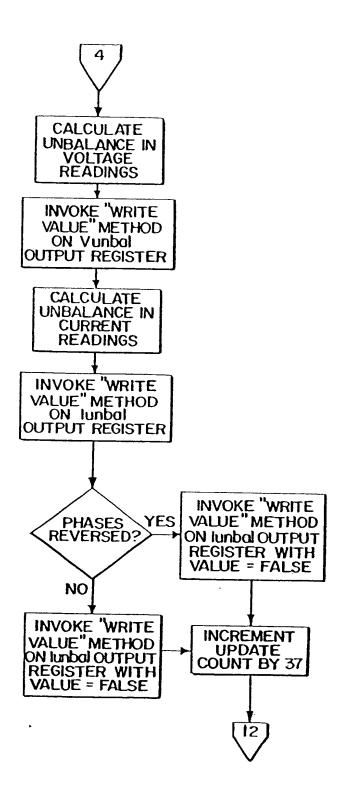
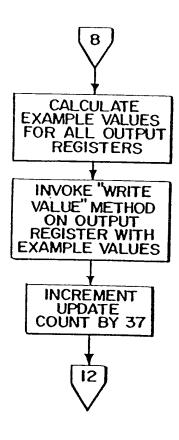


FIG. 12H

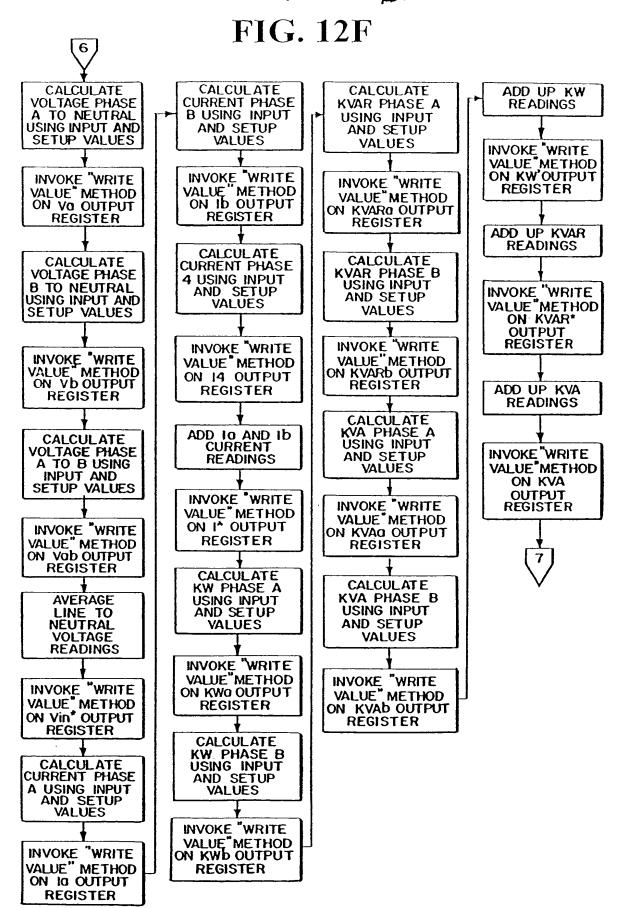


17 8207

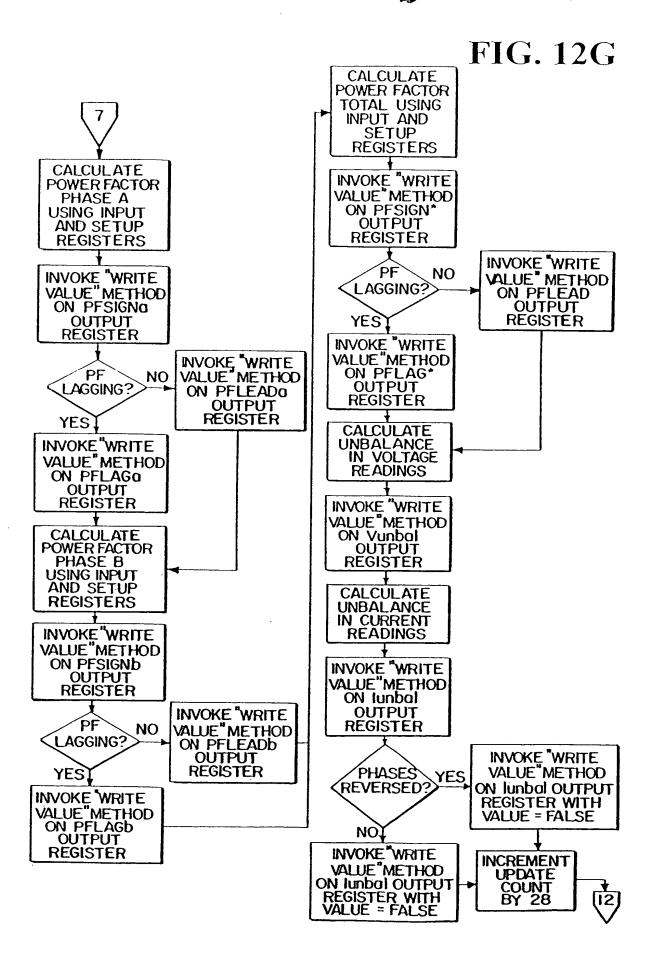
FIG. 12E CALCULATE INVOKE WRITE CALCULATE INVOKE WRITE VOLTAGE PHASE VALUE" METHOD ON ID OUTPUT TOTAL KVAR VALUE" METHOD ON VUNDOI A TO B USING **USING INPUT** INPUT AND AND REGISTER OUTPUT SETUP VALUES SETUP VALUES REGISTER CALCULATE INVOKE "WRITE INVOKE WRITE CURRENT PHASE CALCULATE VALUE" METHOD ON VOD OUTPUT VALUE" METHOD ON KVAR OUTPUT C USING INPUT UNBALANCE IN AND CURRENT REGISTER SETUP VALUES REGISTER READINGS CALCULATE CALCULATE INVOKE WRITE INVOKE WRITE VOLTAGE PHASE TOTAL KVA VALUE" METHOD ON IC OUTPUT B TO C USING VALUE" METHOD USING INPUT **INPUT AND** ON Tunbal AND REGISTER SETUP VALUES SETUP VALUES OUTPUT REGISTER CALCULATE INVOKE "WRITE VALUE" METHOD ON KVA OUTPUT INVOKE "WRITE **AVERAGE** VALUE" METHOD PHASE TO PHASE ON Vbc OUTPUT **VOLTAGE** PHASES YES REGISTER REGISTER REVERSED? INVOKE "WRITE CALCULATE CALCULATE VALUE" METHOD VOLTAGE PHASE POWER FACTOR NO ON VII OUTPUT TOTAL USING C TO A USING REGISTER INPUT AND SETUP VALUES INPUT AND INVOKE WRITE INVOKE WRITE SETUP VALUES VALUE" METHOD VALUE" METHOD ON lunbol ON lunbal CALCULATE OUTPUT OUTPUT TOTAL PHASE INVOKE WRITE INVOKE "WRITE REGISTER CURRENT REGISTER VALUE" METHOD VALUE"METHOD WITH VALUE WITH VALUE ON Vca OUTPUT ON PFSIGN = FALSE = FALSE REGISTER OUTPUT INVOKE WRITE VALUE METHOD ON 18 OUTPUT REGISTER **CALCULATE** INVOKE WRITE REGISTER CURRENT PHASE A USING INPUT VALUE METHOD NO INCREMENT ON PFLEAD* UPDATE LAGGING? AND COUNT BY 17 OUTPUT SETUP VALUES CALCULATE REGISTER TOTAL KW YES **USING INPUT** INVOKE WRITE AND INVOKE "WRITE VALUE" METHOD CALCULATE SETUP VALUES UNBALANCE IN VALUE" METHOD VOLTAGE ON 1a OUTPUT ON PFLAG READINGS REGISTER **OUTPUT** INVOKE "WRITE VALUE" METHOD ON KW" OUTPUT REGISTER CALCULATE CURRENT PHASE B USING INPUT AND

SETUP VALUES

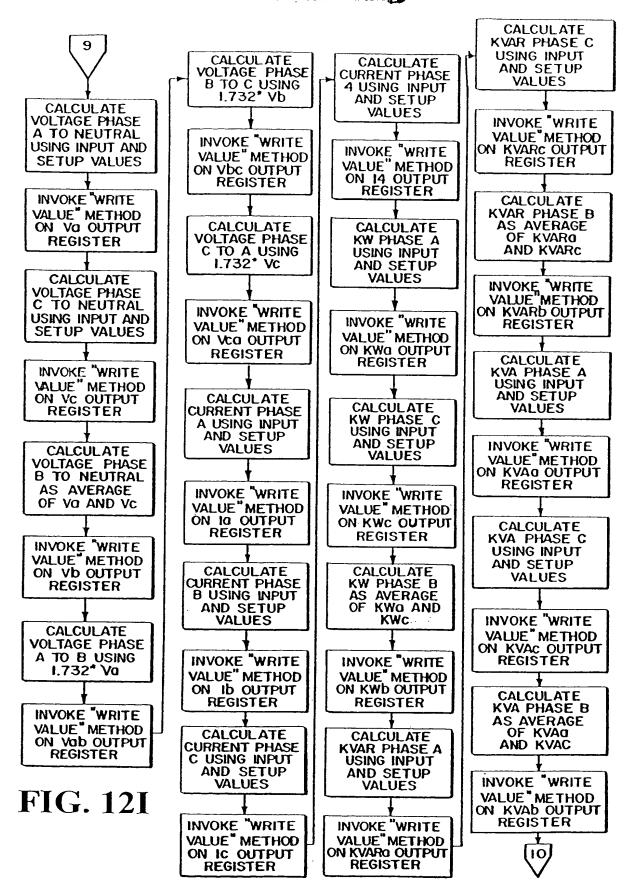
18 9,307



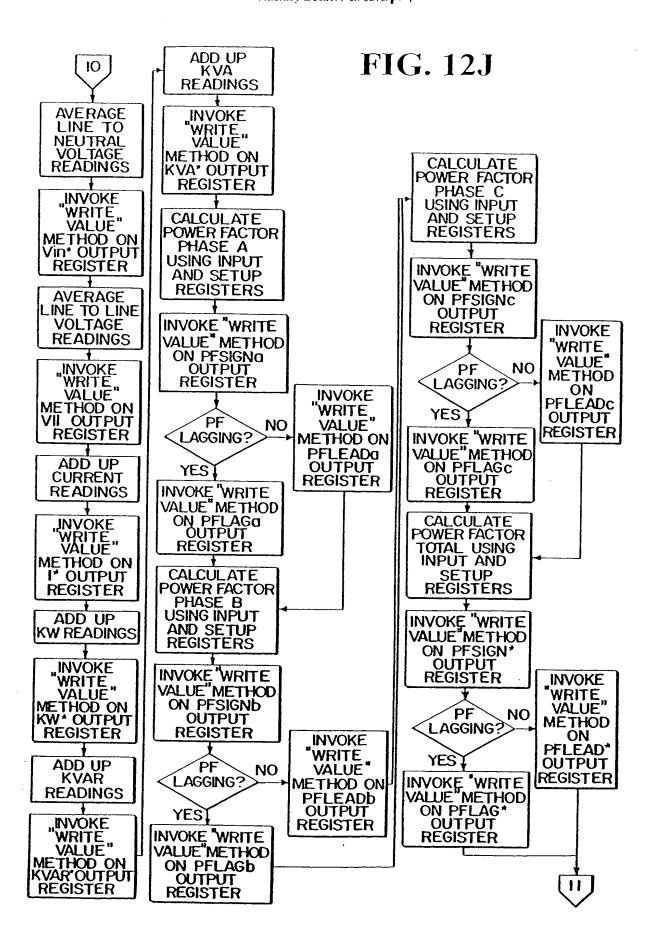
19 12 207



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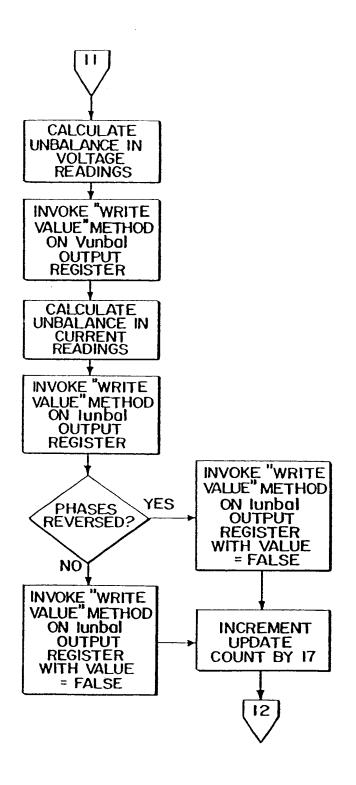
21 07,207

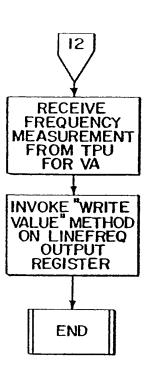


22 00207

FIG. 12K

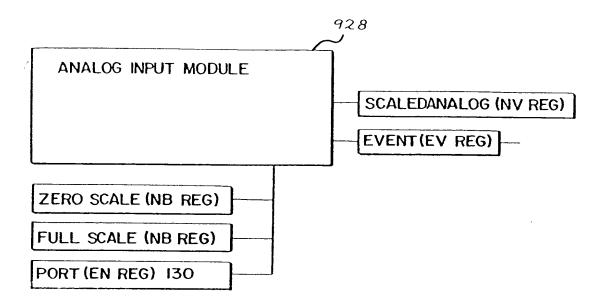
FIG. 12L

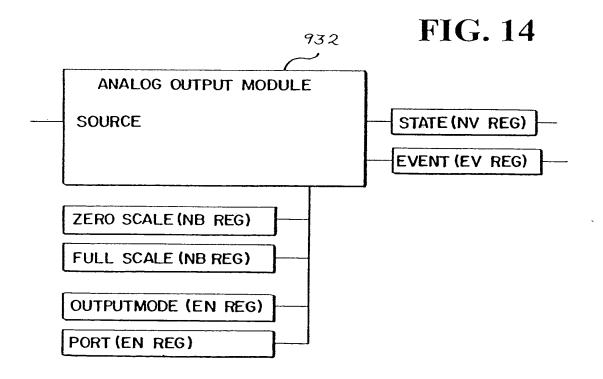




133827

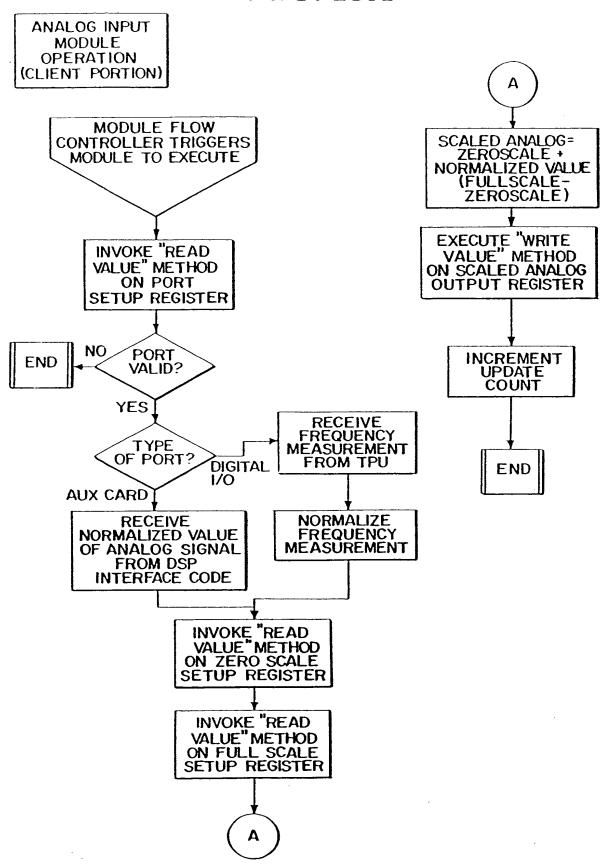
FIG. 13





24 6 307

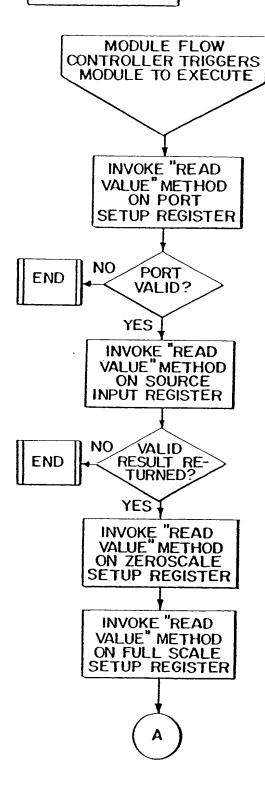
FIG. 13A

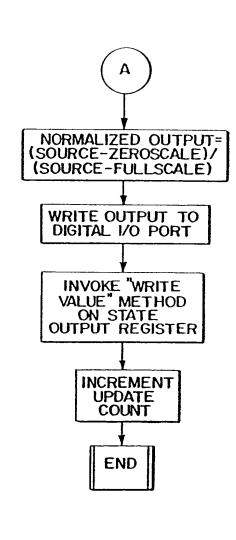


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FIG. 14A

ANALOG OUTPUT MODULE OPERATION (CLIENT PORTION)





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FIG. 15

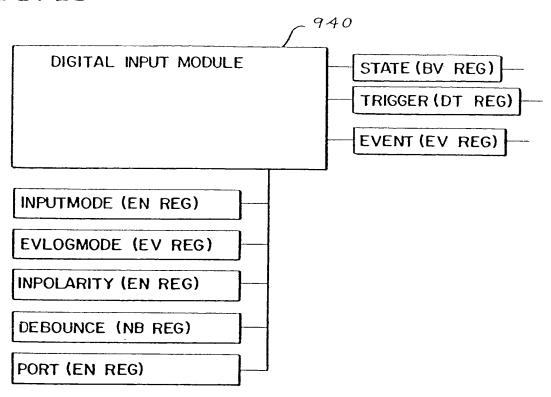


FIG. 16

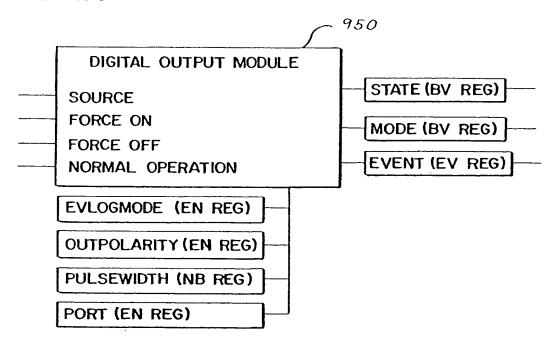
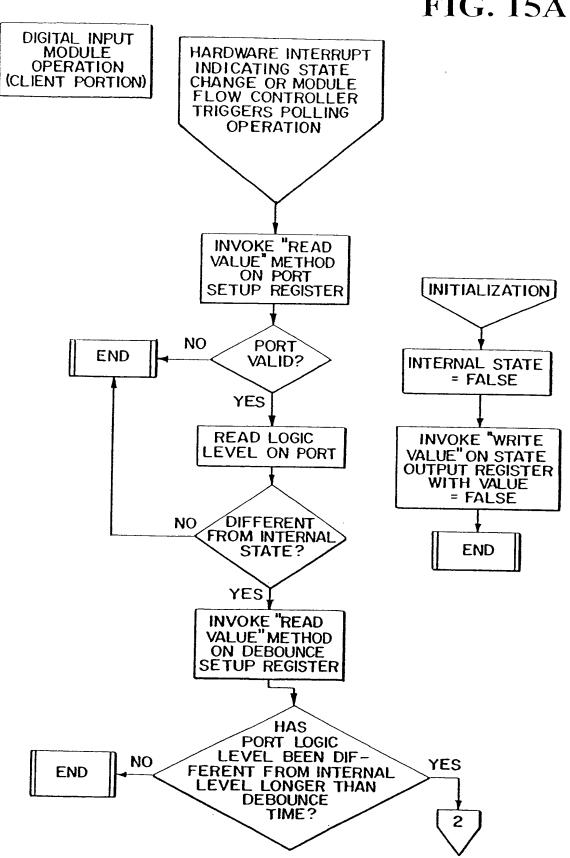
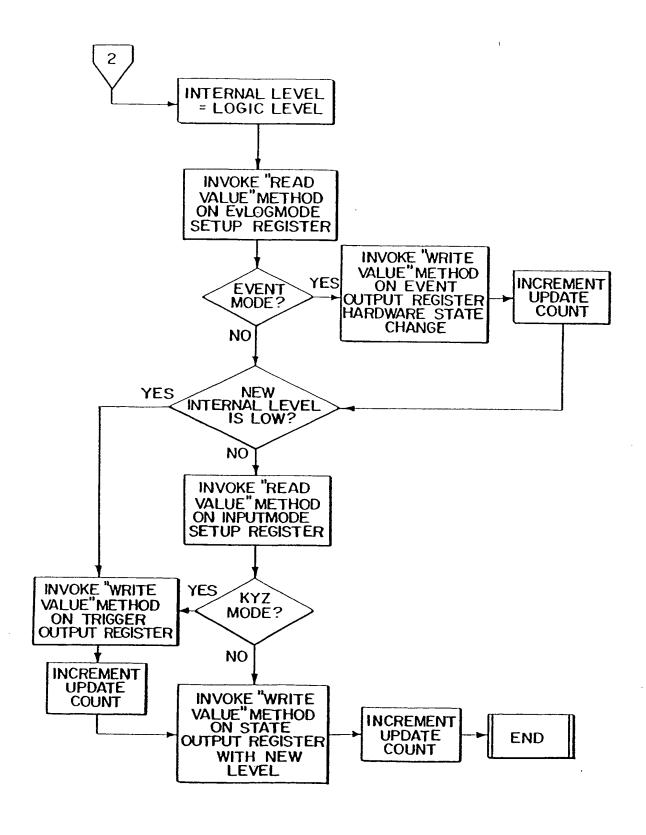


FIG. 15A



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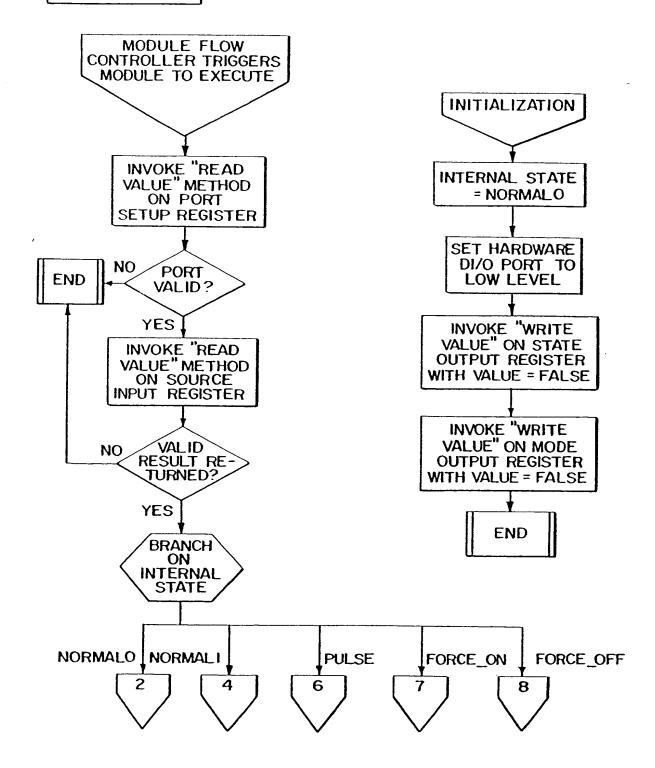
FIG. 15B



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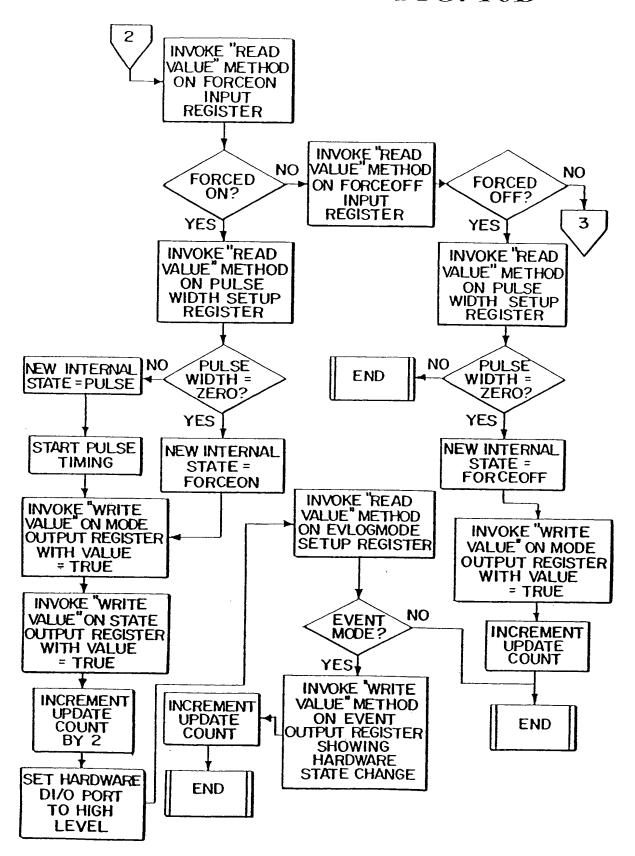
FIG. 16A

DIGITAL OUTPUT MODULE OPERATION (CLIENT PORTION)



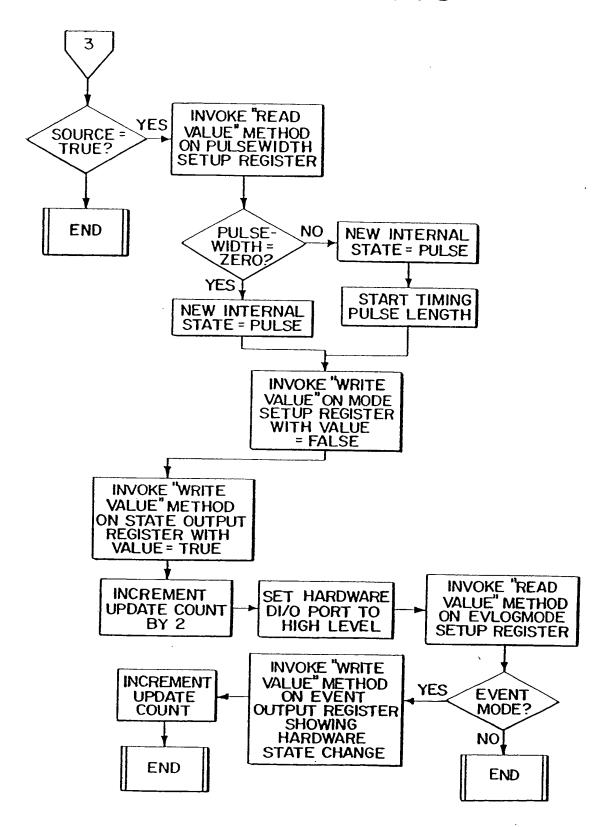
30 8007

FIG. 16B

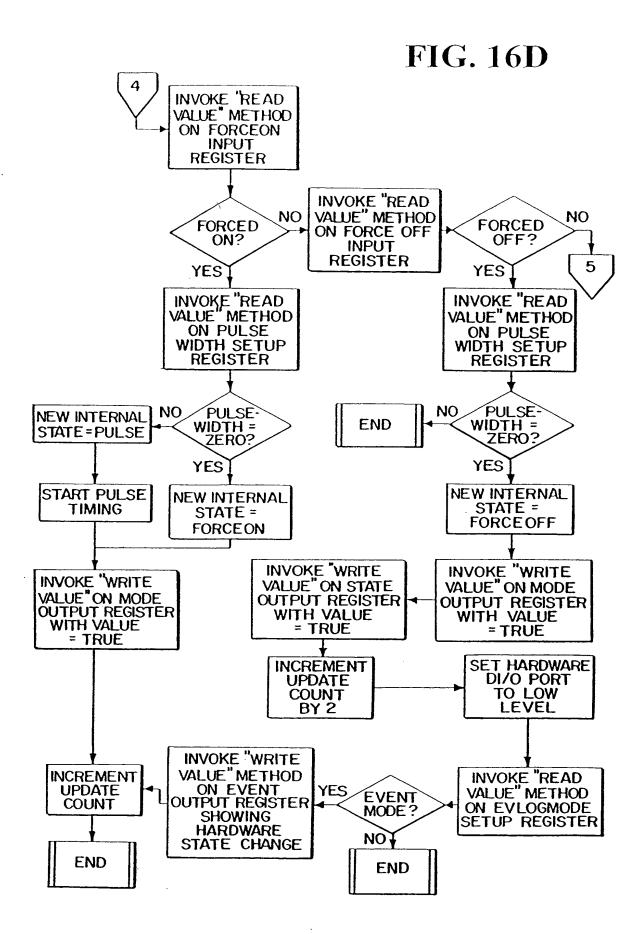


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FIG. 16C

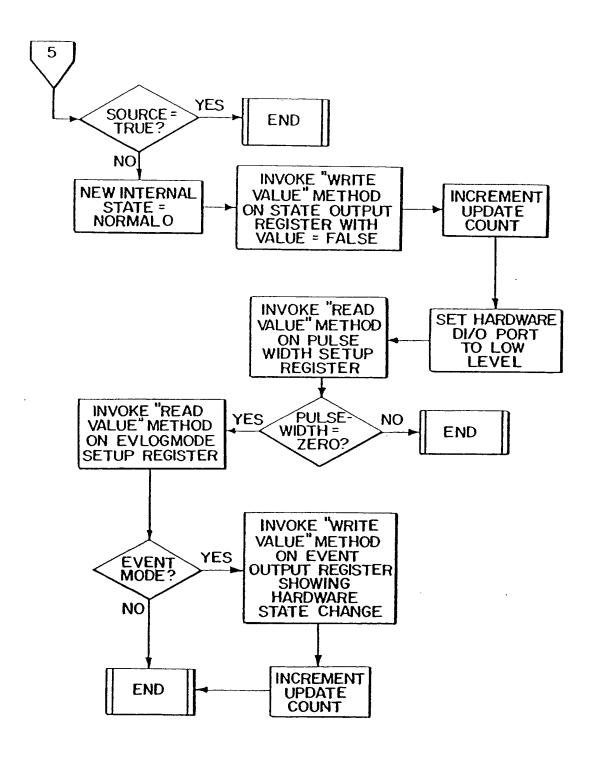


30 9007



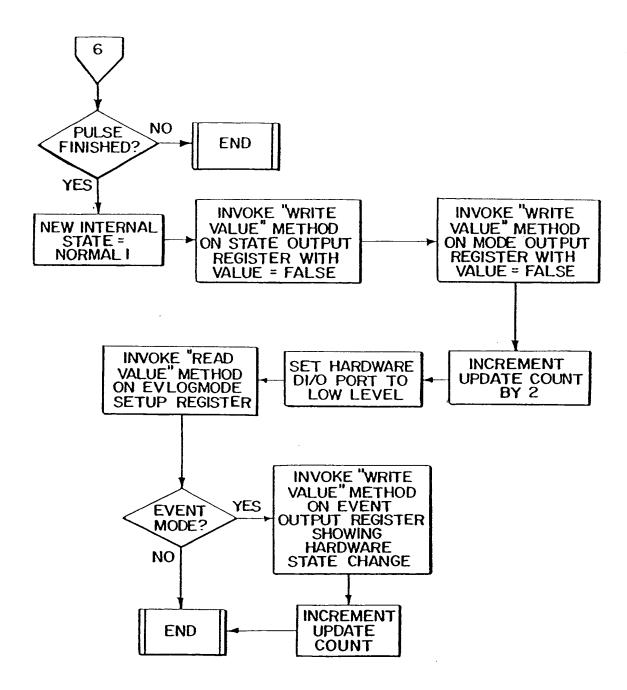
33 2007

FIG. 16E



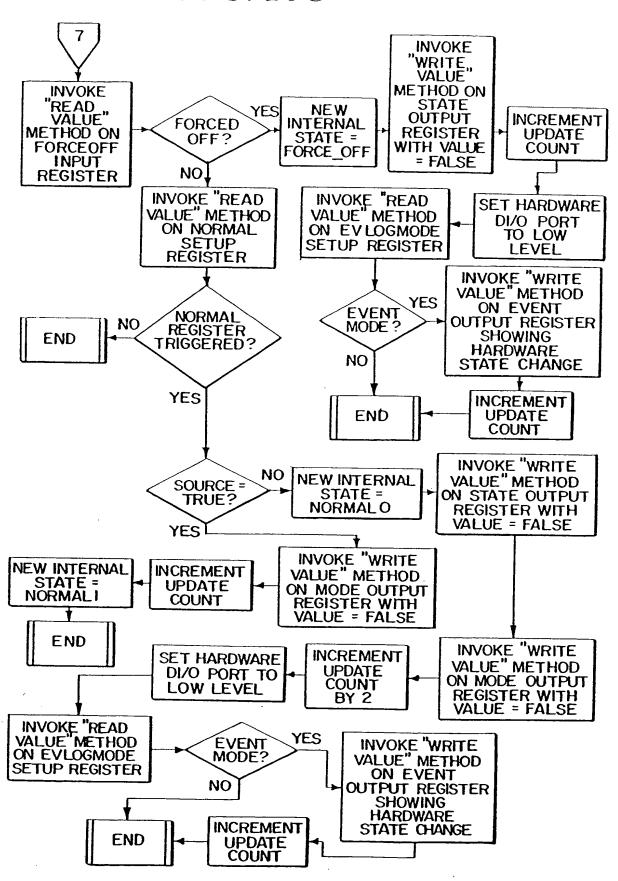
34 6 207

FIG. 16F



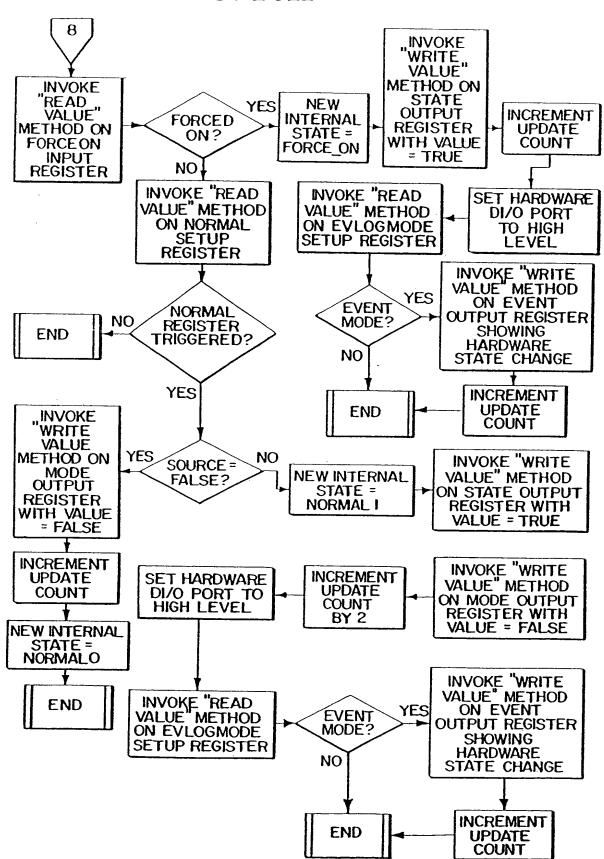
35 9207

FIG. 16G



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FIG. 16H



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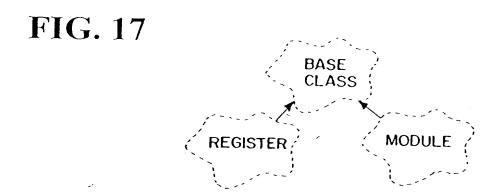
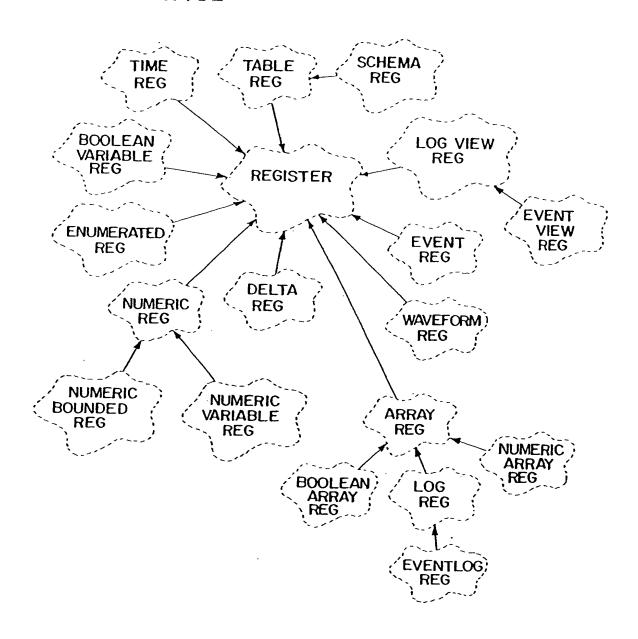
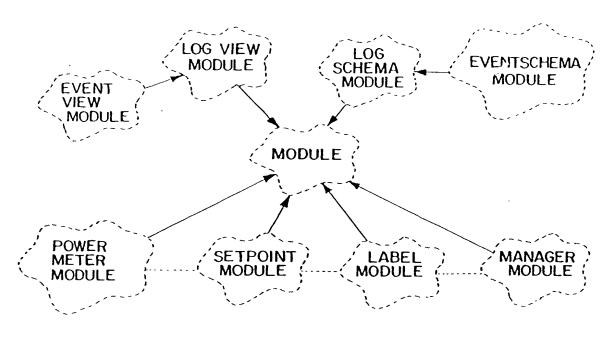


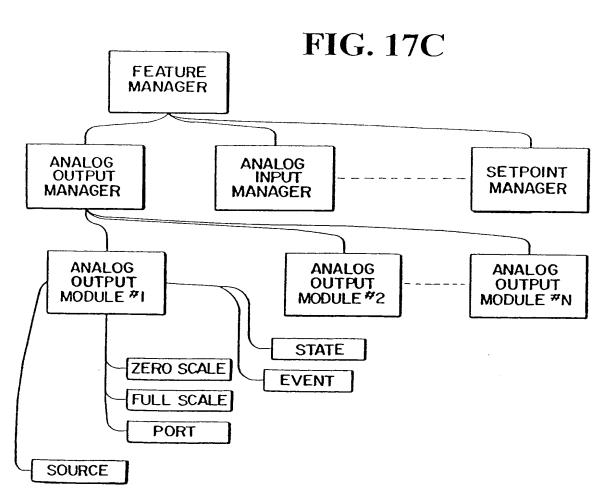
FIG. 17A



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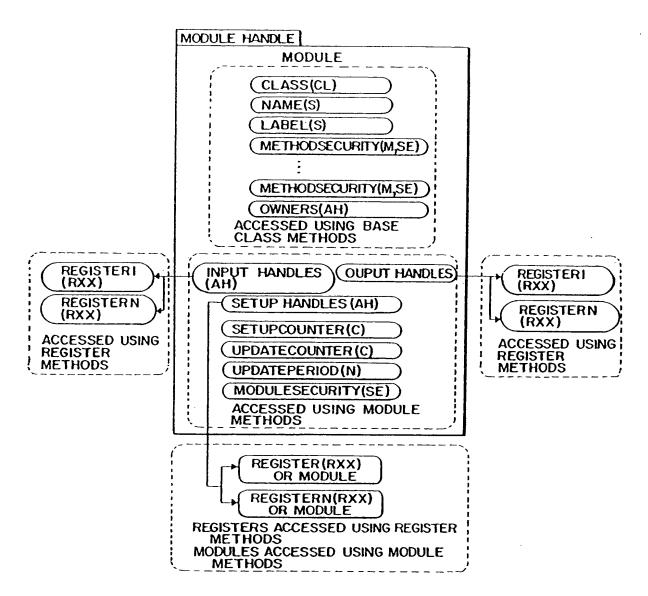
FIG. 17B

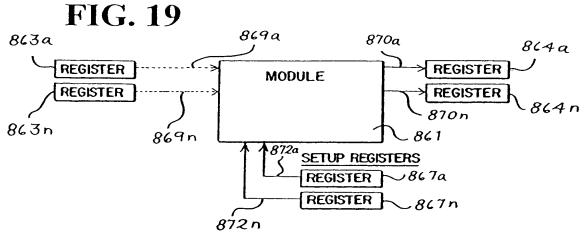




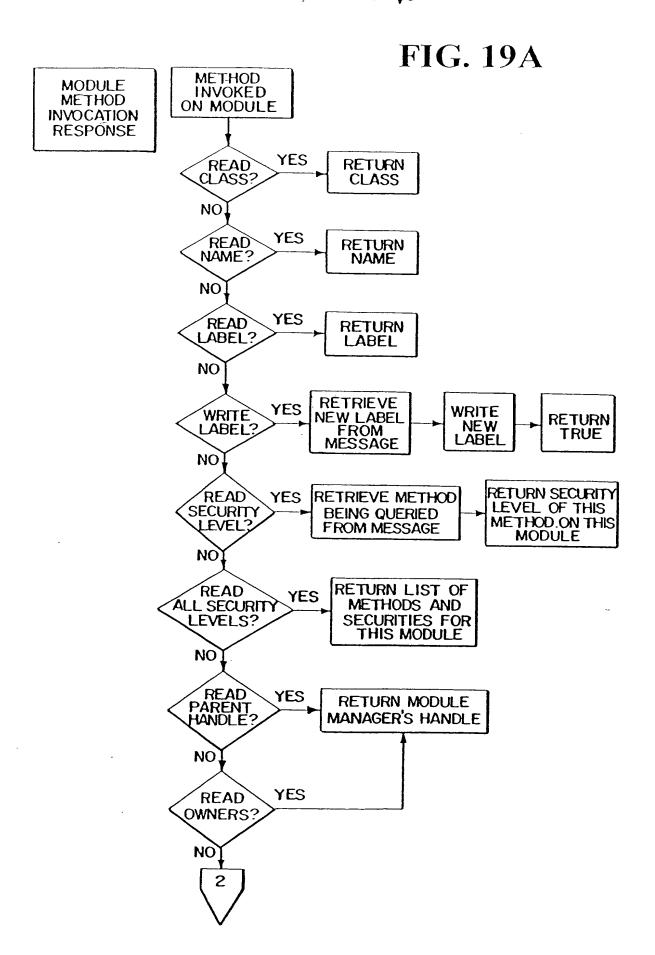
39 9.307

FIG. 18



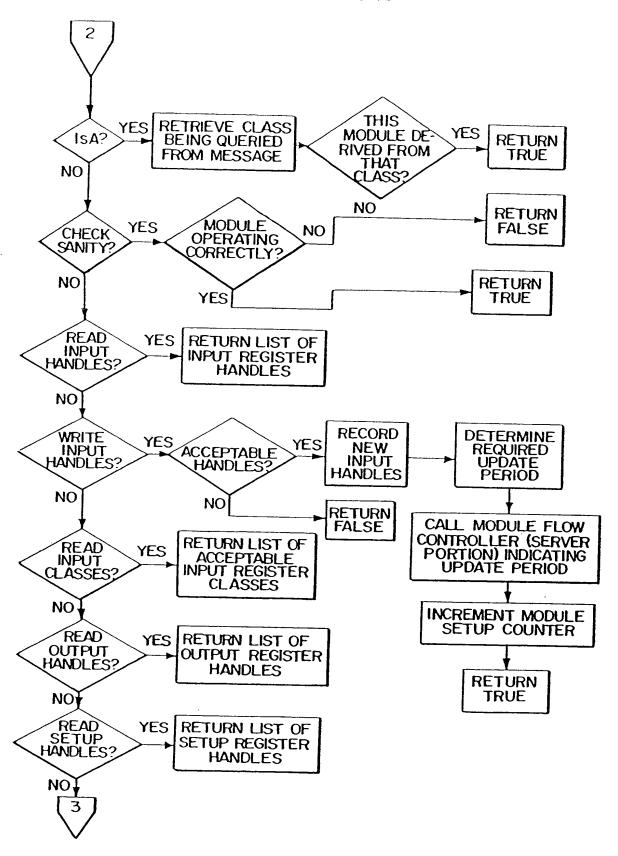


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FIG. 19B



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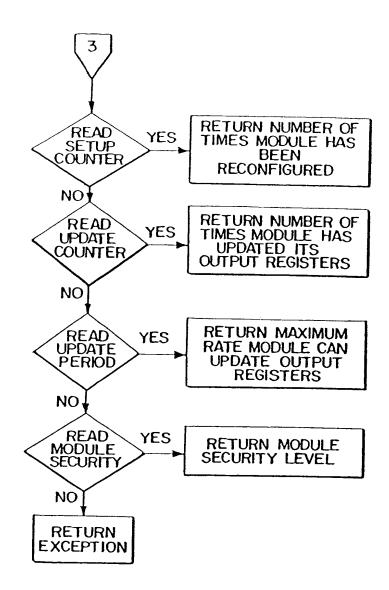
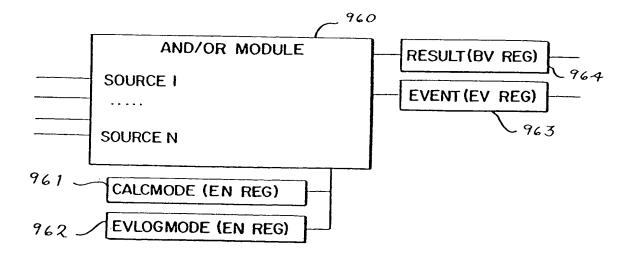


FIG. 20



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FIG. 20A

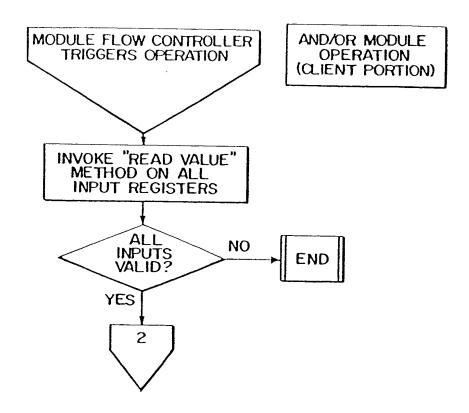
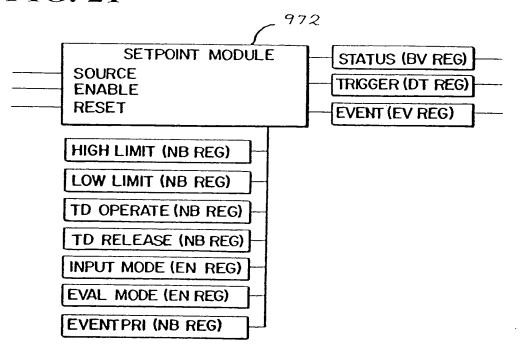
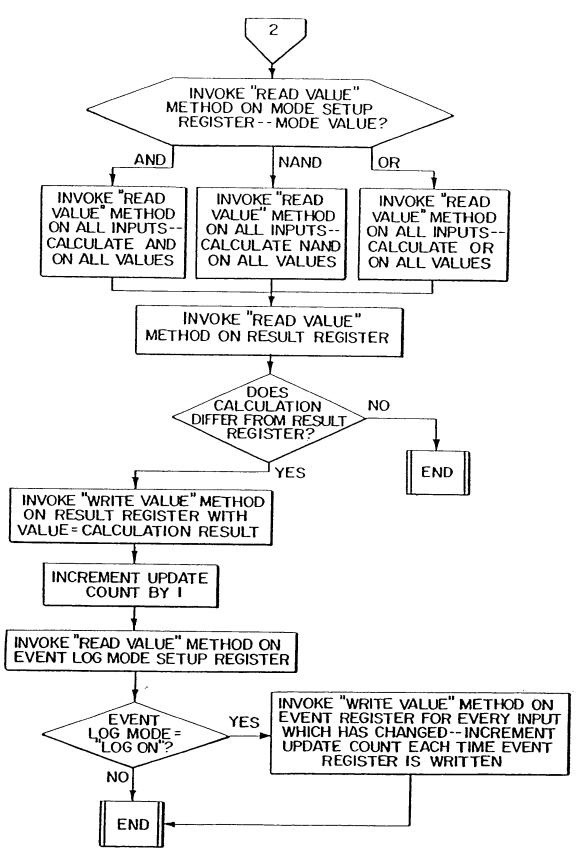


FIG. 21



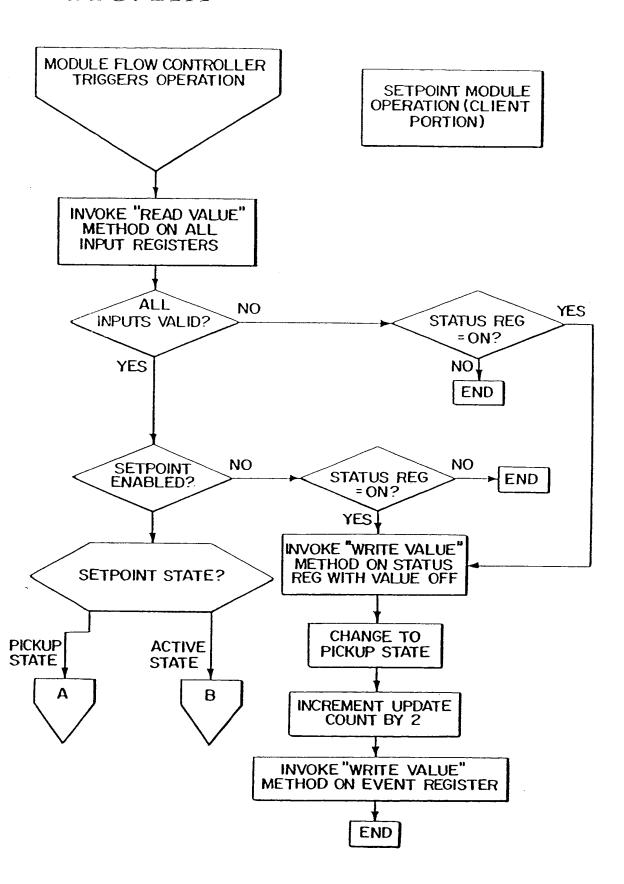
44 8 201

FIG. 20B



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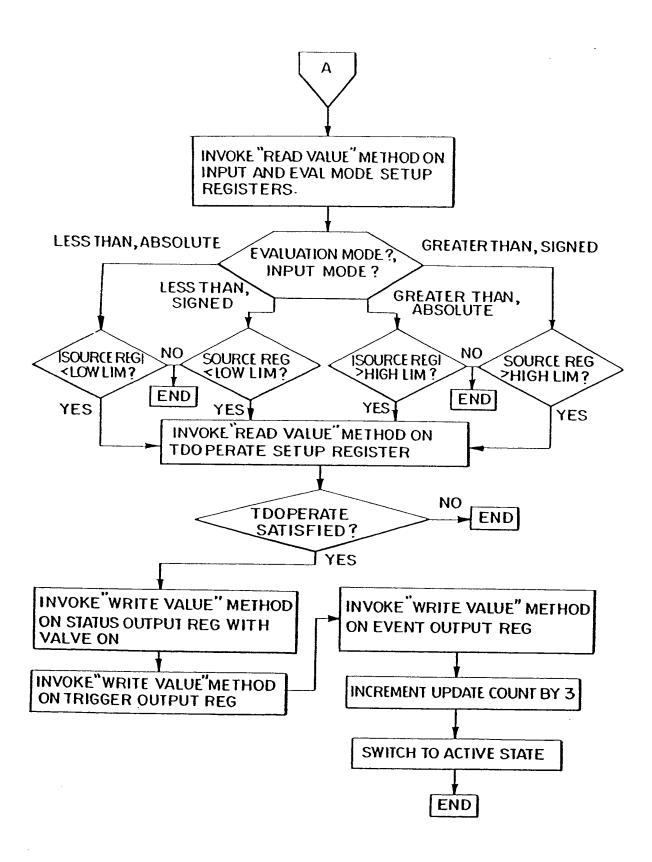
FIG. 21A



Inventor(s): Ronald G. Hart Attorney Docket No : 6270/\$\(\square\)

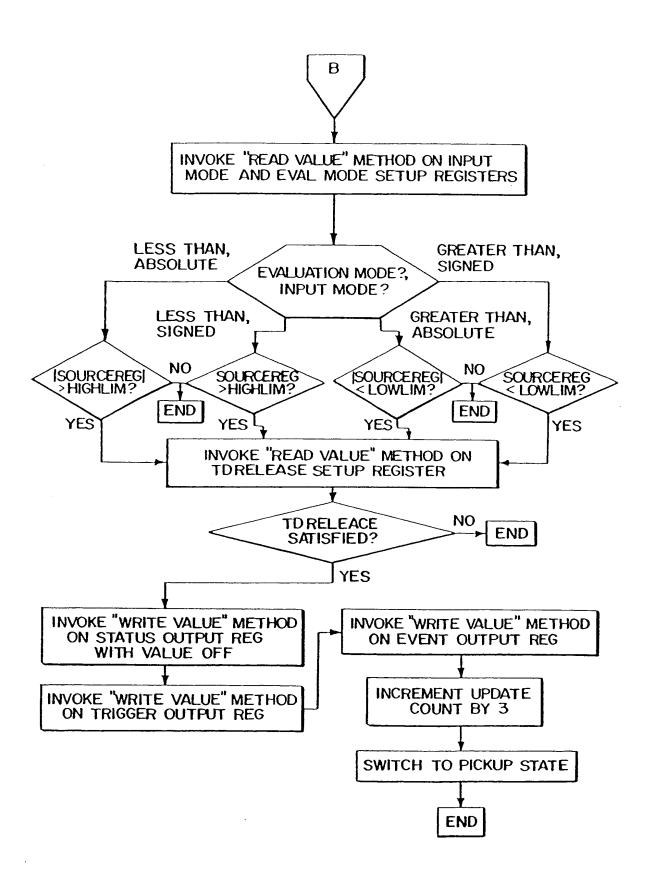
46 9007

FIG. 21B

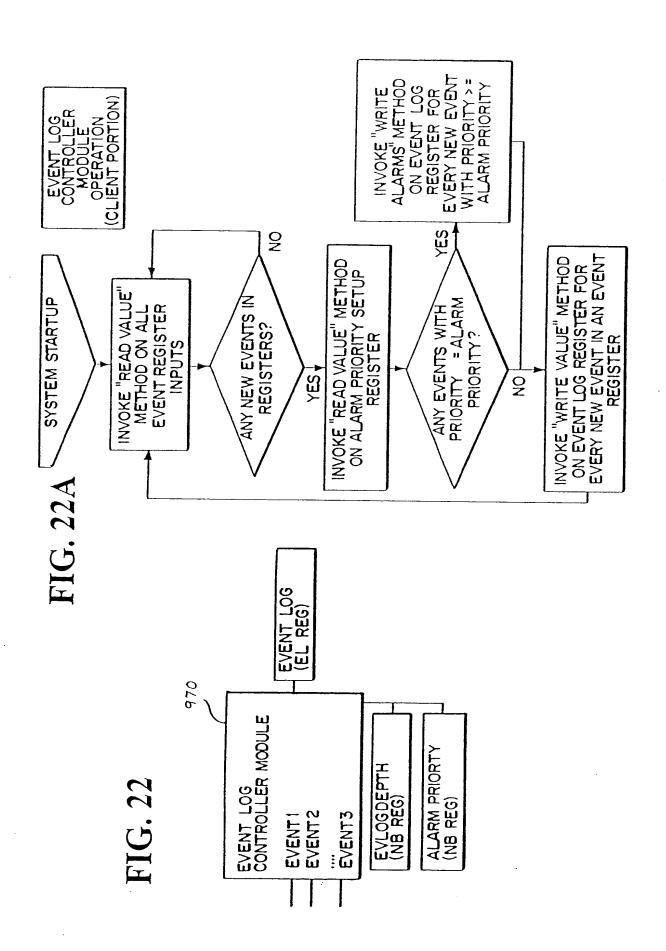


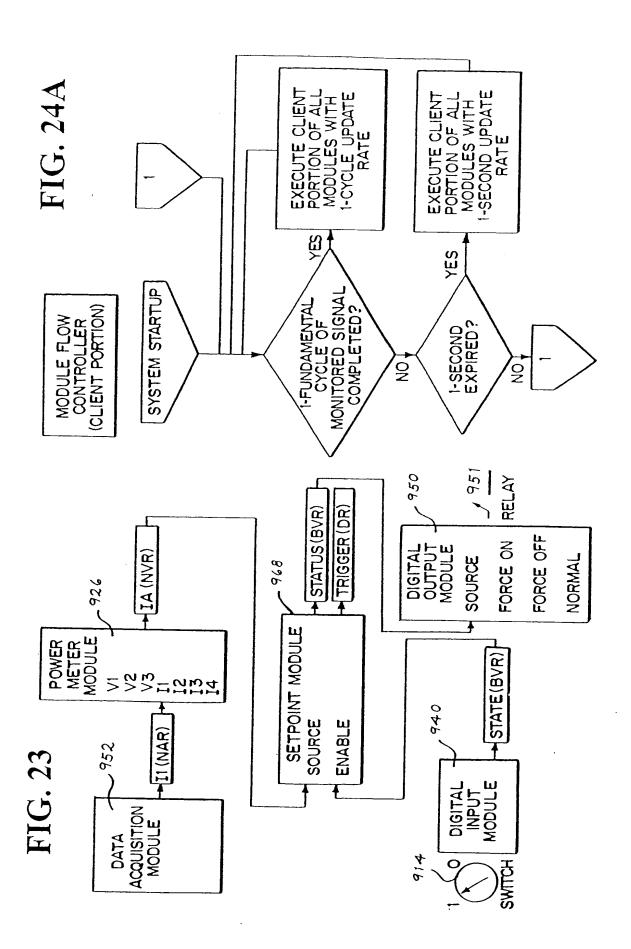
47 9207

FIG. 21C



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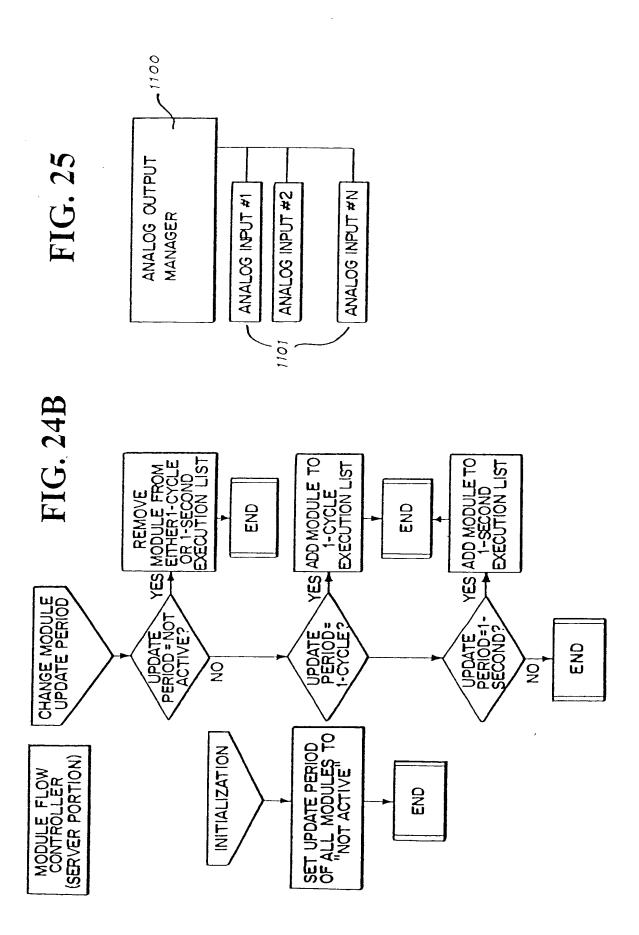
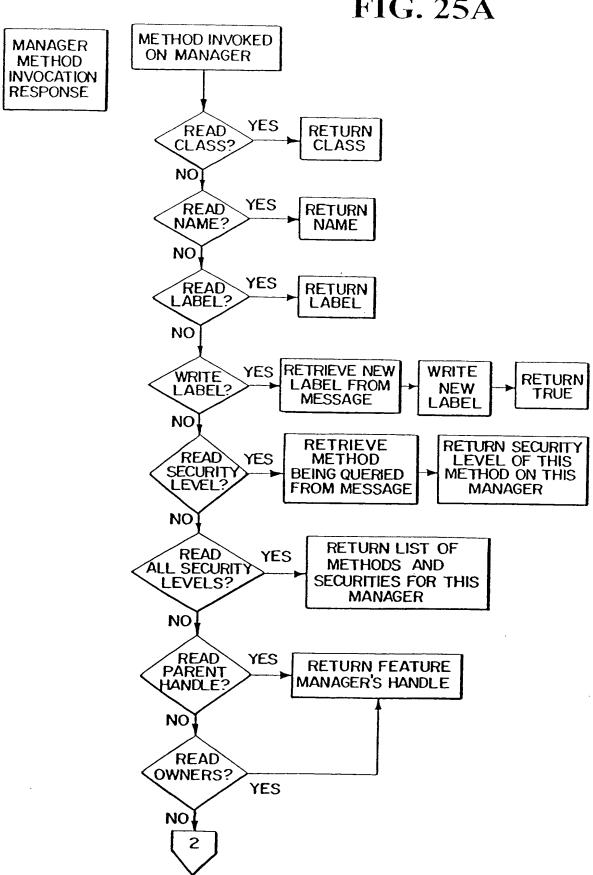
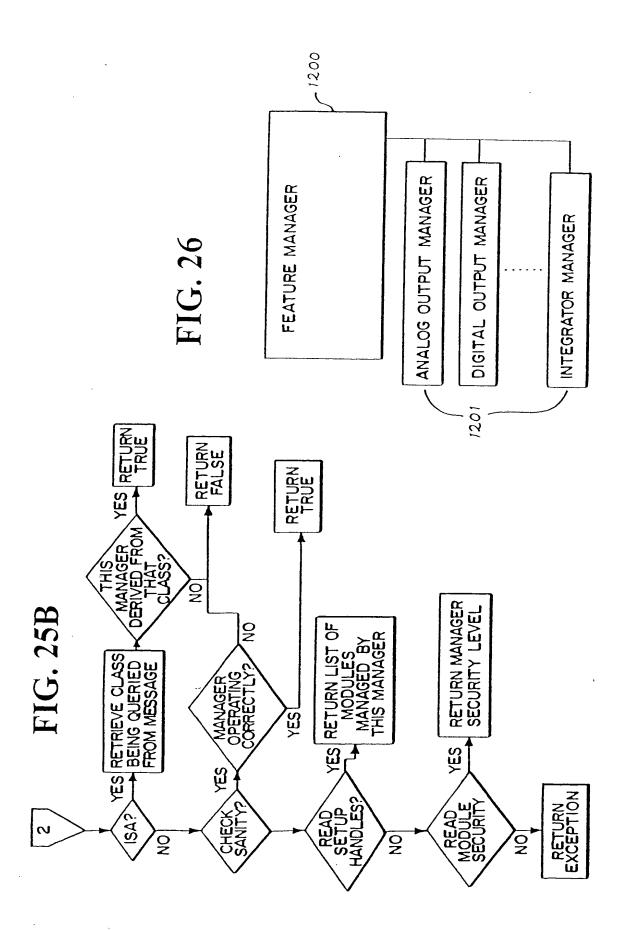


FIG. 25A

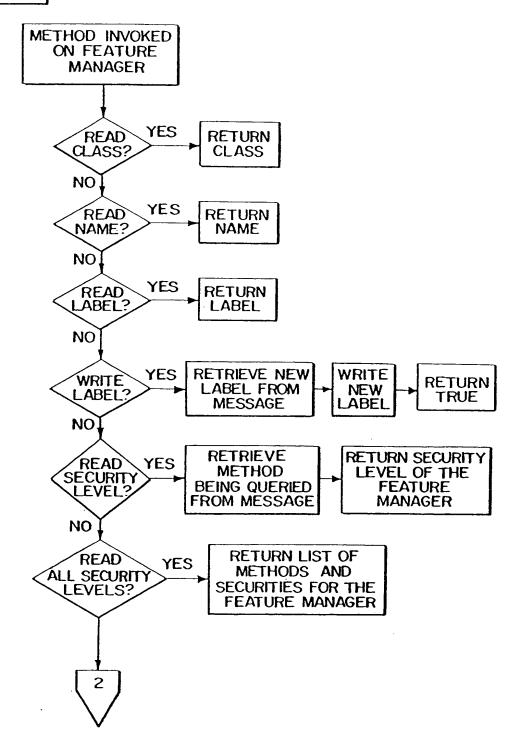




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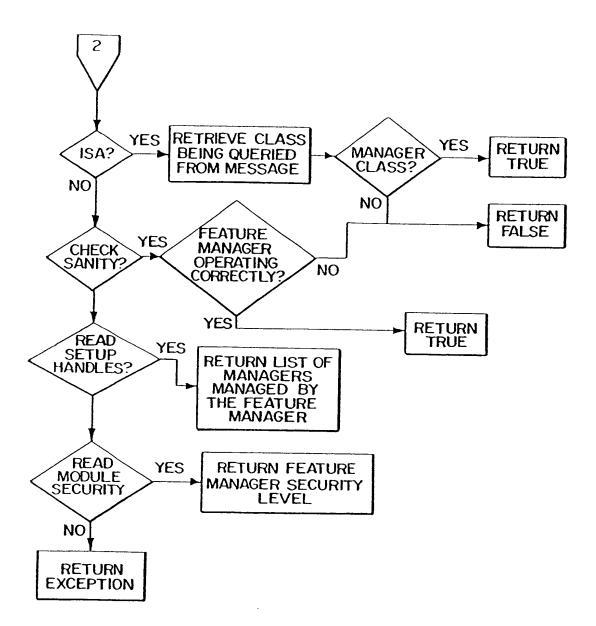
FIG. 26A

FEATURE MANAGER METHOD INVOCATION RESPONSE



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FIG. 26B



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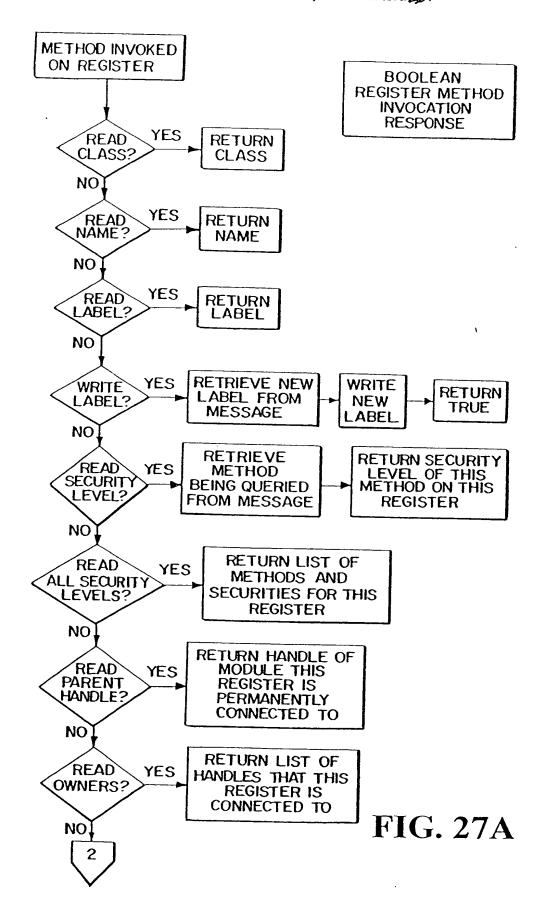
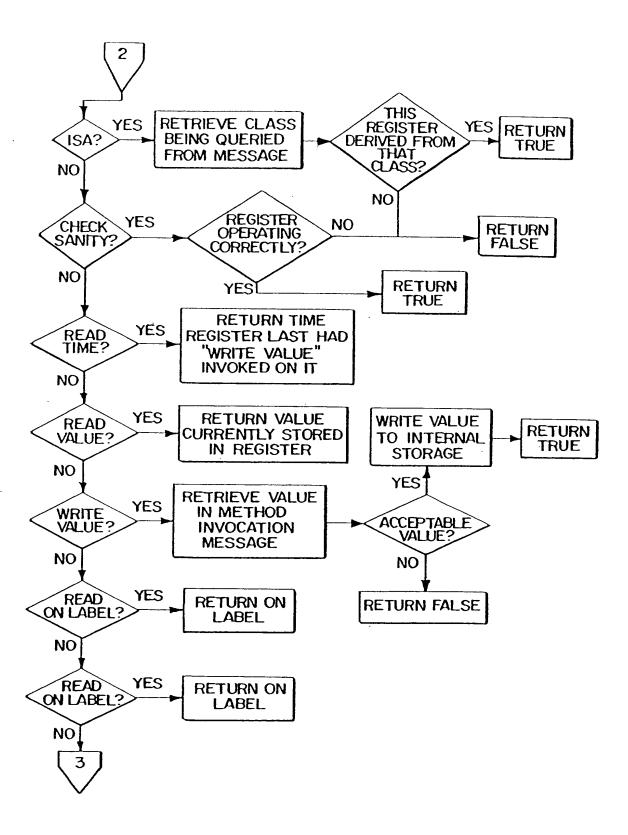


FIG. 27B



MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS

57 8207

Inventor(s): Ronald G. Hart Attorney Docket No.: 6270/

FIG. 27C

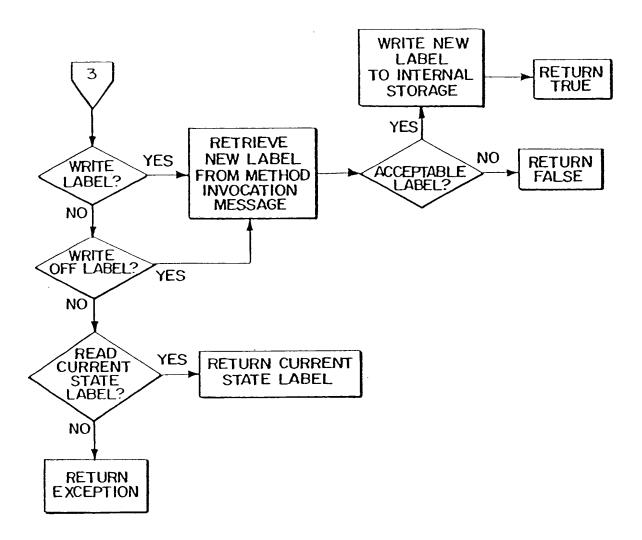


FIG. 28A

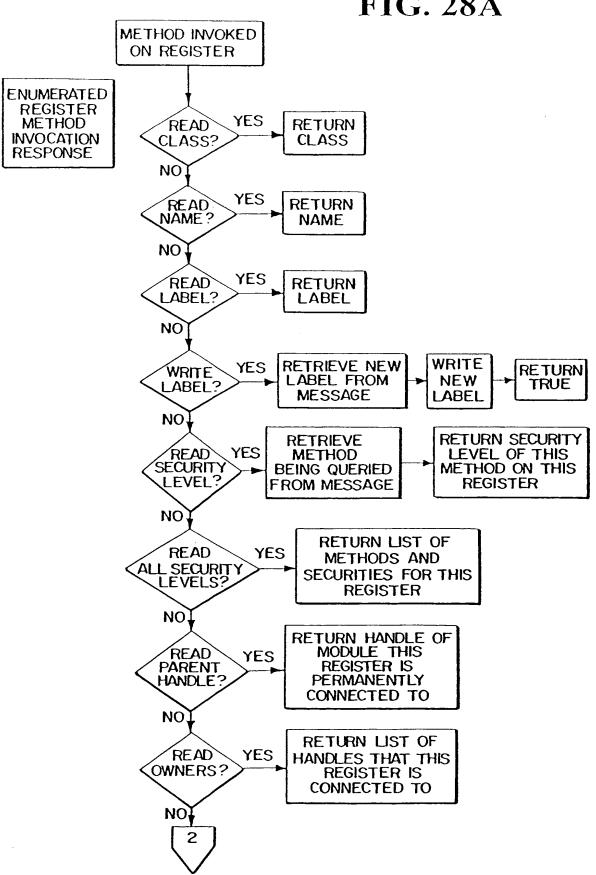


FIG. 28B

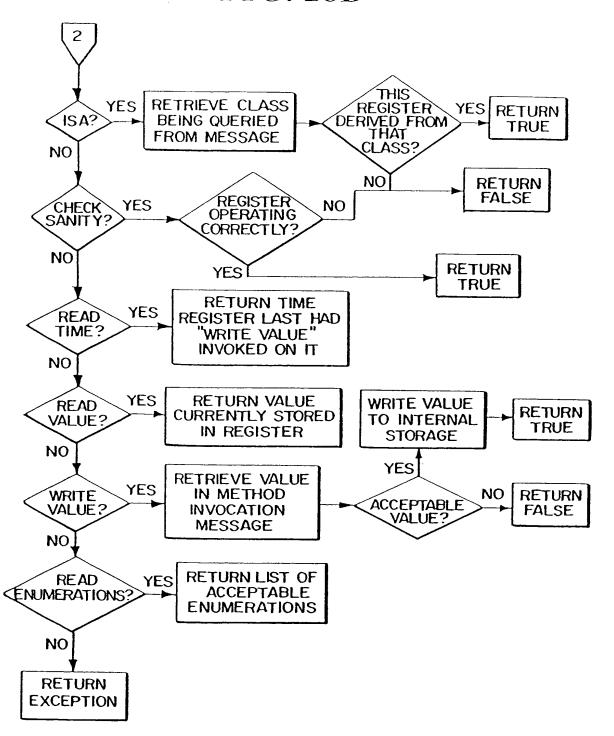


FIG. 29A

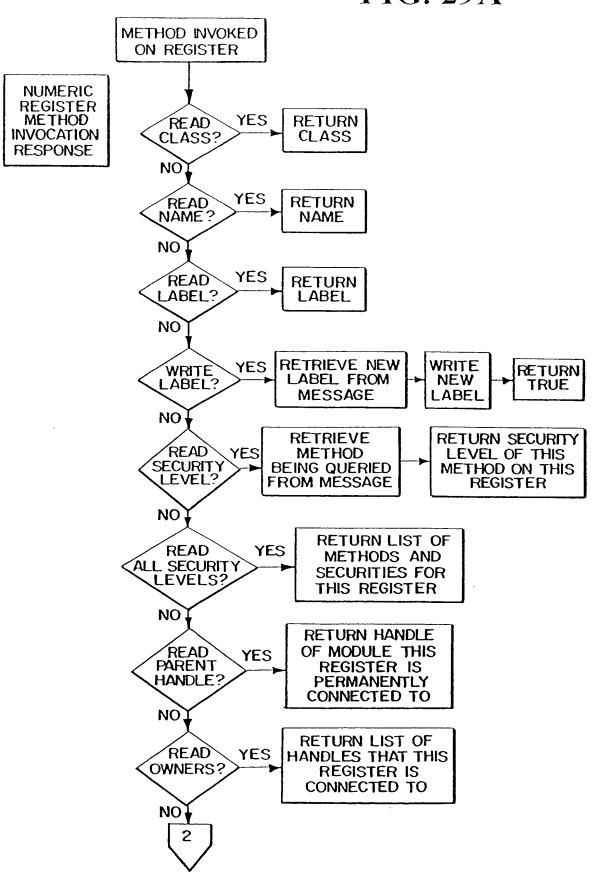
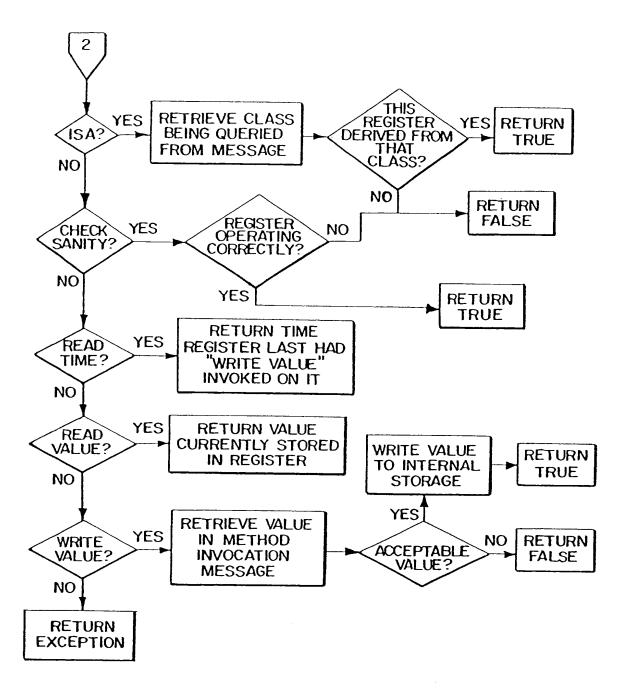
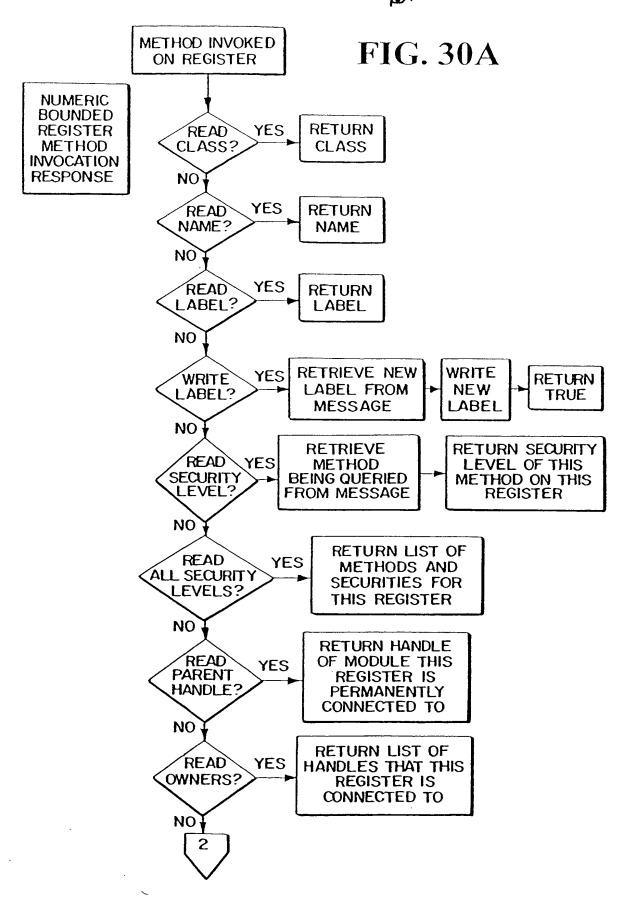


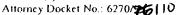
FIG. 29B

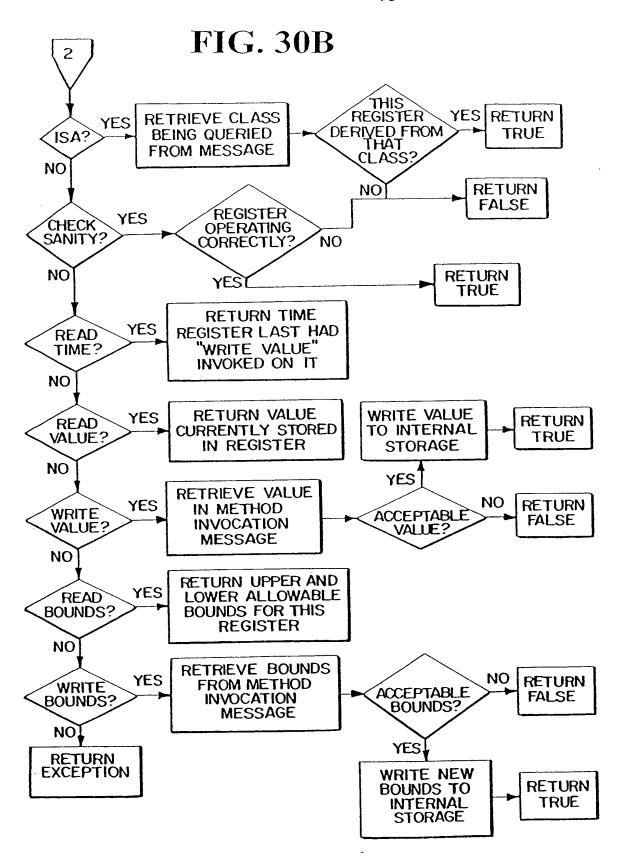




Inventor(s): Ronald G. Hart

63 9207



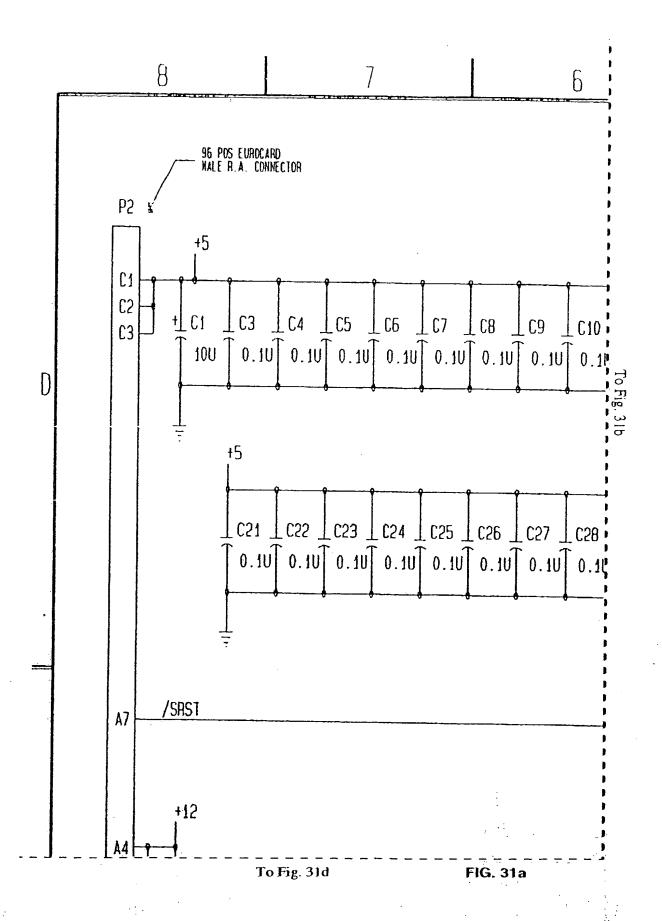


Inventor(s): Ronald G. Hart

64 9 007

Attorney Docket No. 6270/76, Serial No. 10/088,431

4270/110 10/613,701



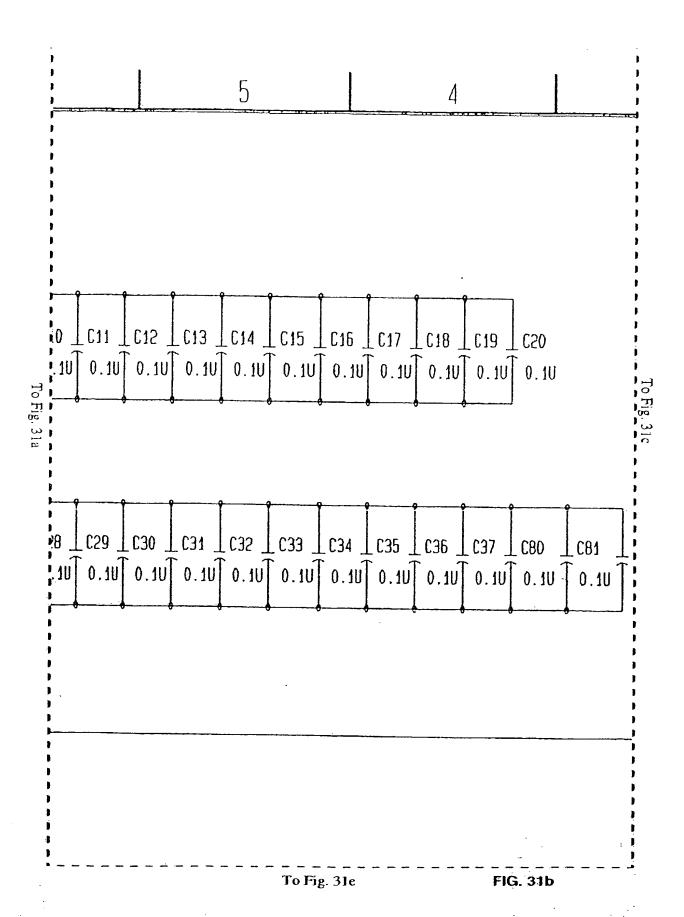
Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS 65 9 207

Inventor(s): Ronald G. Hart

Altorney Docket No. 6270/76, Serial No. 10/068,431

6270/110

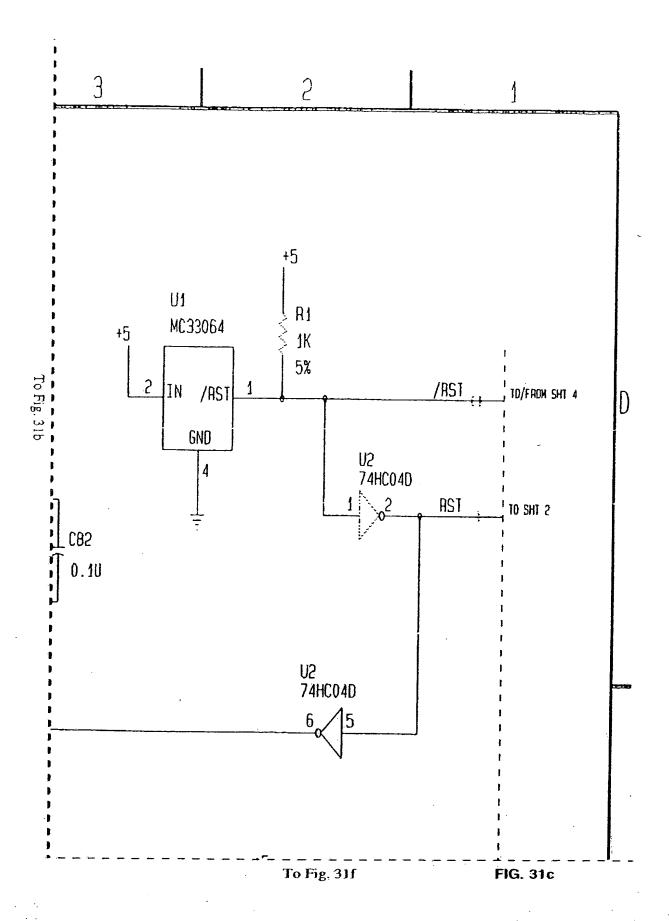
10/613:701



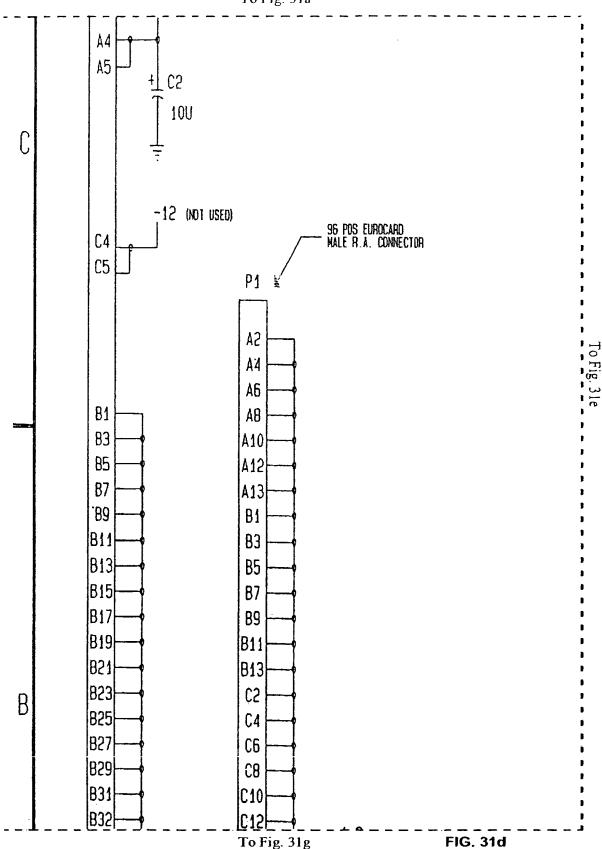
Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS Inventor(s): Ronald G. Hart

Attorney Docket No. 6270/76, Serial No. 10/068,431

10/613.701 6270/110



To Fig. 31a



Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS Inventor(s): Ronald G. Hart 68 7 3.87

Inventor(s): Ronald G. Hart

Altorney Docket No. 627076, Serial No. 10/068,431

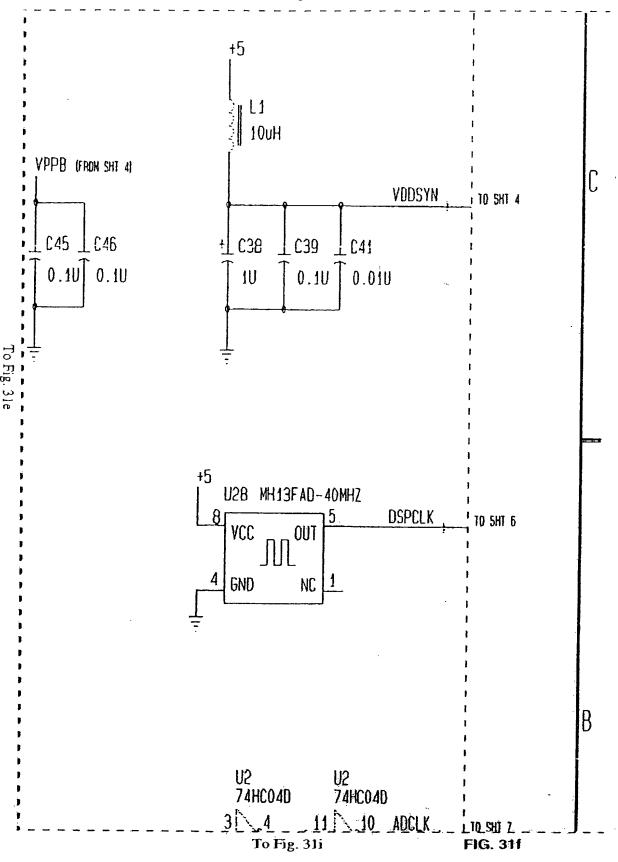
To Fig. 31b

To Fig. 31h FIG. 31e

Inventor(s): Ronald G. Hart
Attorney Docket No. 6270776, Serial No. 10/068,431

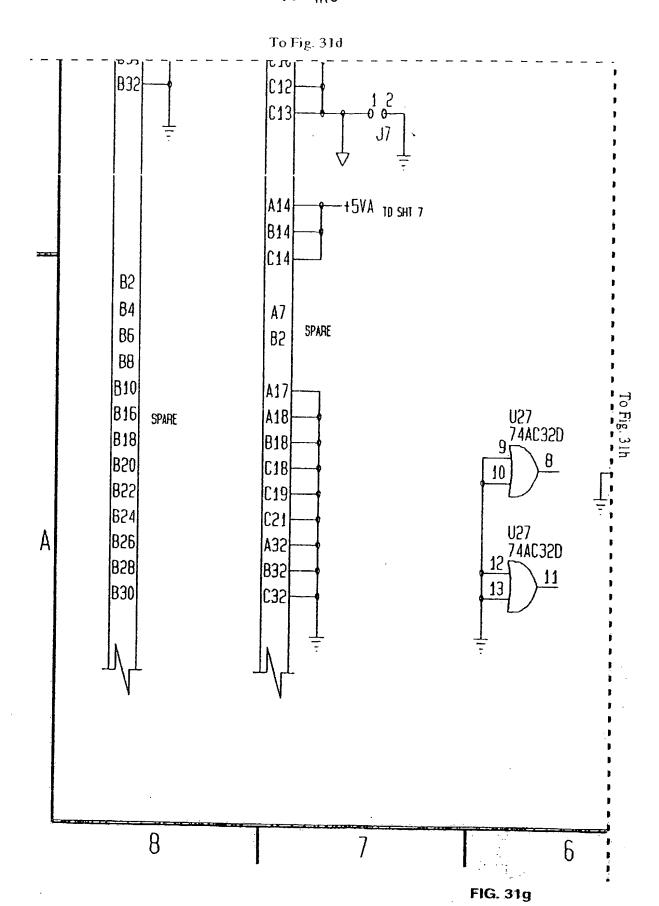
69 9 207





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AGEMENT OF ELECTRICITY DISTRIBUTION
Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76; Serial No. 10/068,431

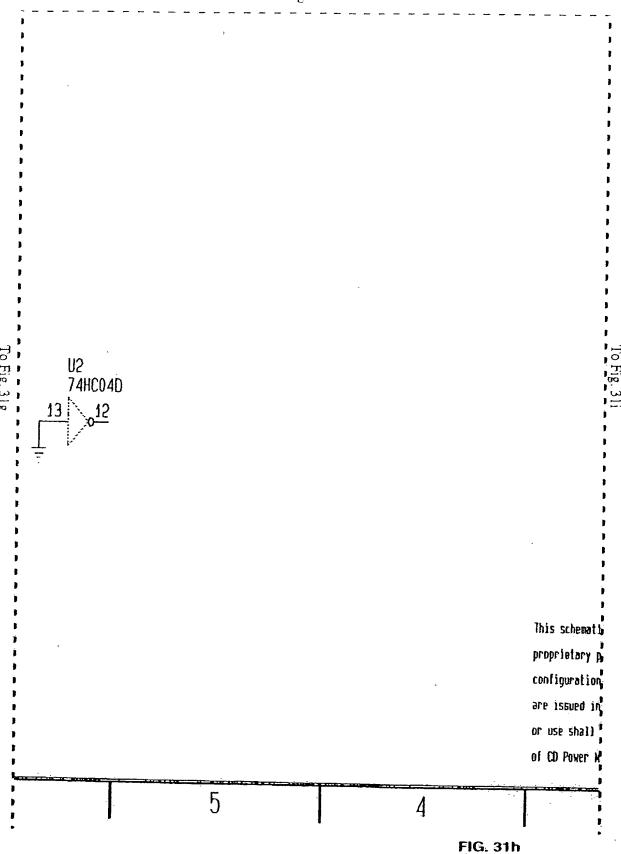


Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS
Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76, Serial No. 10/068,431

6270/110

10/613,701





MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS

Inventor(s): Ronald G. Hart Attorney Docket No. 6270/76, Serial No. 10/968,431

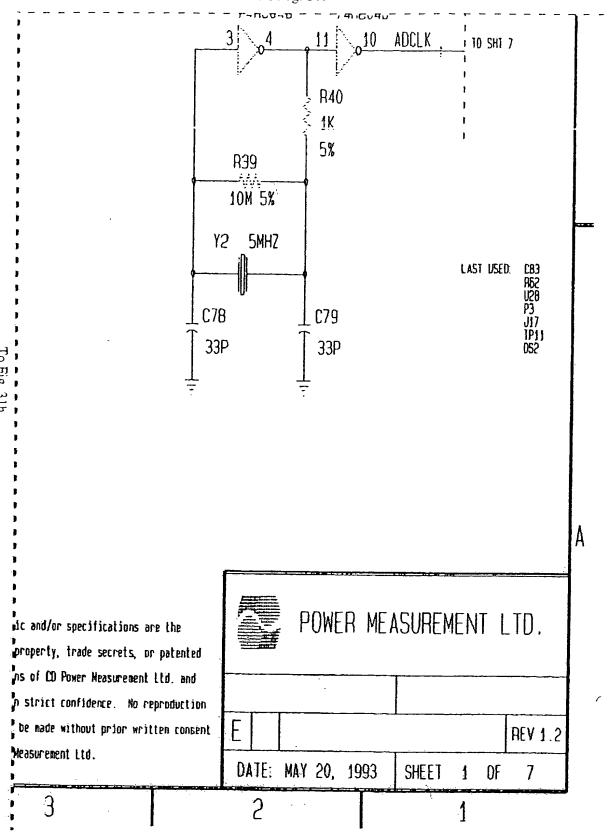
10/613,701

72 900

FIG. 31i

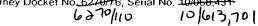
To Fig. 31f

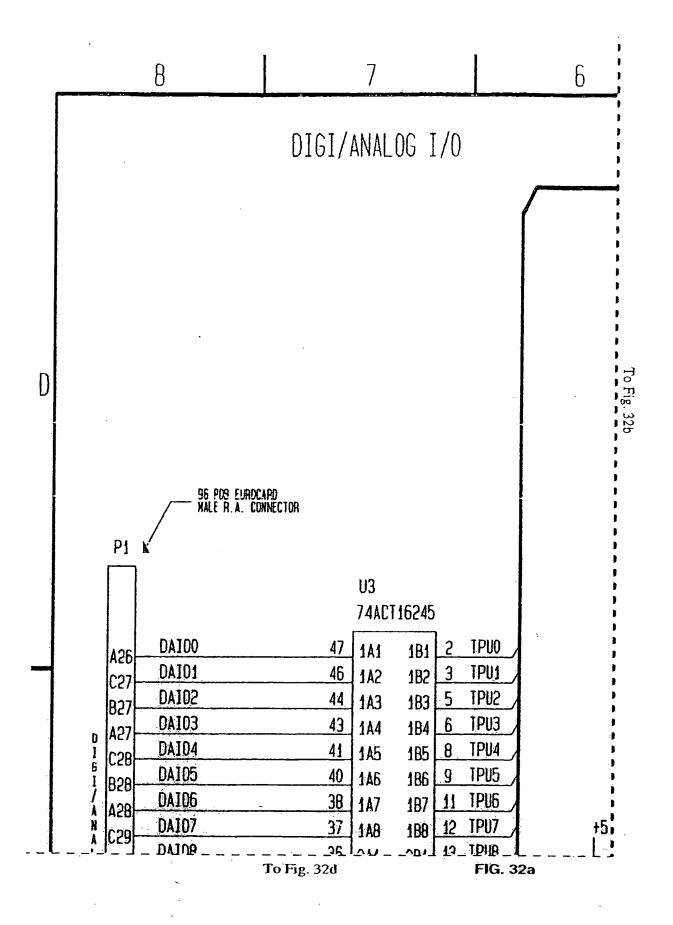
6270/110



Inventor(s): Ronald G. Hart

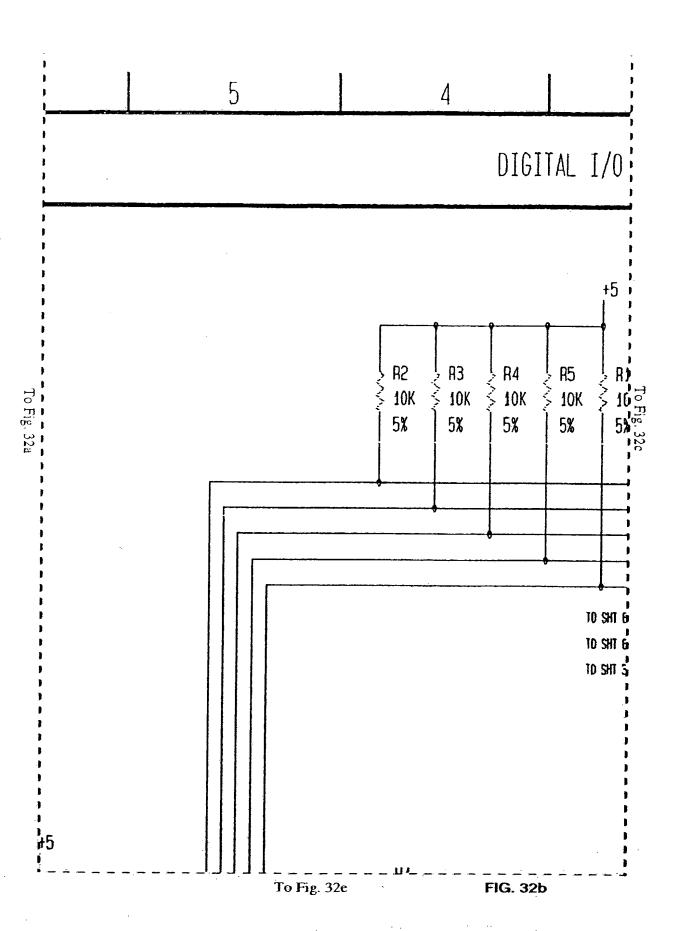
Attorney Docket No. 6279/76, Serial No. 10/068,431-



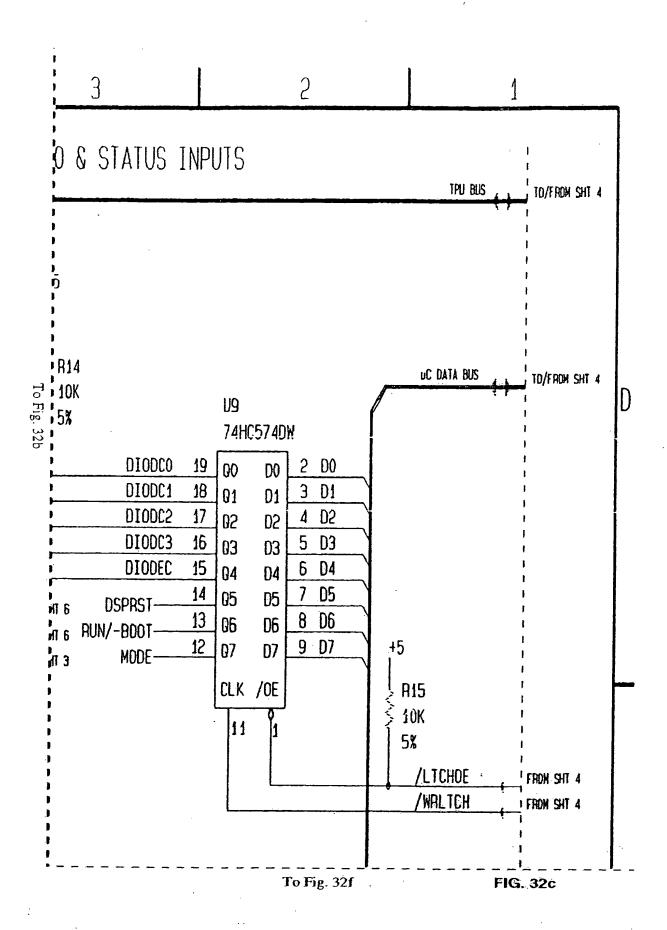


Inventor(s): Ronald G. Hart

Attorney Docket No. -627076, Serial No. 10/068,431



Attorney Docket No. 627076, Serial No. 10/068,431



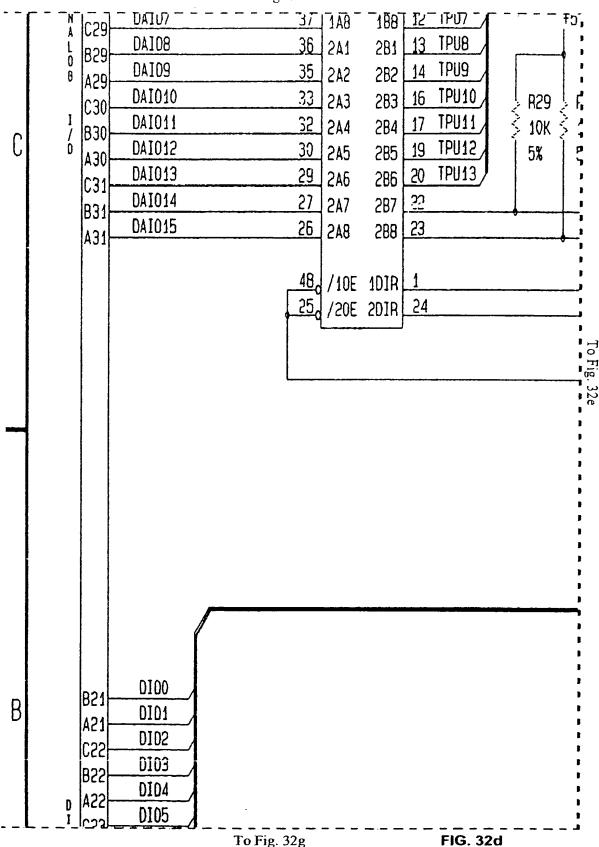
MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS

76 97207

Inventor(s): Ronald G. Hart Altorney Docket No. 6270/76, Serial No. 10/068 431 6270110

10/6/3701

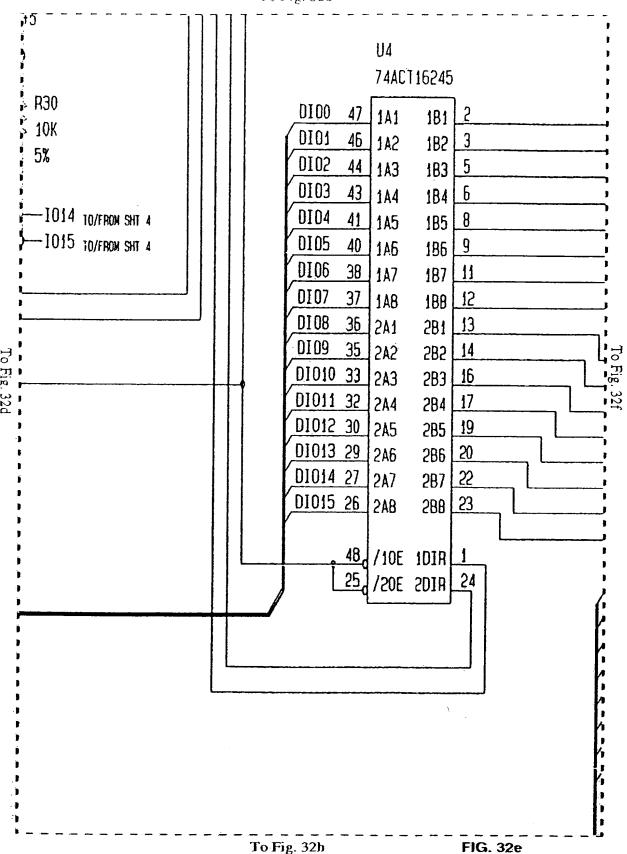
To Fig. 32a



Inventor(s): Ronald G. Hart

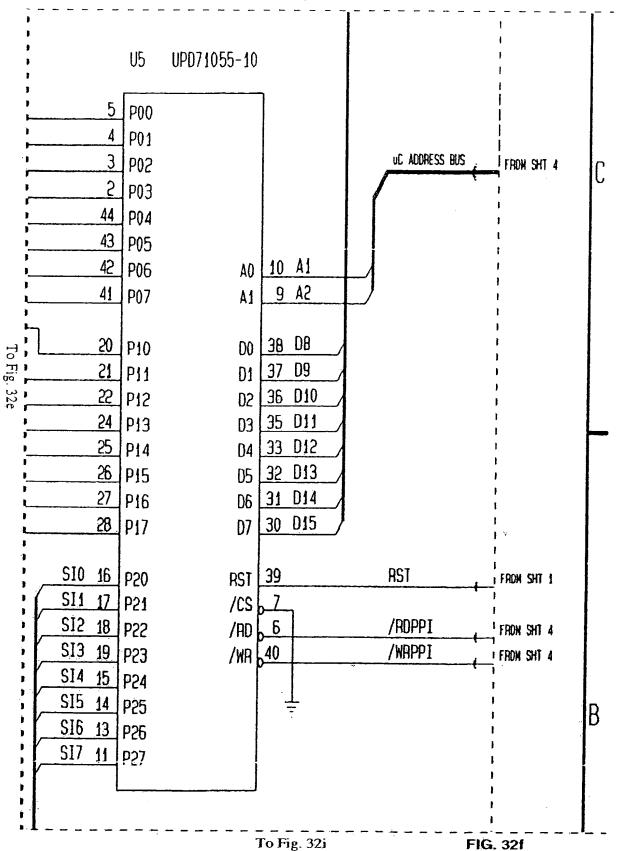
Attorney Docket No. 6270/76, Serial No. 10/068,431

To Fig. 32b



Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76, Serial No. 40/068,431
6370/10 10 6/3,701

To Fig. 32c

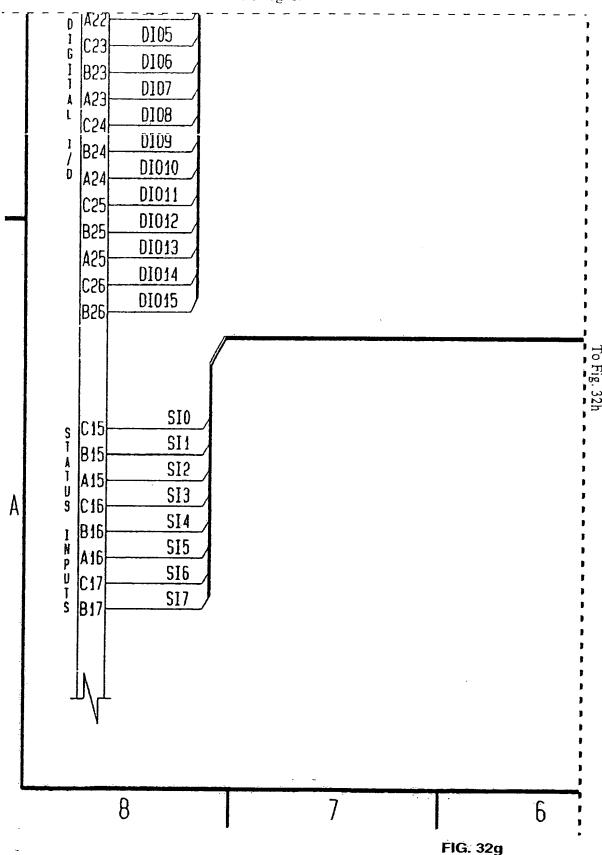


Inventor(s): Ronald G. Hart

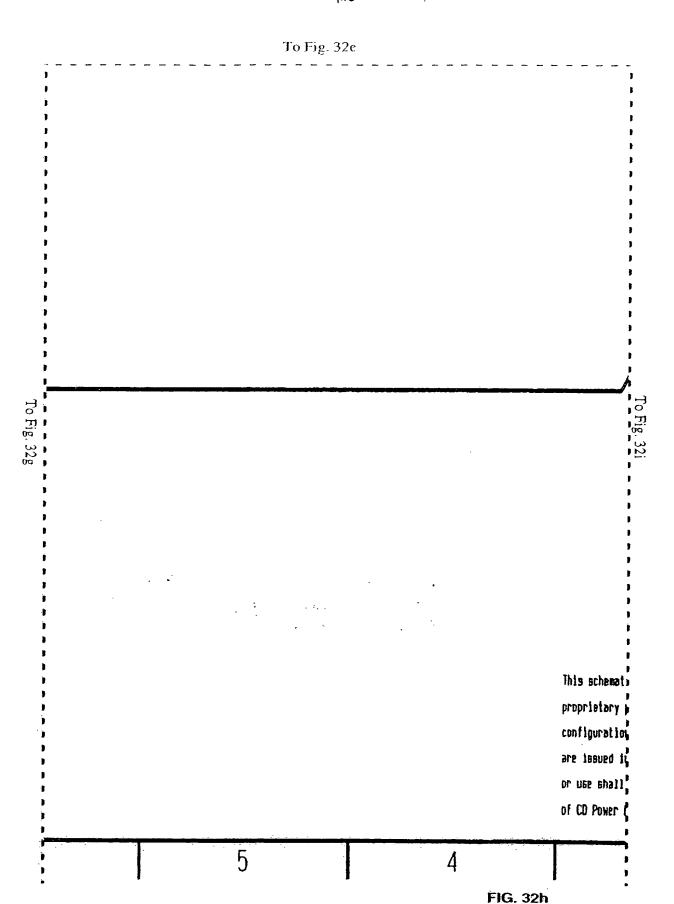
Attorney Docket No. 6270/76, Serial No. 10/068,431

62-70/(10 (0/6/3,70/

To Fig. 32d



Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS
Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76, Serial No. 40/068,431

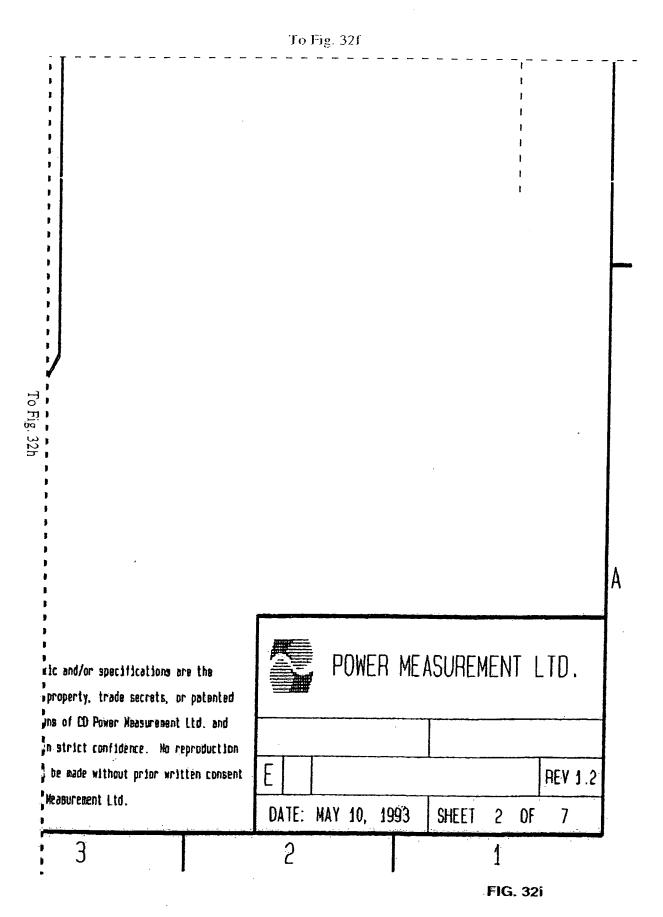


Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS

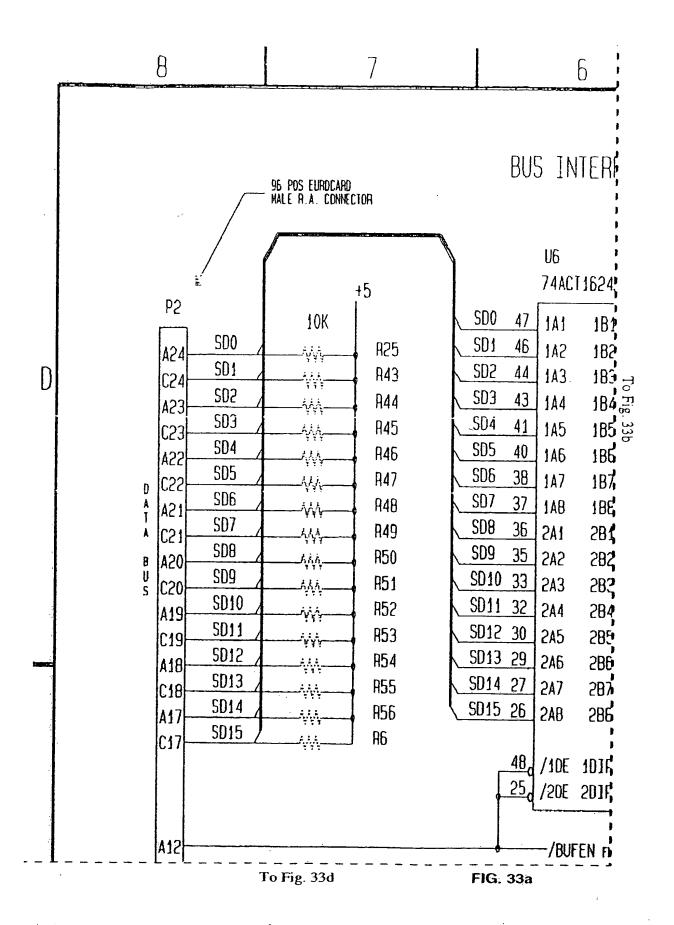
Inventor(s): Ronald G. Hart Attorney Docket No. 6270/76, Serial No. 10/068,437

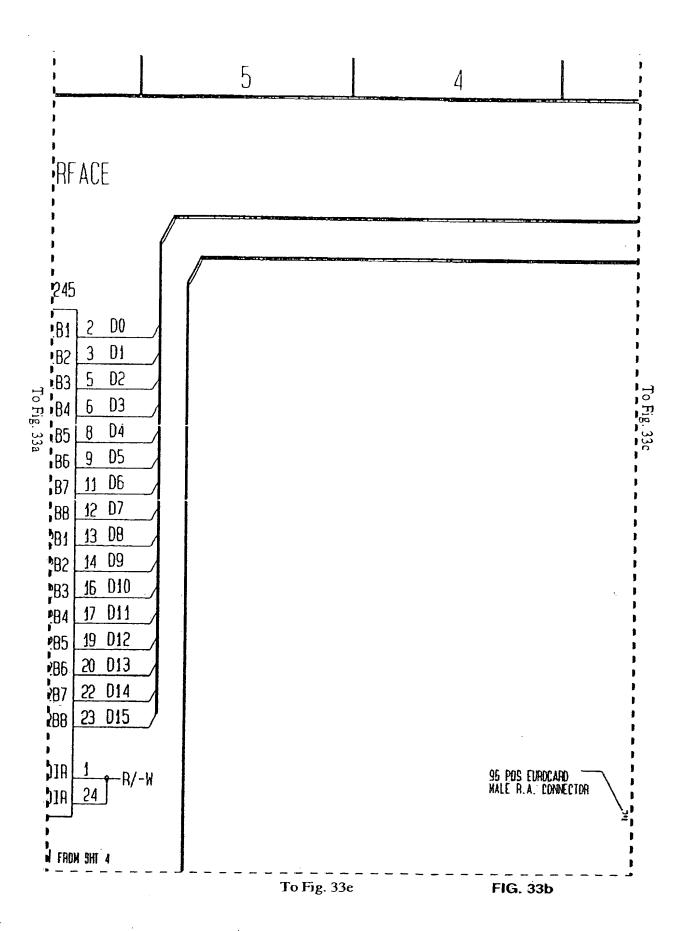
81 8707

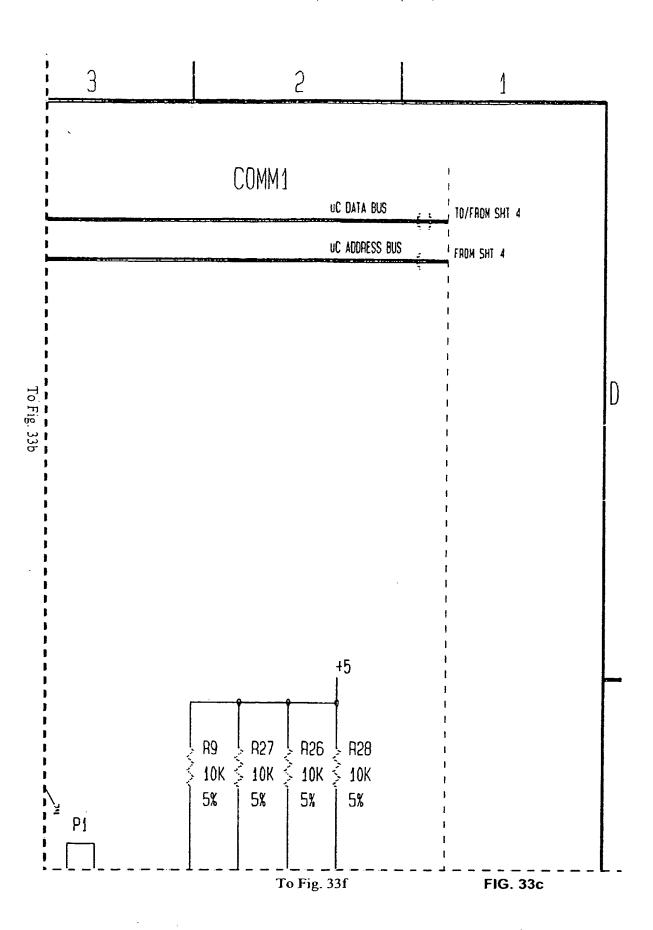
6270/110



Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76, Serial No. 10/068,431

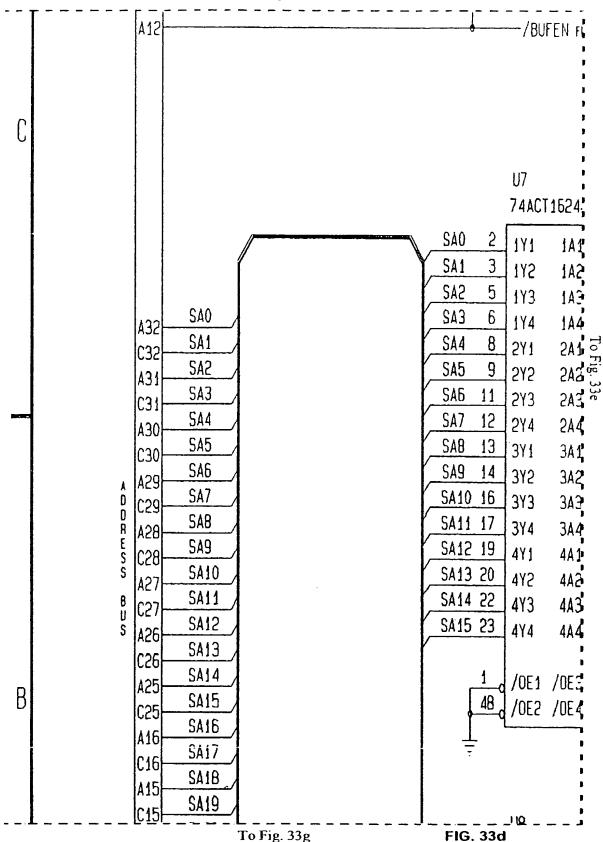




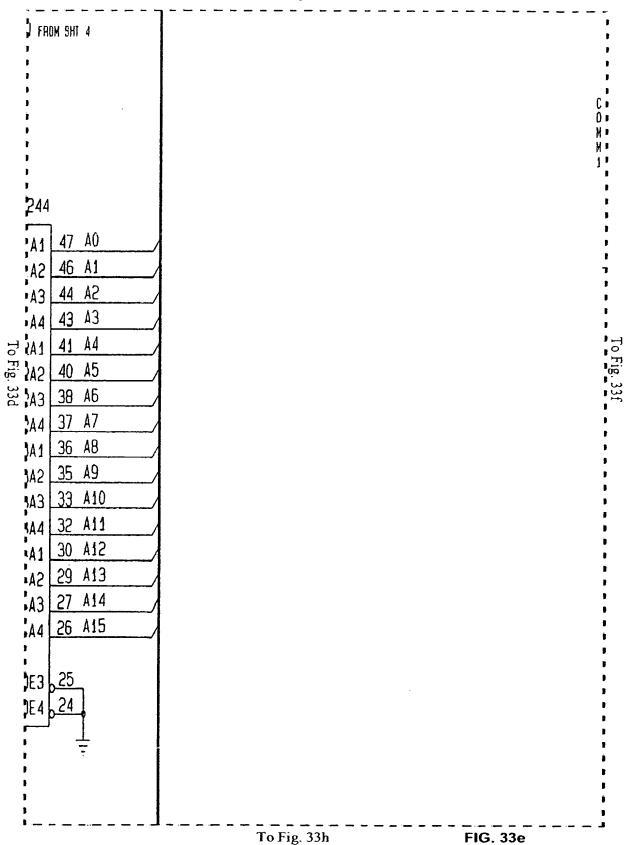


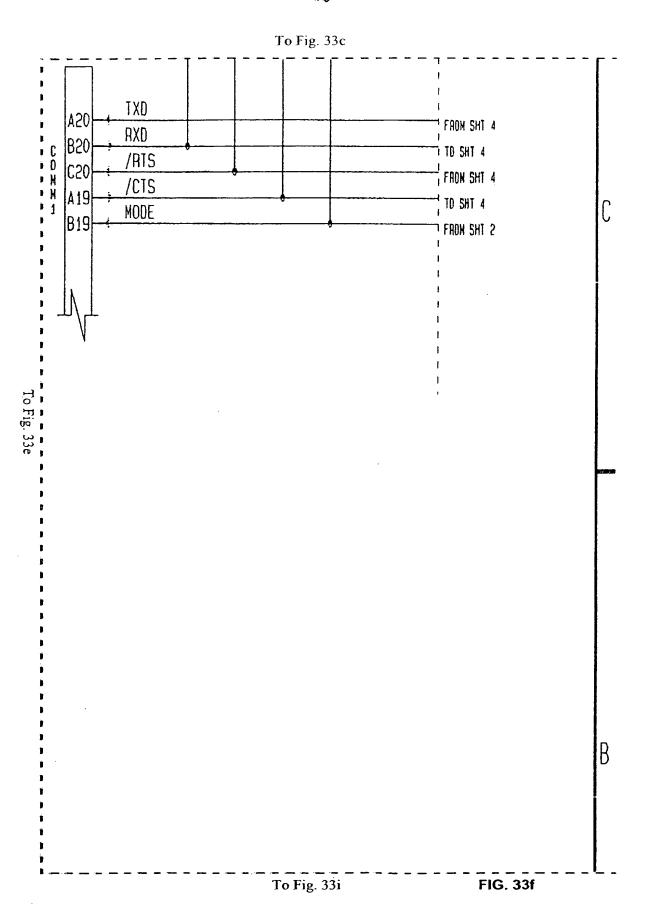
Attorney Docket No. 6270/76, Serial No. 10/068,431
6319(10 (0/6/3,70)





To Fig. 33b





Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76, Serial No. 10/068,437
6370/10 10/613,70/

To Fig. 33d

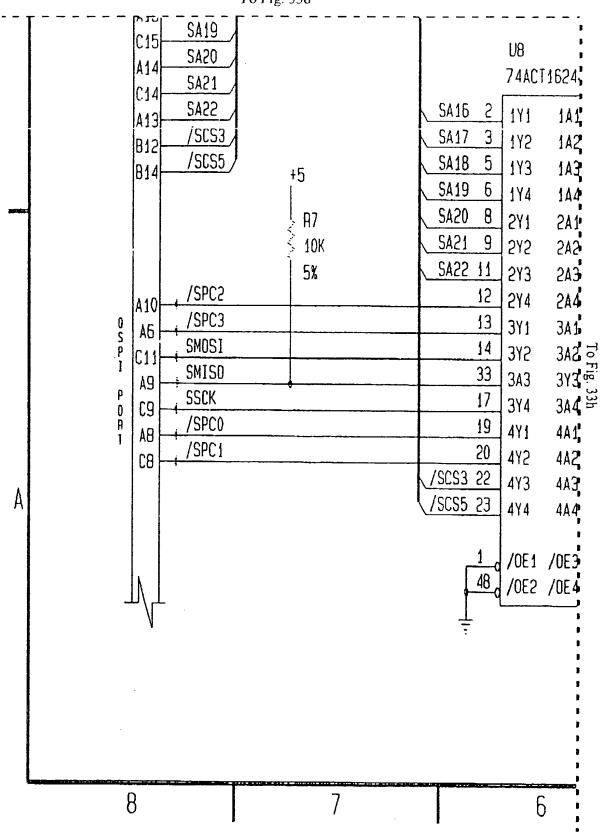


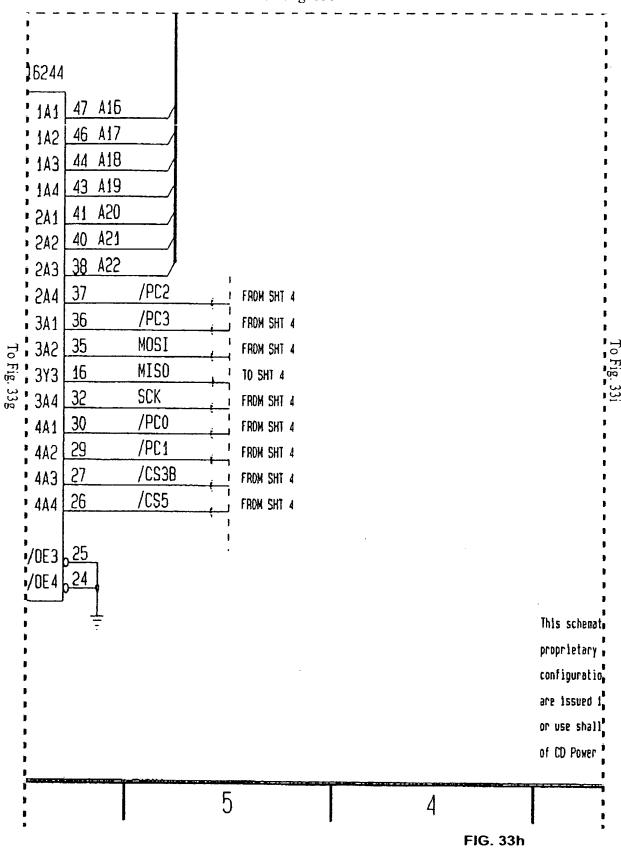
FIG. 33g

Inventor(s): Ronald G. Hart

Attorney Docket No. 6279/76, Serial No. 10/068,431

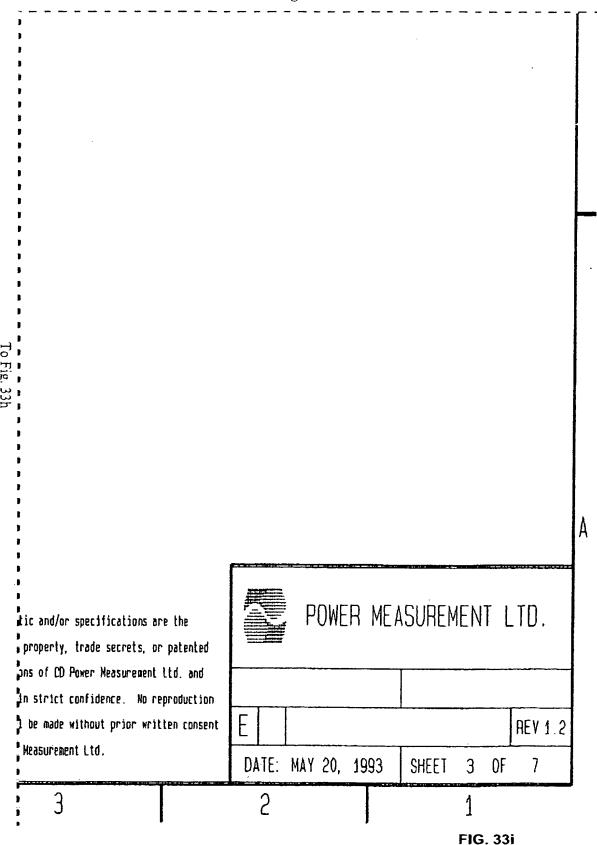
6220/110

To Fig. 33e

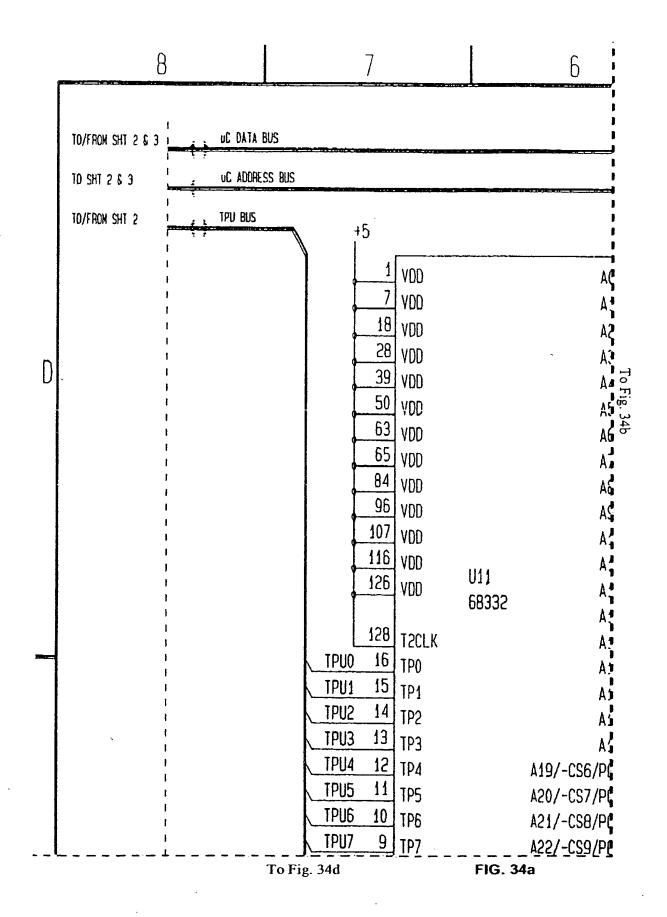


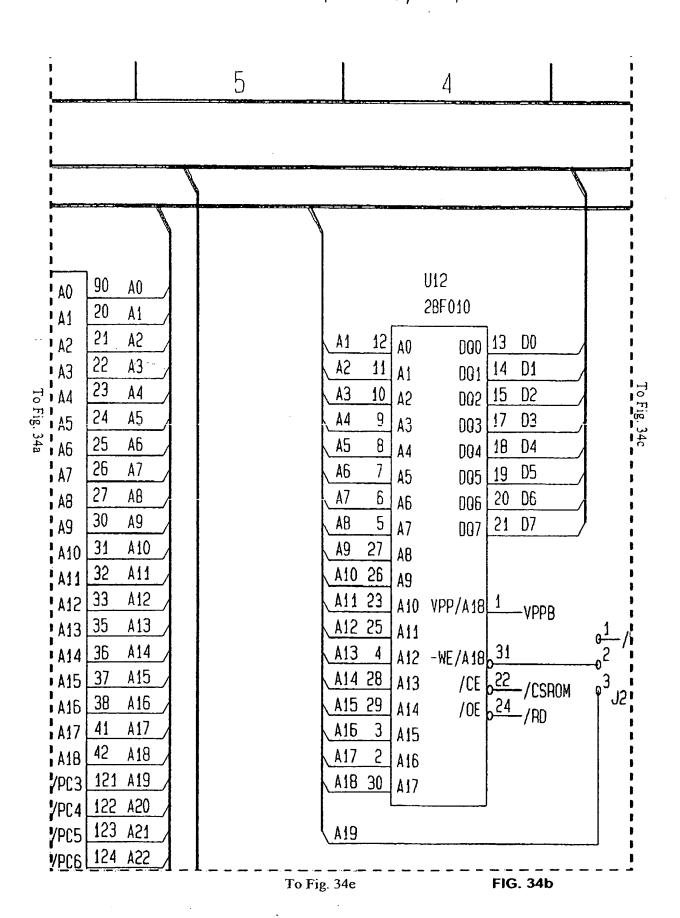
Inventor(s): Ronald G. Hart Attorney Docket No. 0270775, Serial No. 40/068,431 6 みつにひ (0/6(う,つ)

To Fig. 33f

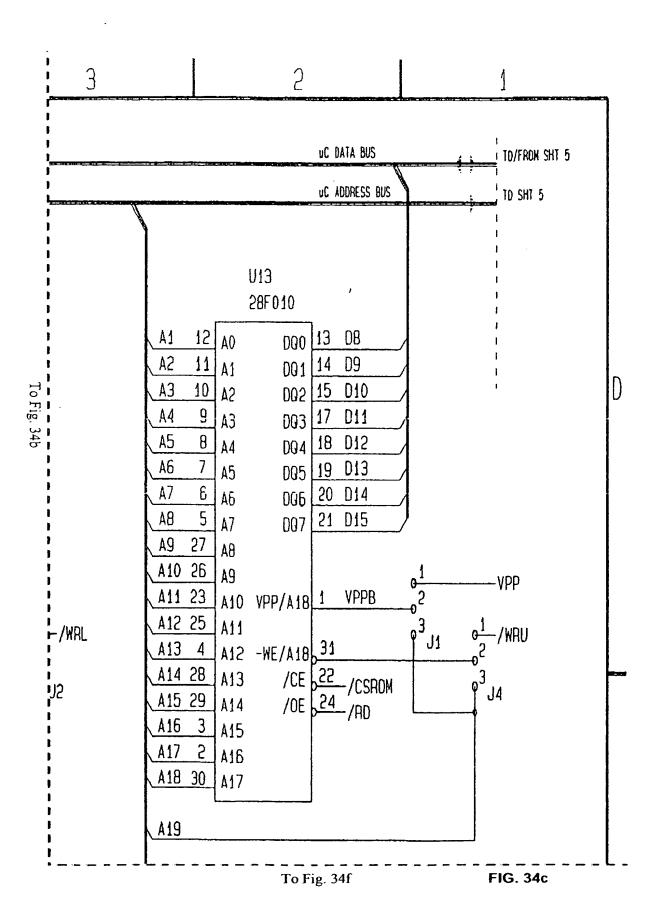


Attorney Docket No. 6270/78, Serial No. 10/068,431-6270/110 /0/613,701





Inventor(s): Ronald G. Hart Attorney Docket No. 6270/76, Serial No. 10/068,431 62-10/10 10/613,70/



AGEMENT OF ELECTRICITY DISTRIBUTION OF Inventor(s): Ronald G. Hart

Attorney Docket No. 6270/76, Serial No. 10/068,431

6370(10 10/6/3,701

To Fig. 34a

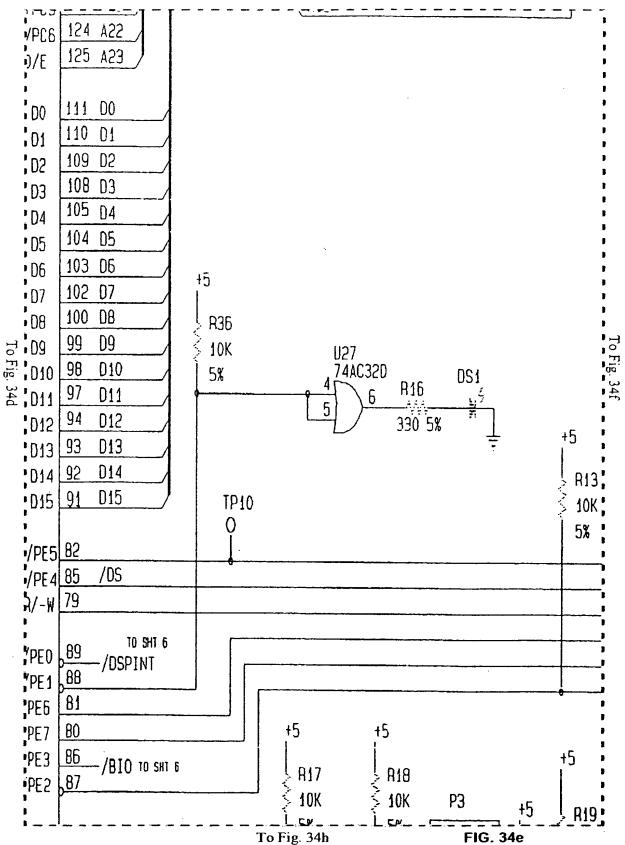
10 F1g. 34a					
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			45 I ~~~~	-1 '' 0	A23/-CS10/h
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		1	-		D ę
	TO/FROM SHT 2	1014		7 - IR01/PF1	Dγ
		1015 1015	7	-\(\frac{1}{2}\)	Dē
	FROM SHT 3	/CTS	7!	-v	02 를
	TO SHT 2	/LTCHOE	7.		DS To Fig. 3.
		! !	/EXINT	-IRO5/PF5	£ 10
	FROM SHT 6	XF (278		-IRO6/PF6	D\$
	TO SHT 3	RTS /	7:	-IRO7/PF7	D4
		1	_		Dţ
	·) 	/BERR	2d -BERA	D?
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	TO/FADM SHT 1	' /RST	61	-RESET	-AS/P
		!]			-DS/P
		¦ '/AST			R/
В		l pvn	_		,
	FROM SHT 3	RXD	5	71770	-DSACKO/PE
	TO SHT 3	I TXD	5		-DSACK1/PE
	to sht 3	SCK	4!	SCK/PD2	SIZO/PE
	FROM SHT 3	MISO	4:	MISD/PDO	SIZ1/PE
	TO SHT 3	MOSI	4	7	-AMC/PE
•	to sht 3	/PCO	4	7, 1, 2, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 1, 2, 2, 1, 2, 2, 2, 2, 2, 2, 2, 2, 2, 2, 2, 2, 2,	-AVEC/PE
_	to sht 3	/PC1	4	PCS1/PD4	, ,
					

To Fig. 34g

FIG. 34d

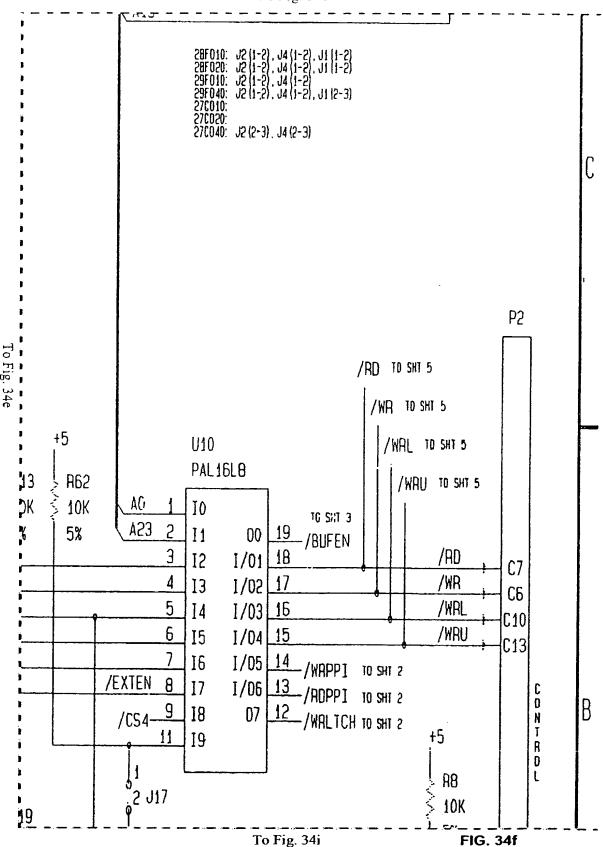
Inventor(s): Ronald G. Hart
Attorney Docket No. 6270776, Serial No. 10/068,431
(62-70/10 10/0613,70/





Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76, Serial No. 10/613,70 /

To Fig. 34c



Inventor(s): Ronald G. Hart

Attorney Docket No. 6270/76, Serial No. 10/068,431 6270/10

10/613,701

To Fig. 34d

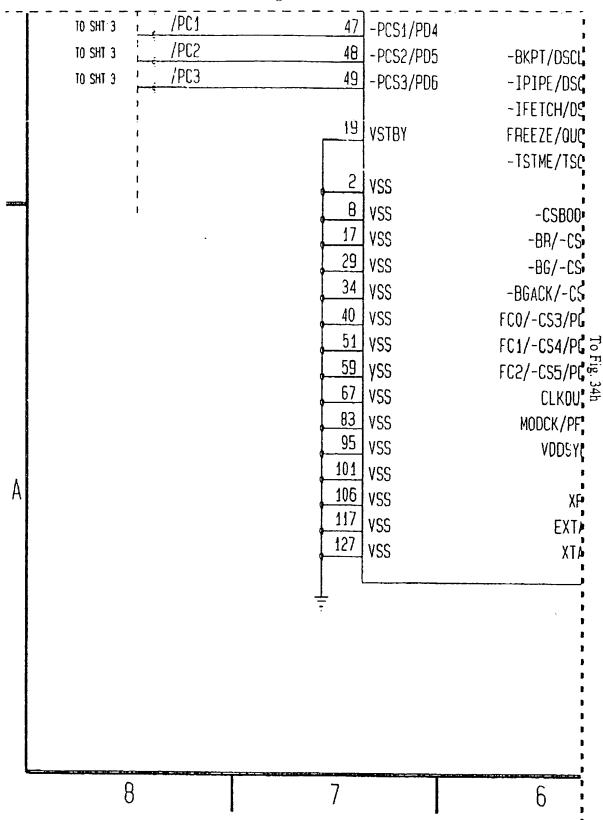
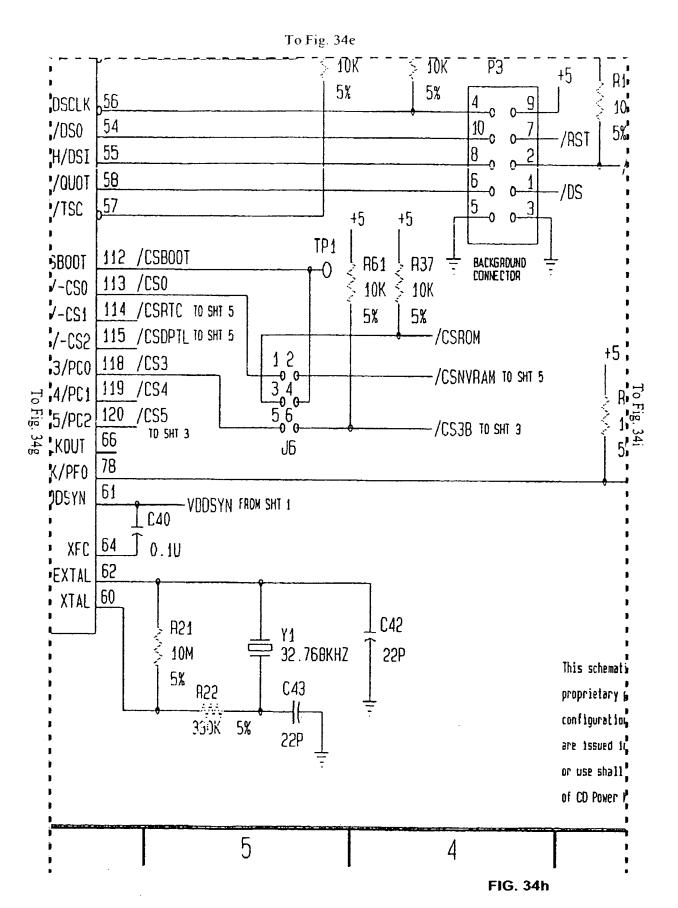
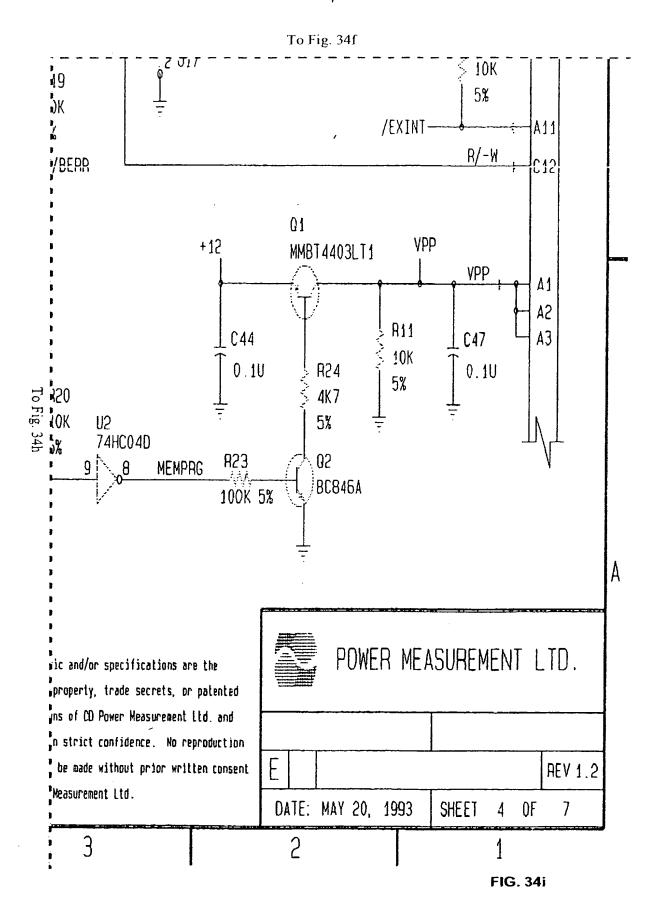


FIG. 34g

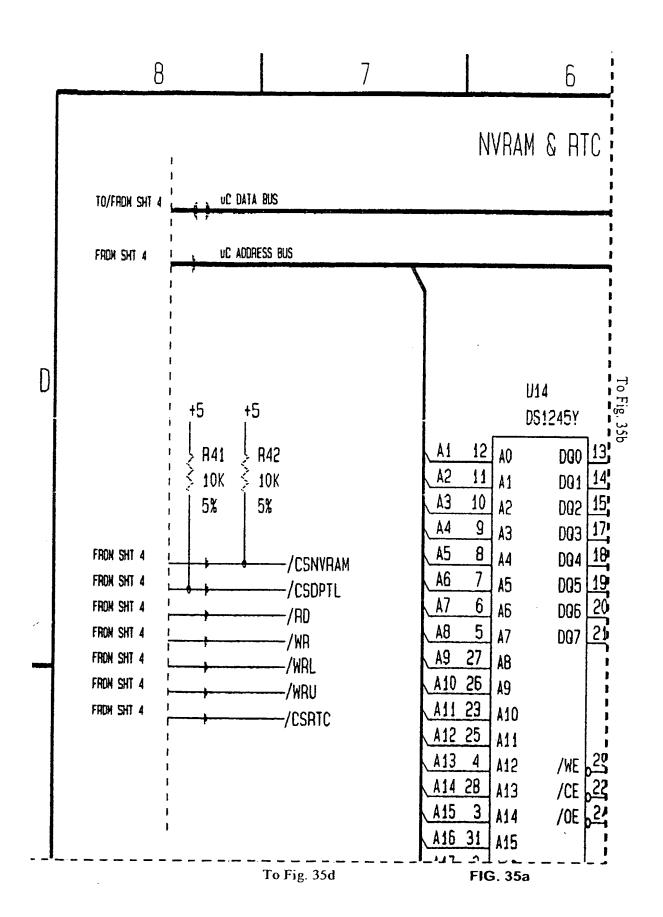
Attorney Docket No. 6270/76, Serial No. 10/060,431 (6) 10/6/3,70



Inventor(s): Ronald G. Hart Attorney Docket No. 6270/76; Serial No. 40/058.431 6270/10 10/6/3,701

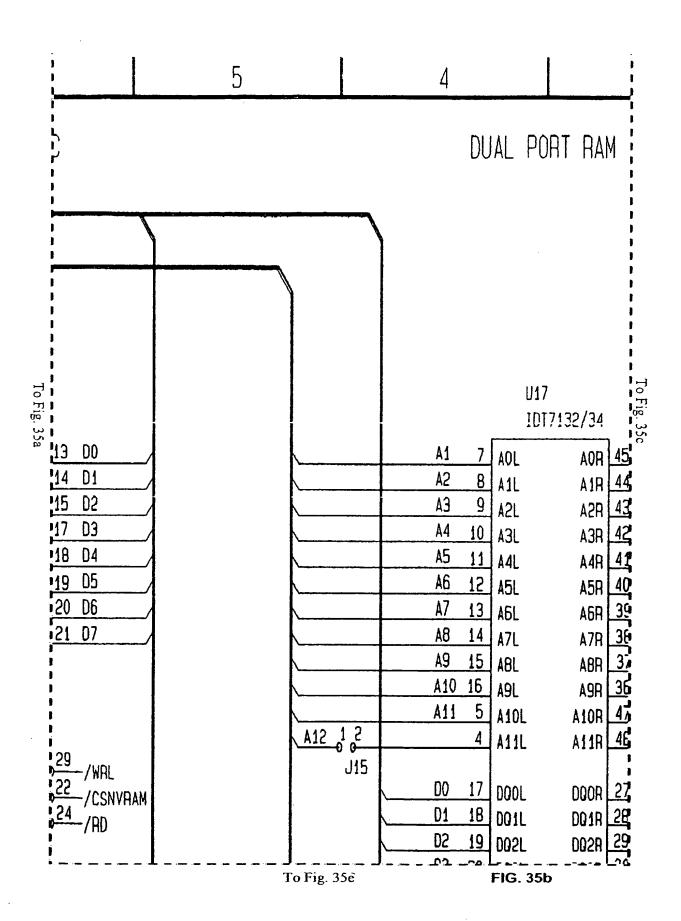


Inventor(s): Ronald G. Hart Attorney Docket No. 6270/76, Serial No. 10/068,431-6270/110 /0/6/3,70 (

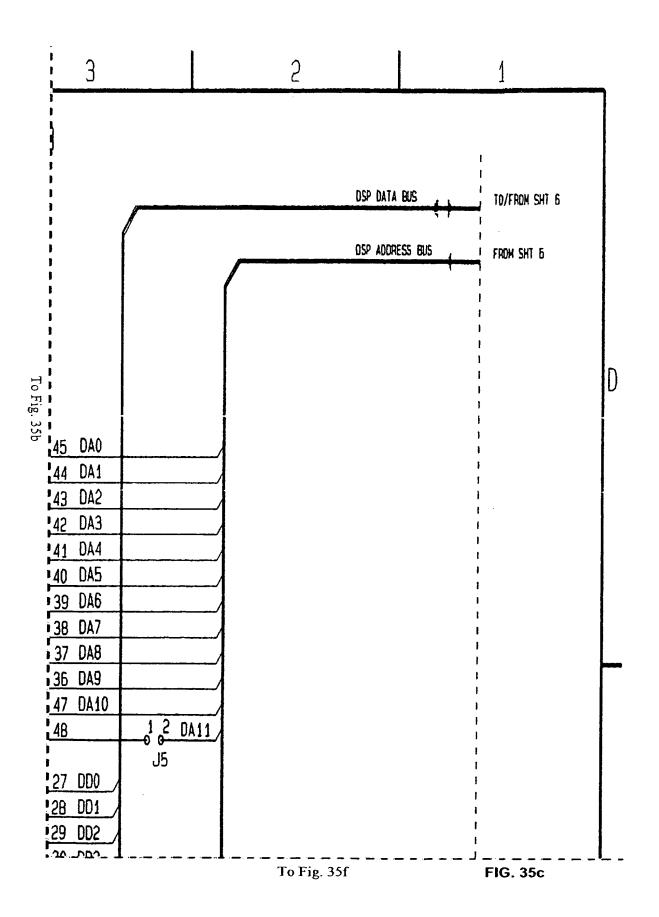


Inventor(s): Ronald G. Hart

Attorney Docket No. 5270/76, Serial No. 10/068,431

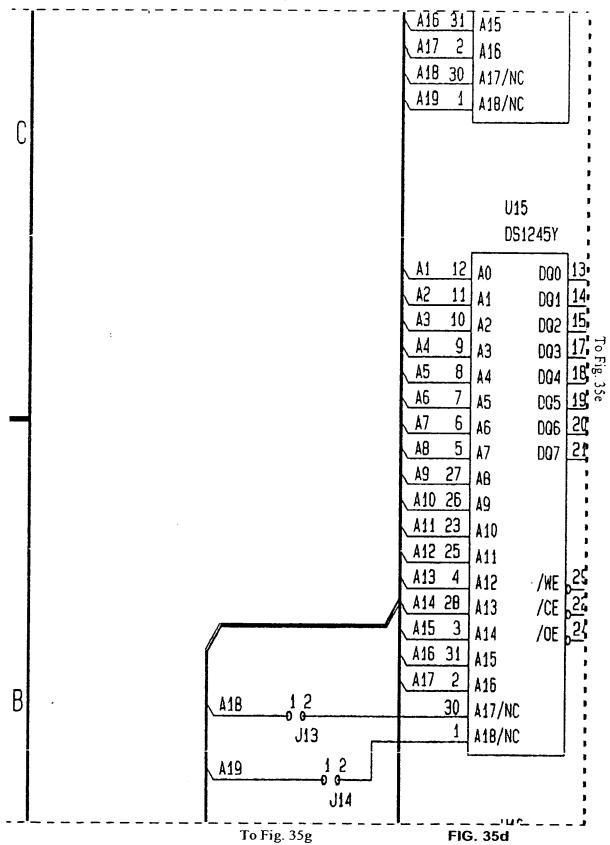


Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76; Serial No. 10/068,431
6 270/11 0 | 0/613,70 |



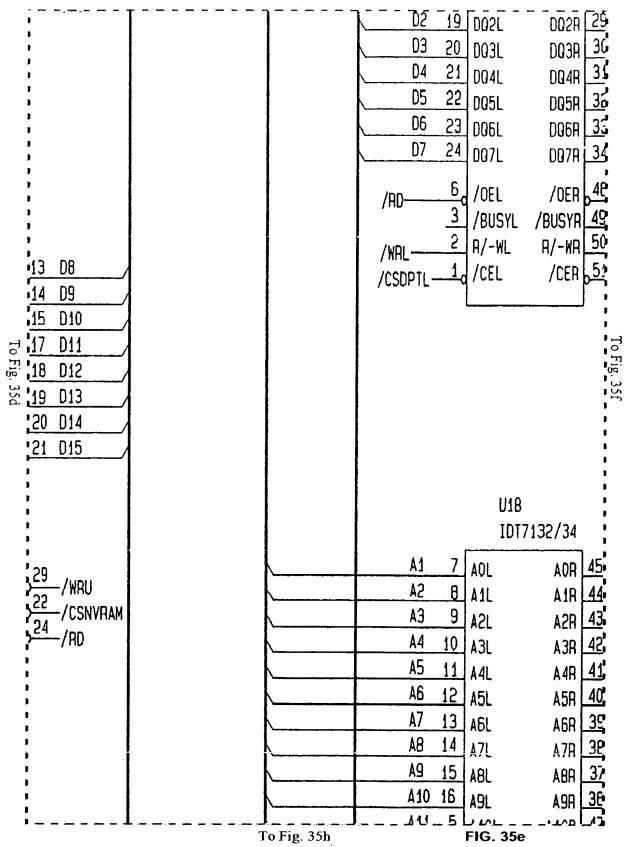
Attorney Docket No. 6279/76, Serial No. 40/008,431

To Fig. 35a



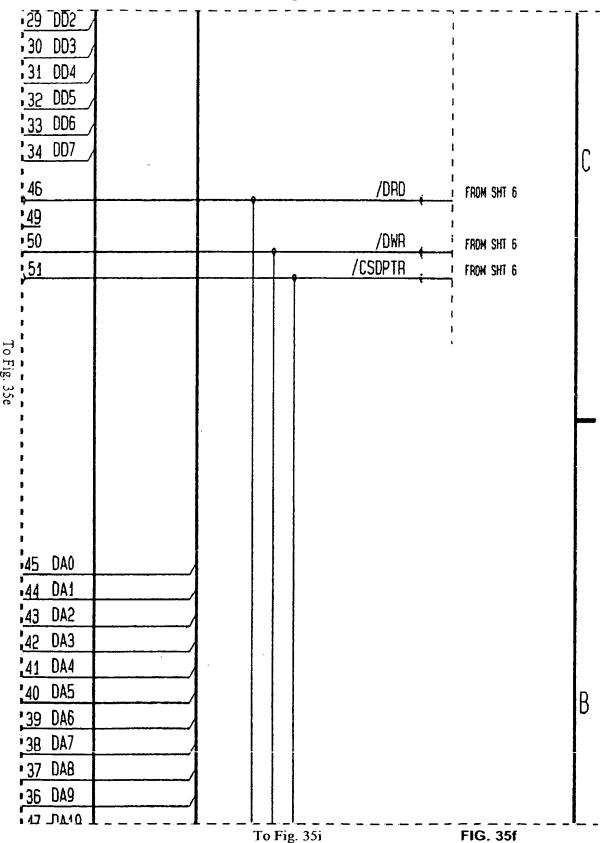
Attorney Docket No. 6270/76, Serial No. 40/068.431 10/613.761

To Fig. 35b

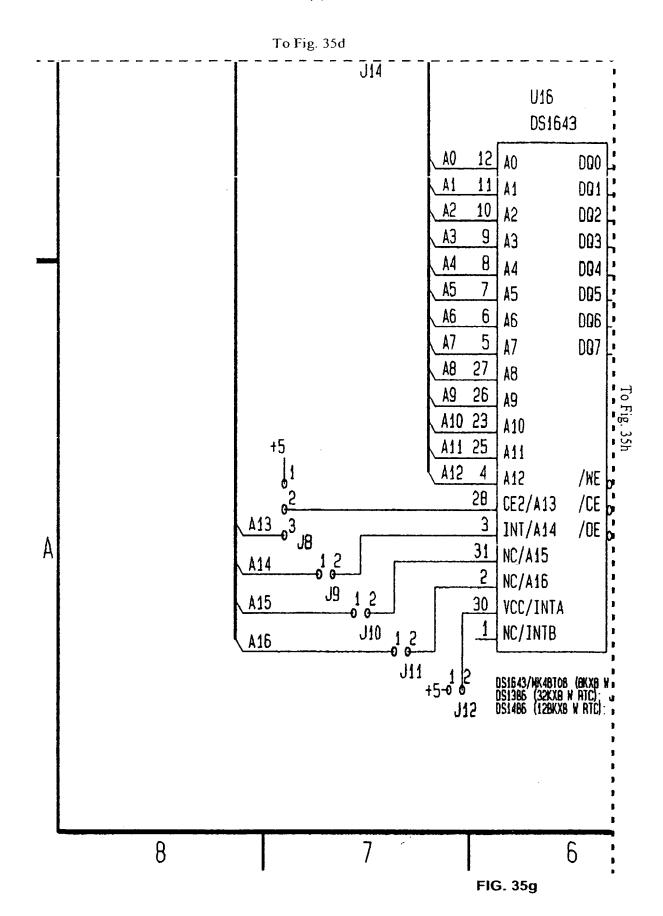


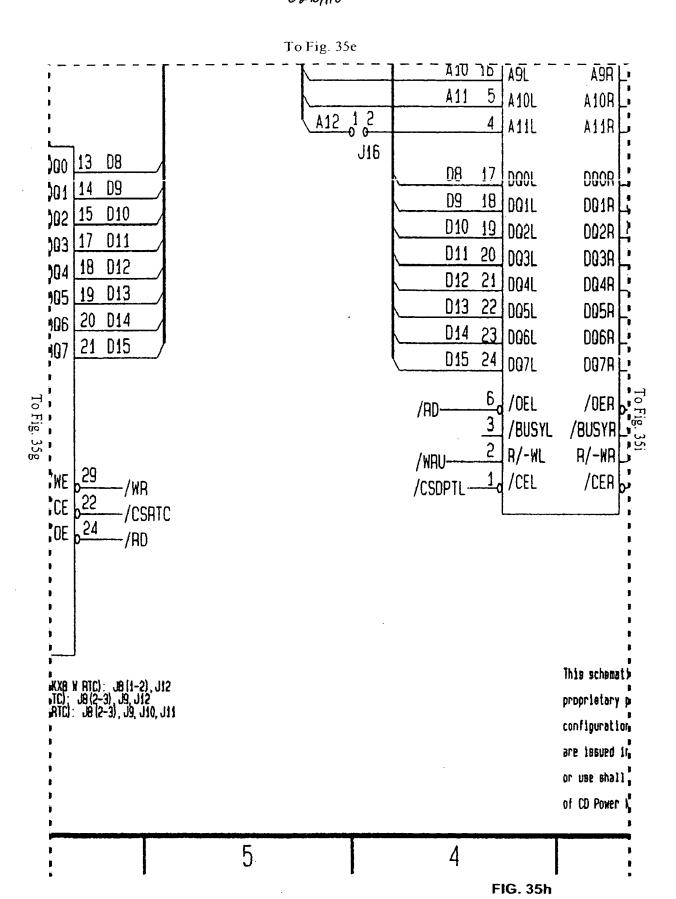
Attorney Docket No. 6270176, Serial No. 40/068,431 しりんりろうり 6370110

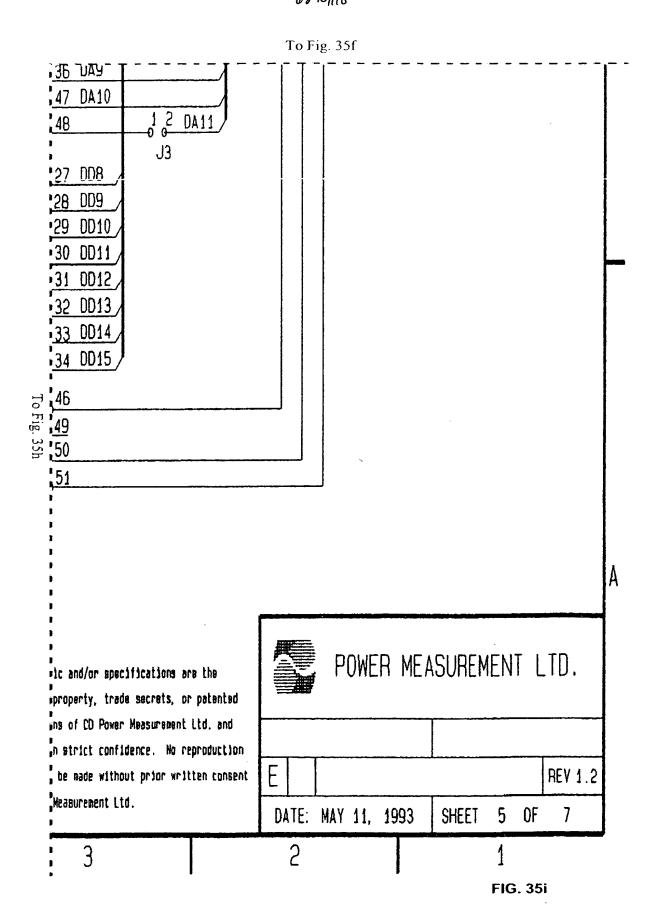


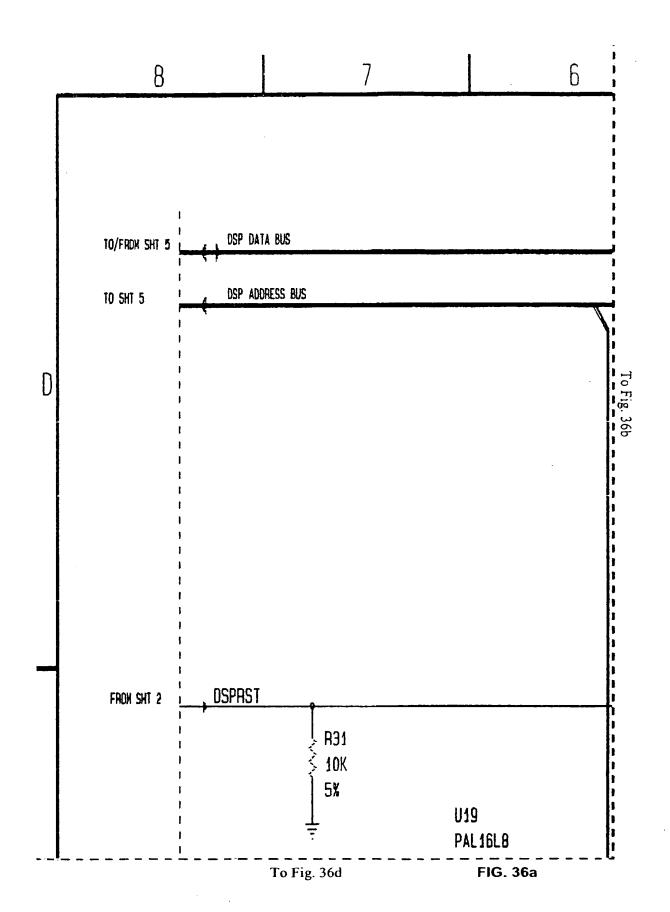


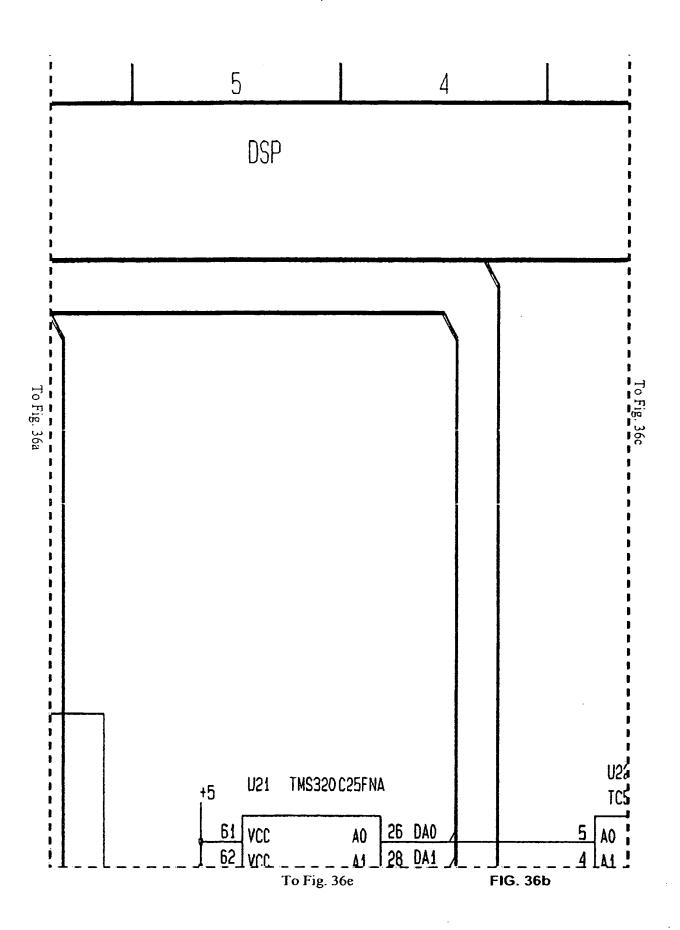
Attorney Docket No. 6270/76, Serial No. 40/068,431 10/61 3,701



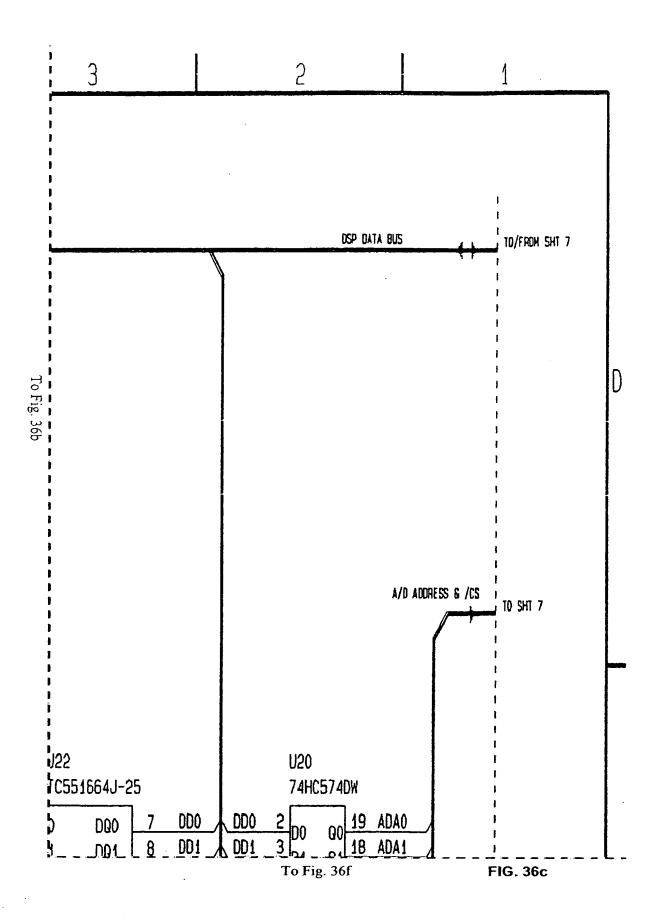








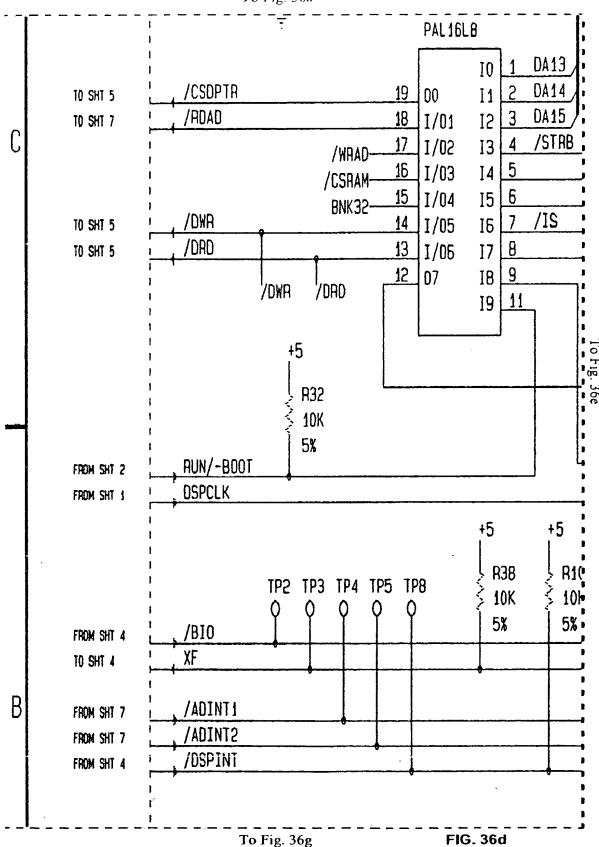
Inventor(s): Ronald G. Hart (!!)
Attorney Docket No. 16/270/70, Serial No. 16/068,431 (0/6/3 70/



Inventor(s): Ronald G. Hart

Attorney Docket No. 6270/76, Serial No. 10/068,431 (0/6/3,70)
6270///0

To Fig. 36a

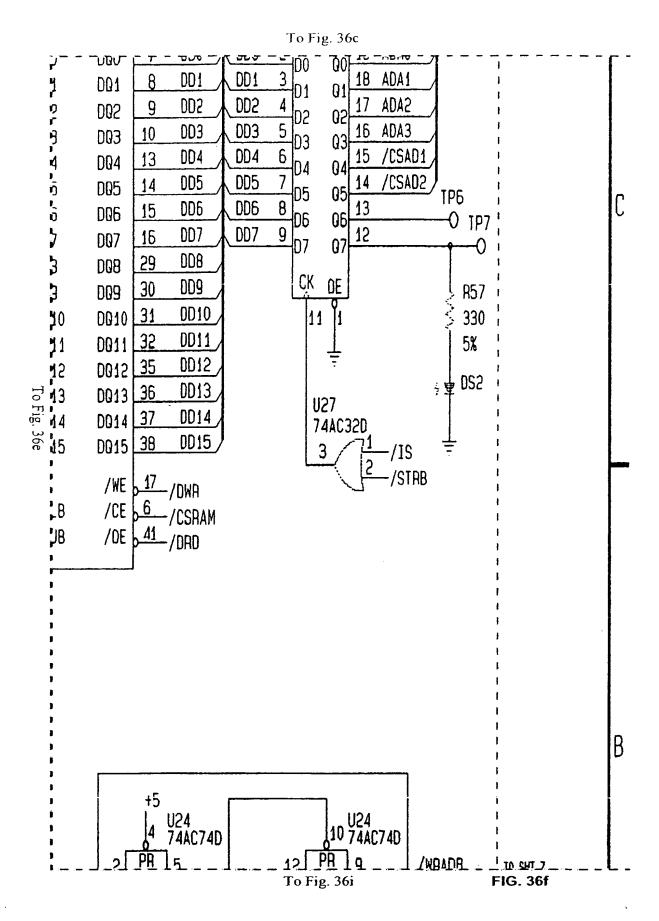


To Fig. 36b

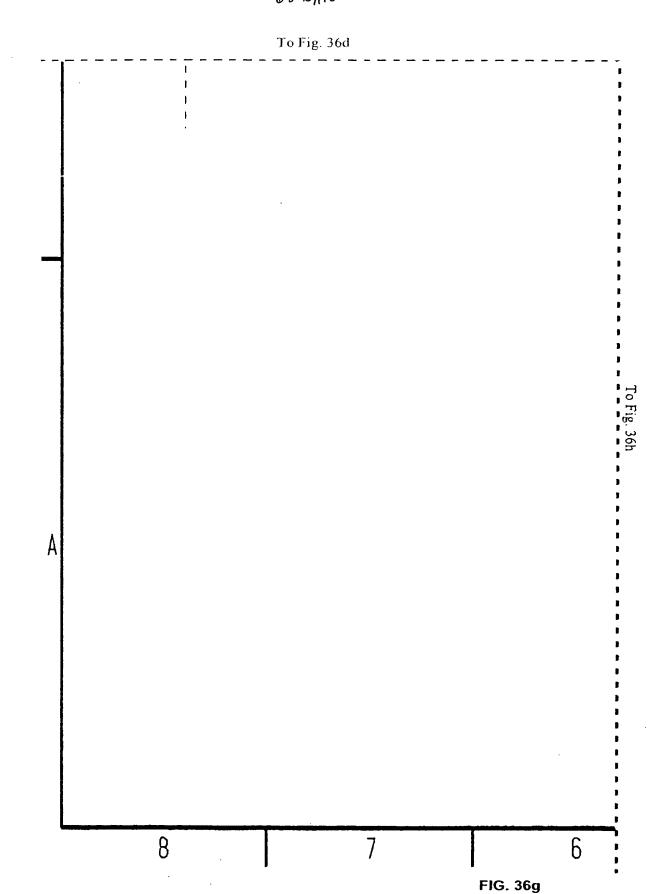
1 -			To Fig. 36			FIG. 36e	
1			, =	D14	3 DD14		
,		<u>60</u>	/IACK	D13	4 DD13	;	
		V	/INT2	D12	5 DD12] :	
)]		21	/INT1	D11	6 DD11] :	
)		20	/INTO	D10	7 DD10] :	
			М	D9	8 DD9	1	
' !		<u>55</u> 0	XF	D8	9 DD8	1	
٠ <u>٠</u> %		51 68		D7	11 DD7	1	
10K	<u> </u>	<u>51</u>	X1	D6	12 DD6	-]	
910	CTK5—	52	X2/CLKIN	D4 D5	13 DD5	1	
,	CLK1-	57	CLKOUT1	D3	14 DD4	- 1 = :	
, ;		58	CL VOUT 4	D2	15 DD3	1 1 1	
;		1	MP/-MC	D1	17 DD1 16 DD2	40 d /UB	
	* * * * * * * * * * * * * * * * * * * *	59	/MSC	D0	18 DD0		
		190	/SYNC		40 DD0		
66		<u>50</u>	/BR	A15	43 DA15	BNK32 18 A15	43
1g. 3		66	READY	A14	42 DA14	19 A14 5	ه ۲ ا
To Fig. 36d		<u>55</u>	/HOLDA	A13	41 DA13	20 A13	To Fi
		67 ₀	/HOLD	A12	40 DA12	21 A12	ر
		65 ₀		A11	39 DA11	24 A11	
j				A10	38 DA10	25 A10	
)		45	/DS	A9	37 DA9	26 A9	
) }		46	/IS	AB	36 DA8	27 A8	
) 		47	/PS	A7	34 DA7	42 A7	
,		48	R/-W	AB	33 DA6	43 A6	
<u>B</u>		49	/STRB	A5	32 DA5	44 A5	
3		1 33	700	A4	31 DA4	1 A4	
-/		35	VCC VCC	2A 8A	30 DA3	3 A2 ; 2 A3 ;	
		23	VCC	Δi	29 DA2	,	
:		62			28 DA1	4 4	

Inventor(s): Ronald G. Hart

Attorney Docket No. 6270/76, Serial No. 10/068,431 (0/6/5,70/ 6270/110



Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS
Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76, Serial No. 10/068,431-10/613,70/



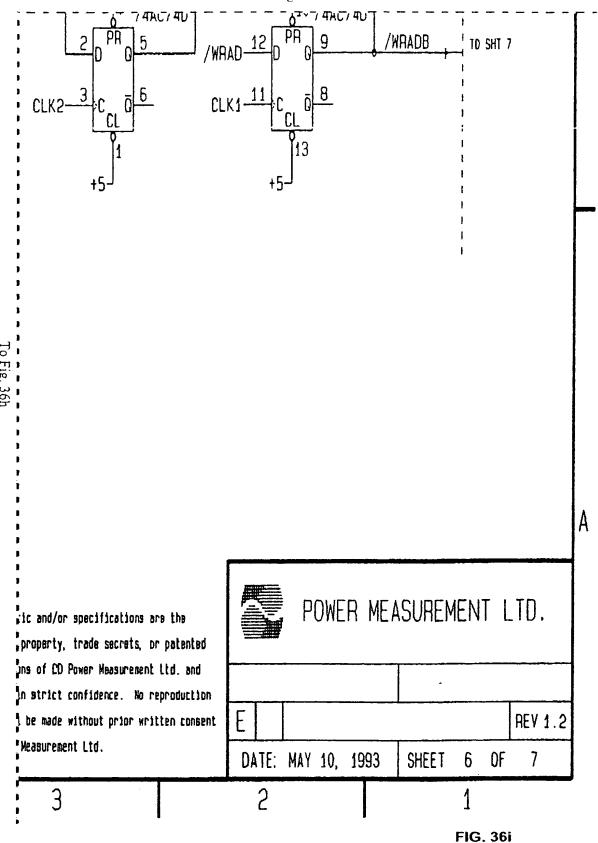
Inventor(s): Ronald G. Hart (しん)
Attorney Docket No. 6270/76, Serial No. 10/068,431 いんりょうしょ

To Fig. 36e 3 DD14 D14 2 DD15 64 CLKA 015 25 FSR 24 DA 63 CLKX 10 VSS L 53 FSX VSS 54 DX 27 VSS This schemat. proprietary L configuration are issued i or nas apall of CD Power ! 5 4

FIG. 36h

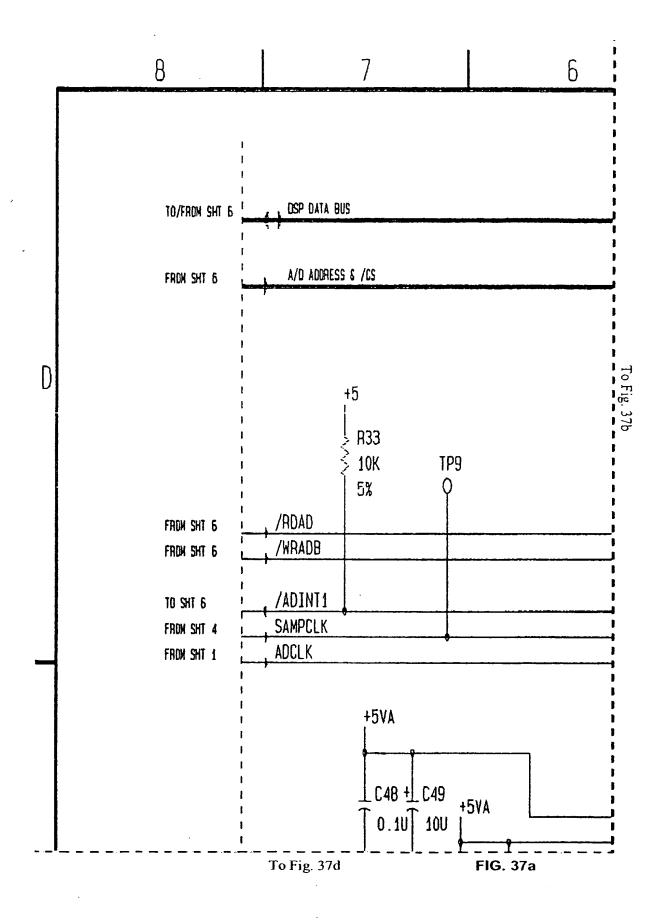
Attorney Docket No. 6270/76, Serial No. 10/068,431 19/613,つい 1 6 よつの/11 0

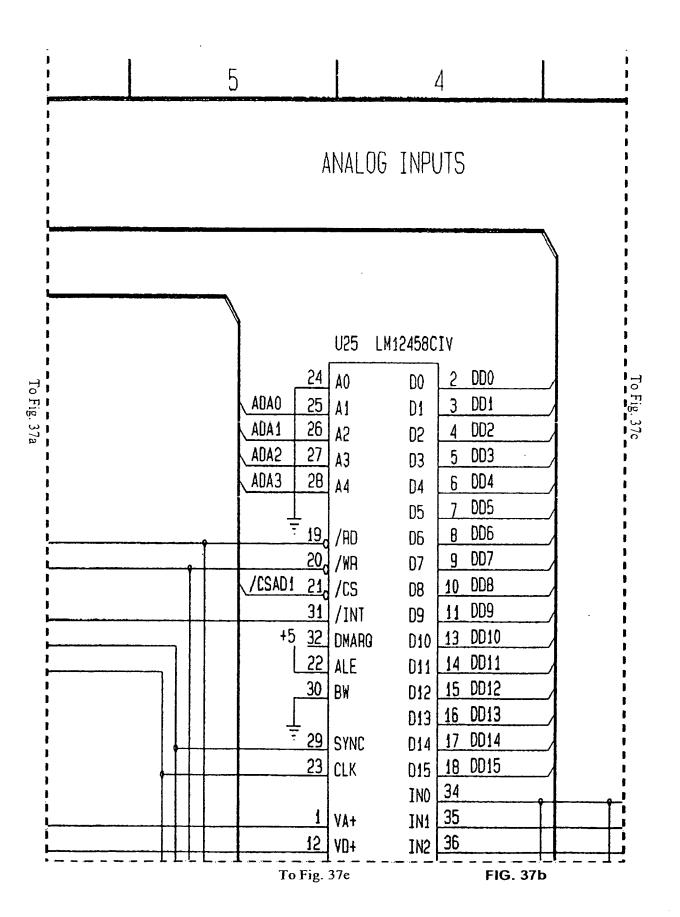
To Fig. 36f

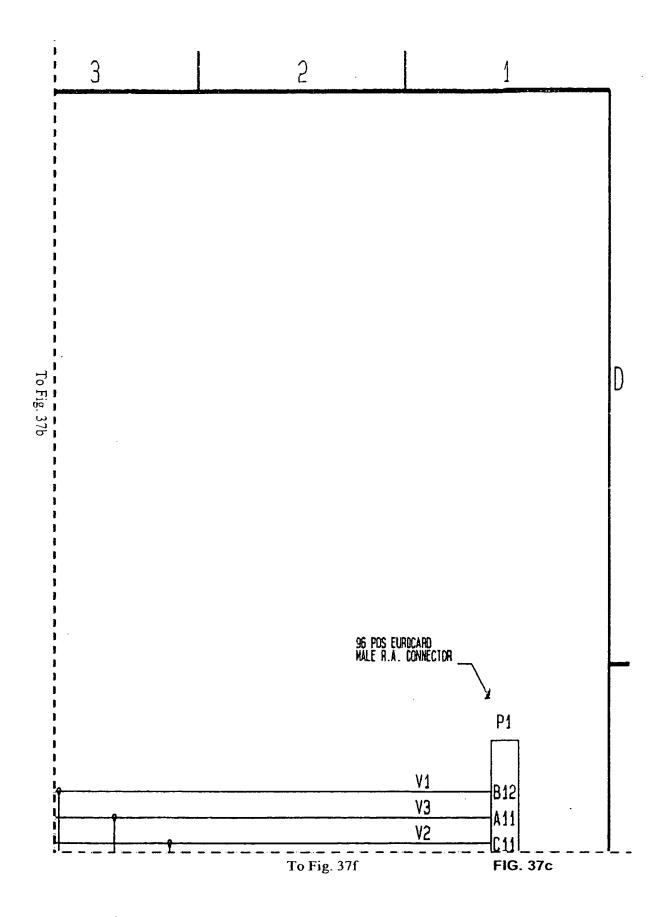


Inventor(s): Ronald G. Hart

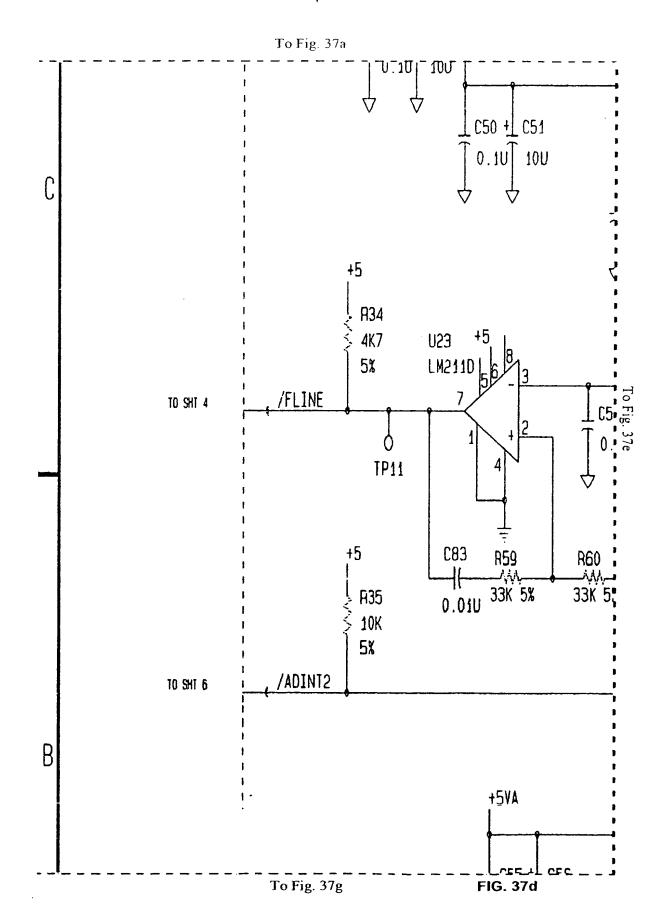
Altorney Docket No.-6270/76, Serial No.-10/68,431 10/613,70/6270/116







Altorney Docket No. 6270/76 Serial No. 10/068,431 10/6/3,70/



To Fig. 37b IN2 36 12 VD+ IN3 37 38 VREF-IN4 43 VREF+ 39 IN5 VREFOUT IN6 €52 1 €53 33 GND IN7 41 0.10 100 C61 0.10 **R58** To Fig. 37d 220K 5% **U26** LM12458CIV 10.01U DD0 A0 D0 ADAO DD1 A1 D1 ADA1 56 005 **A2** 4 05 ADA2 27 DD3 **A3** D3 ADA3 28 DD4 **A4** 04 DD5 05 5% 19_d /RD 8 DD5 **D6** 007 /WR **D7** /CSAD2 DD8 /CS DB 10 31 /INT DD9 11 09 +5 32 DMARO DD10 13 D10 55 14 DD11 ALE D11 15 DD12 30 BW 012 16 DD13 D13 29 SYNC 17 DD14 D14 L D15 18 DD15 23 CLK IND 34

To Fig. 37h

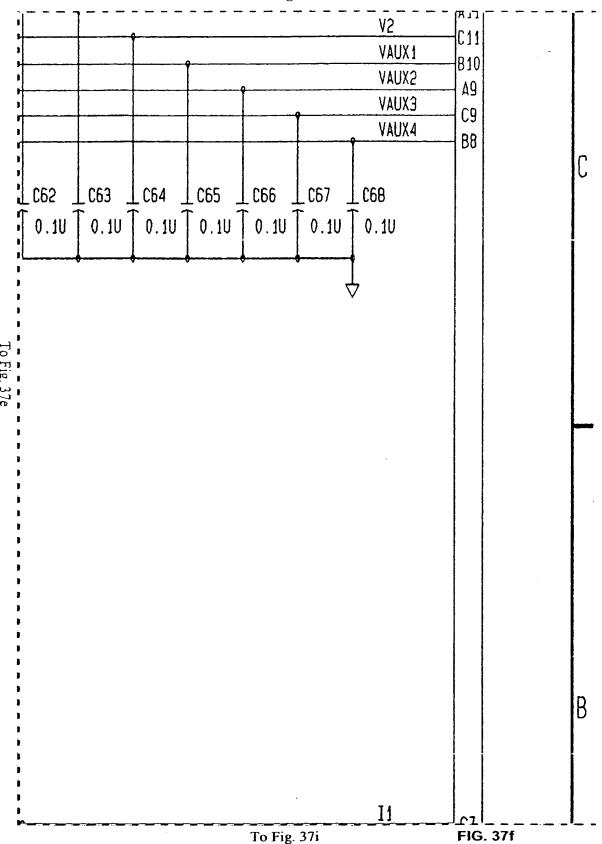
FIG. 37e

Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS

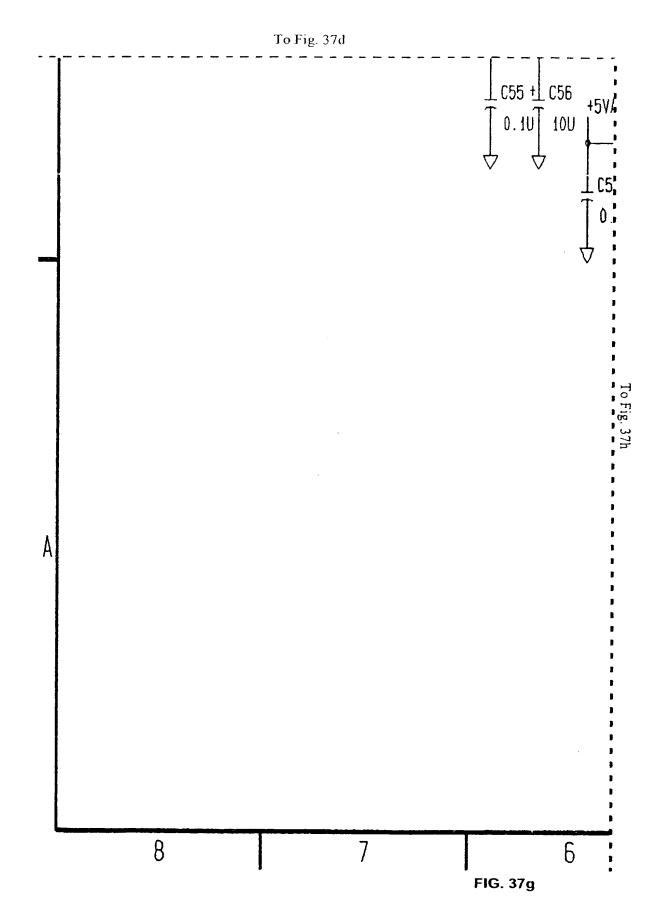
Inventor(s): Ronald G. Hart

Attorney Docket No. 6270/76; Serial No. 40/068,434 (0/6/3,70/

To Fig. 37c

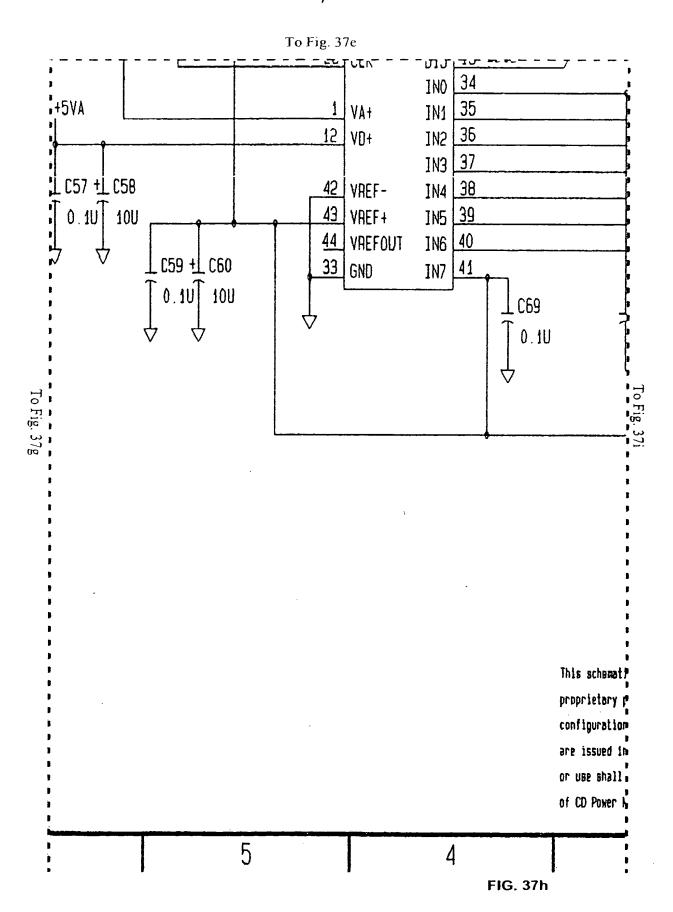


Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS
Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76, Serial No. 40/068,43 0/613,701



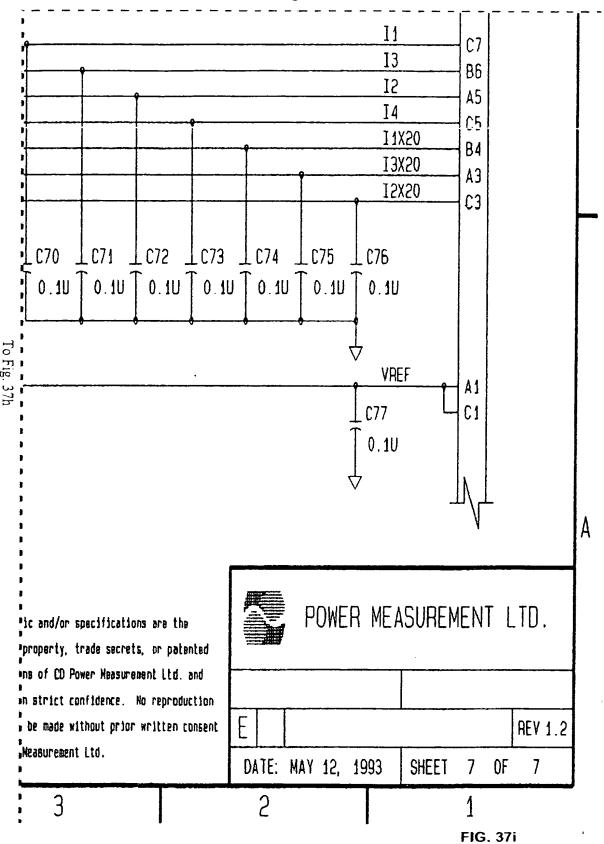
Inventor(s): Ronald G. Hart

Altorney Docket No. 6270/76, Serial No. 40/068,431 10/6/3,70/6270/110



Attorney Docket No. 9270/76, Serial No. 10/068,431 /0/6/3,70/

To Fig. 37f

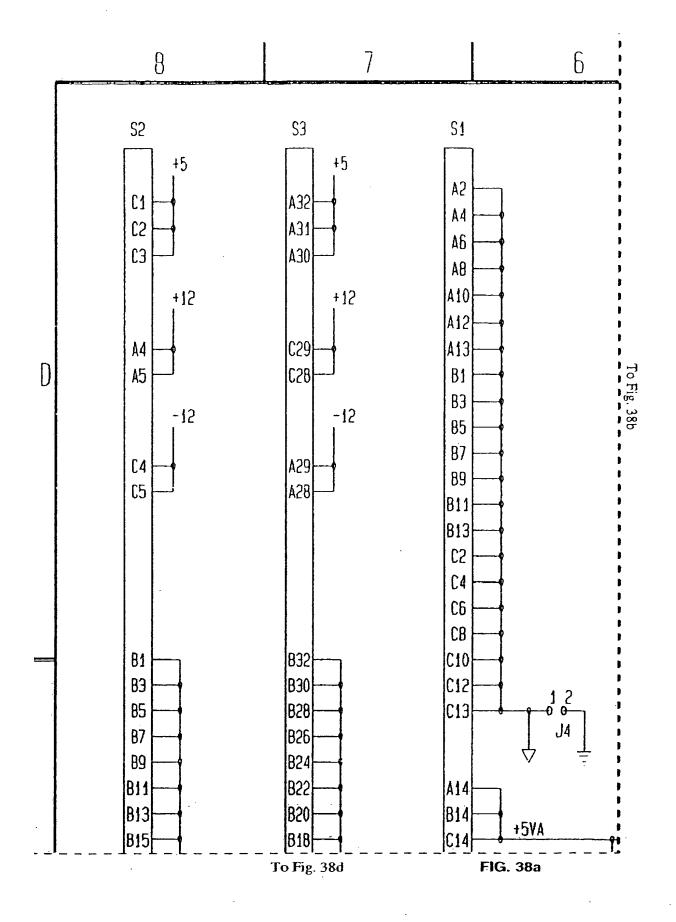


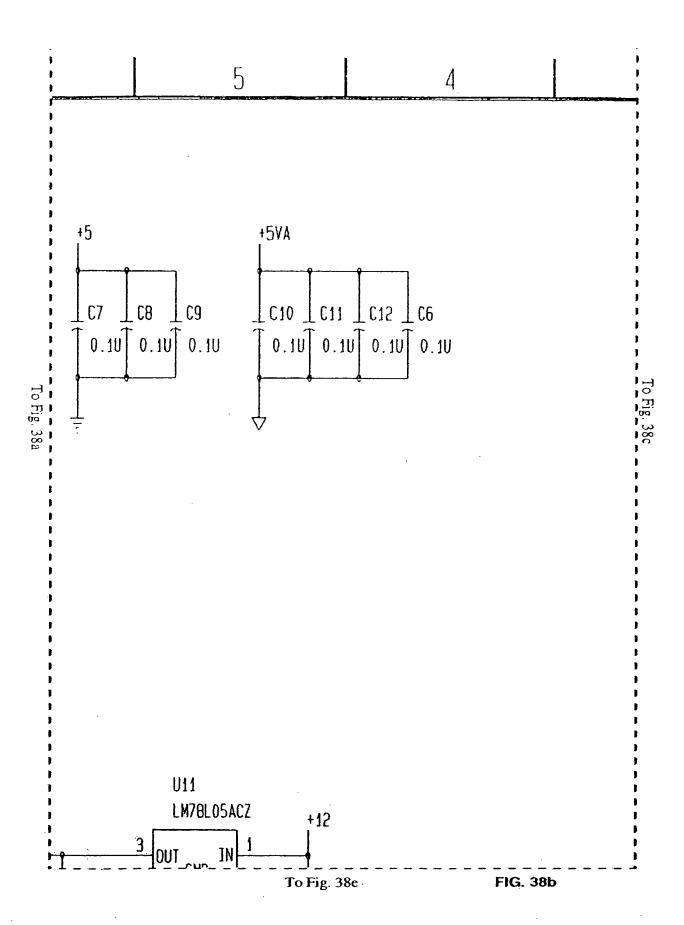
Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS

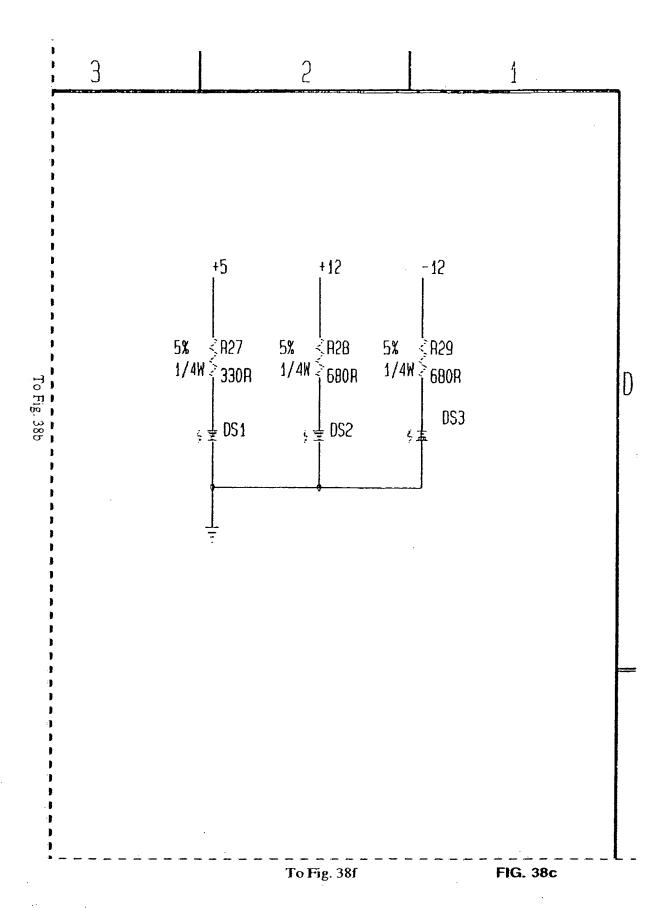
Inventor(s): Ronald G. Hart

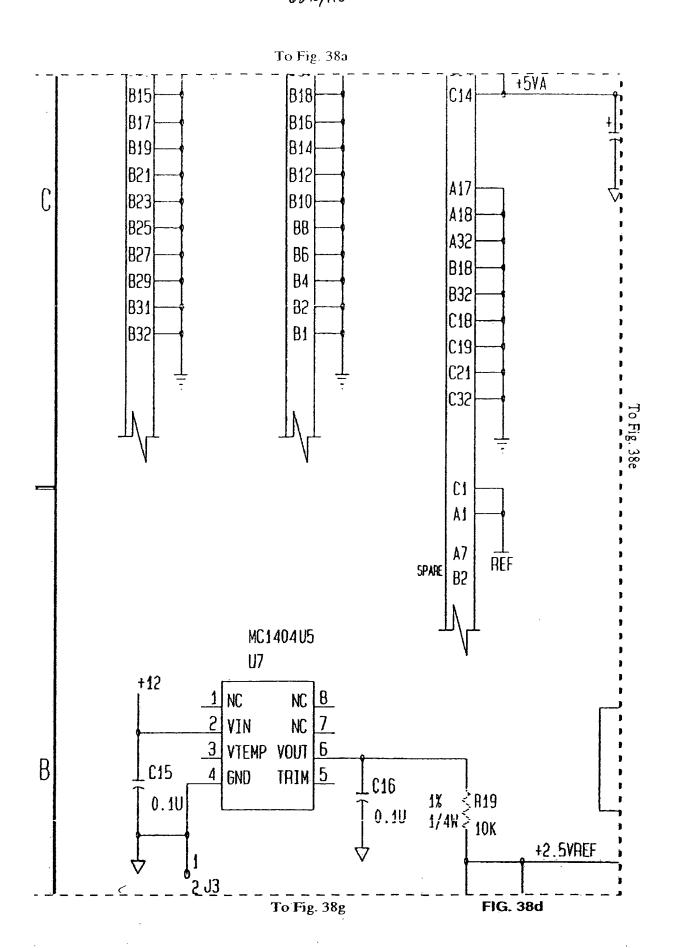
Altorney Docket No. 6270/76, Serial No. 10/068,431

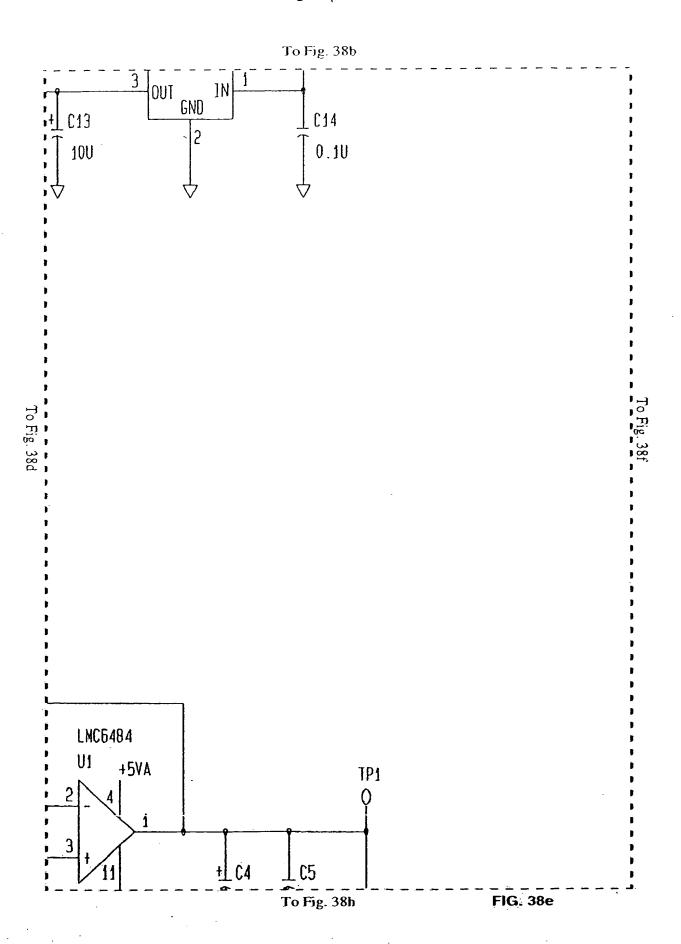
6270/10

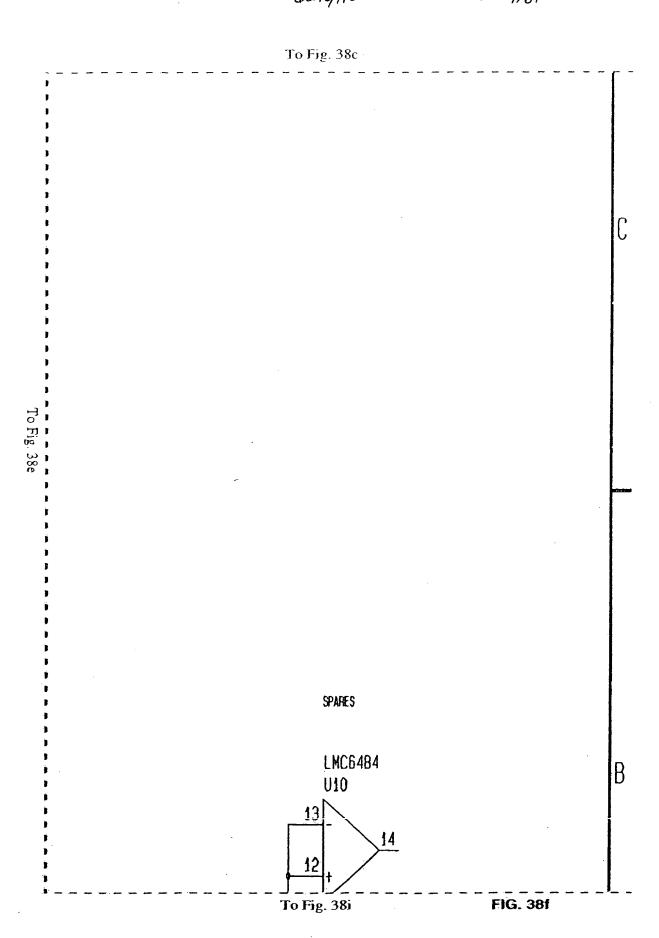


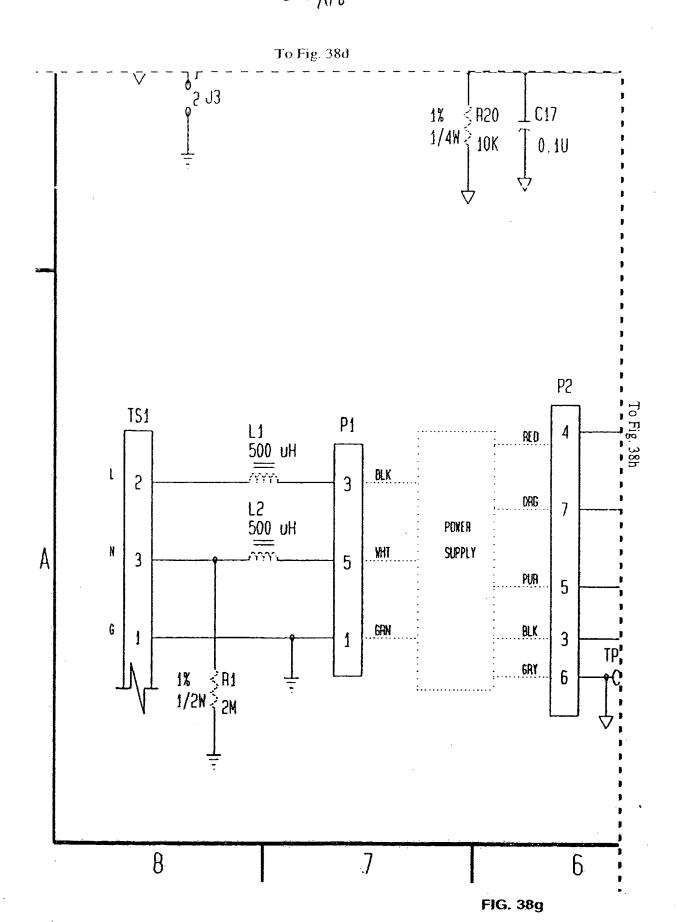


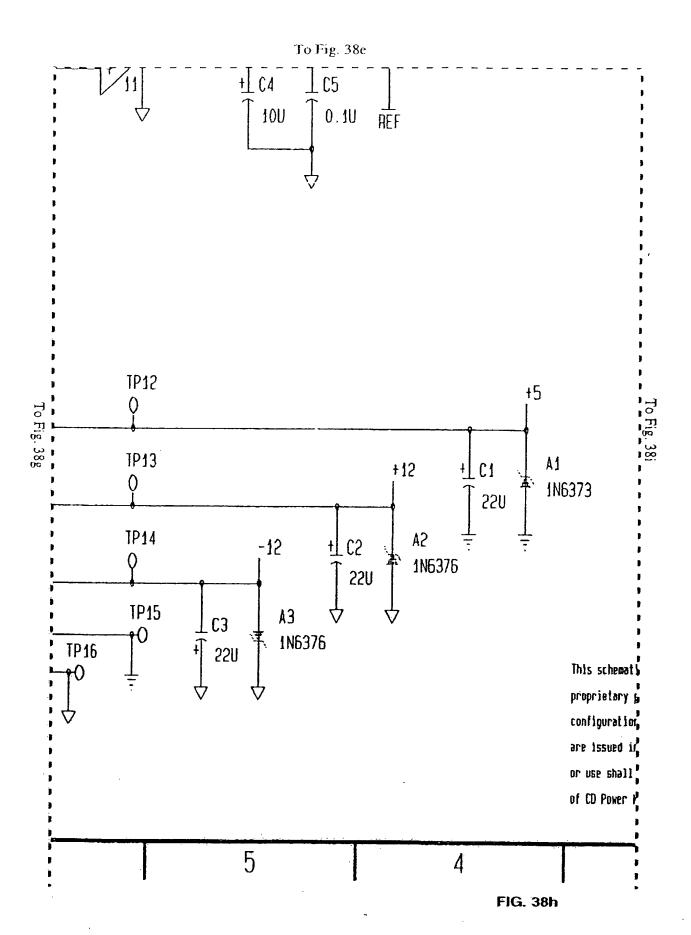


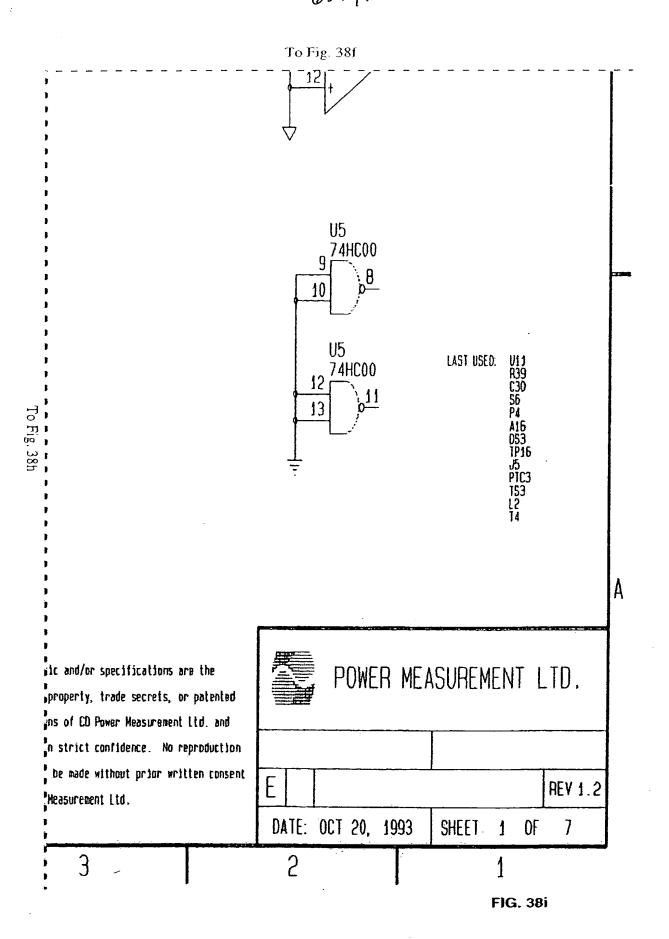


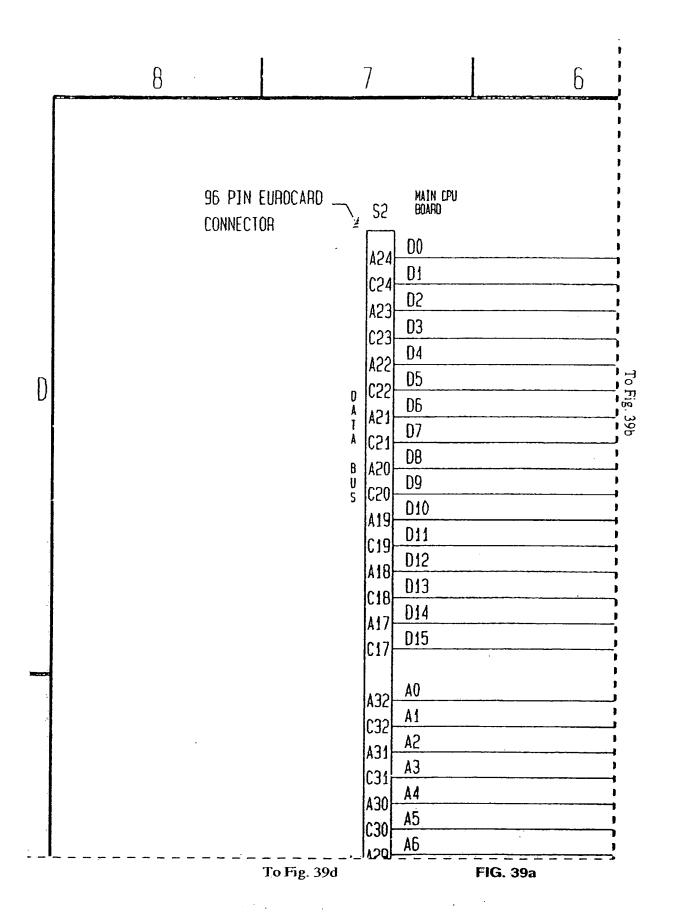








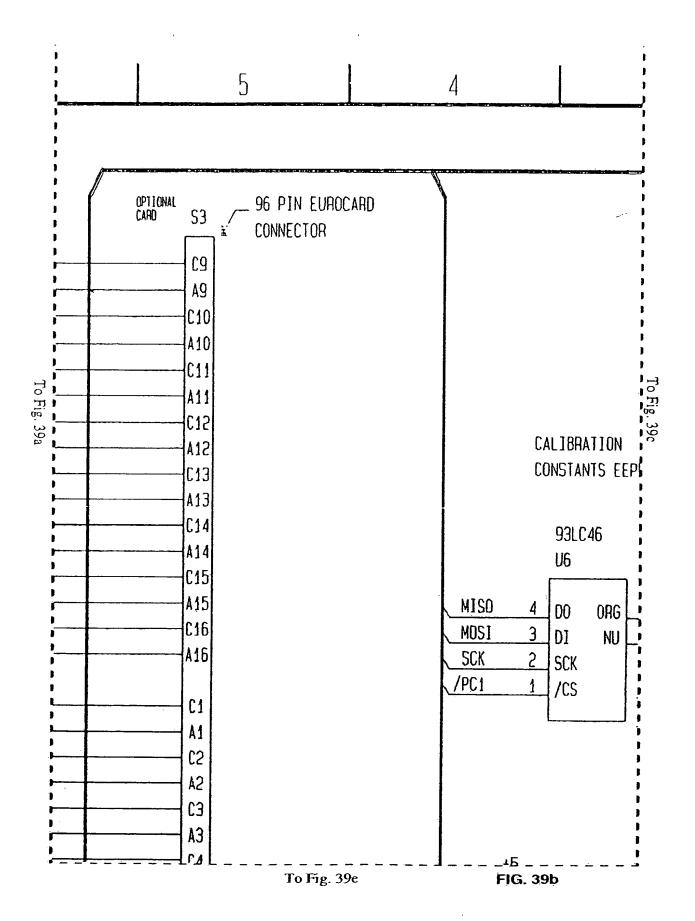


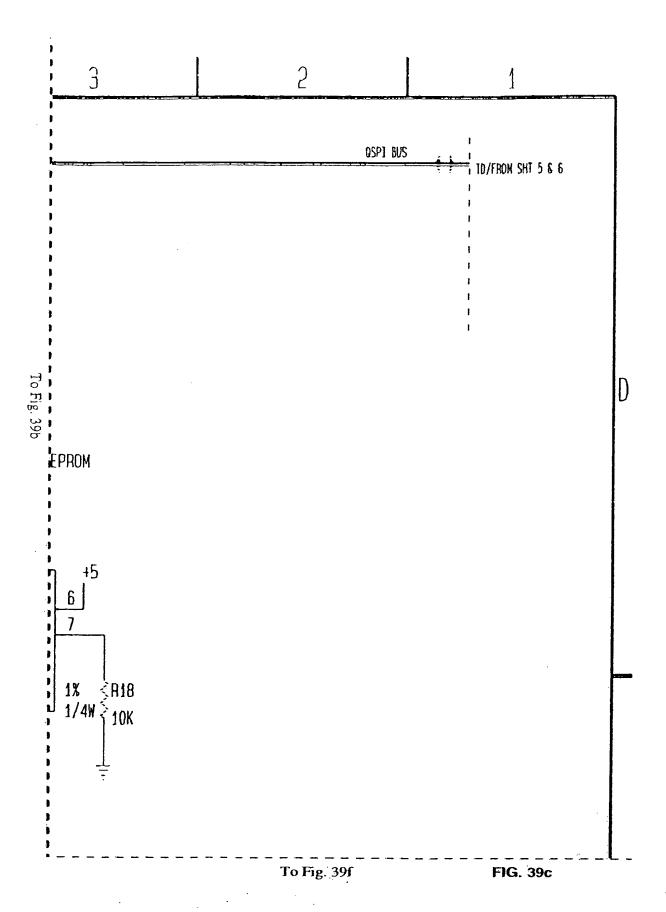


Inventor(s): Ronald G. Hart

1379 207

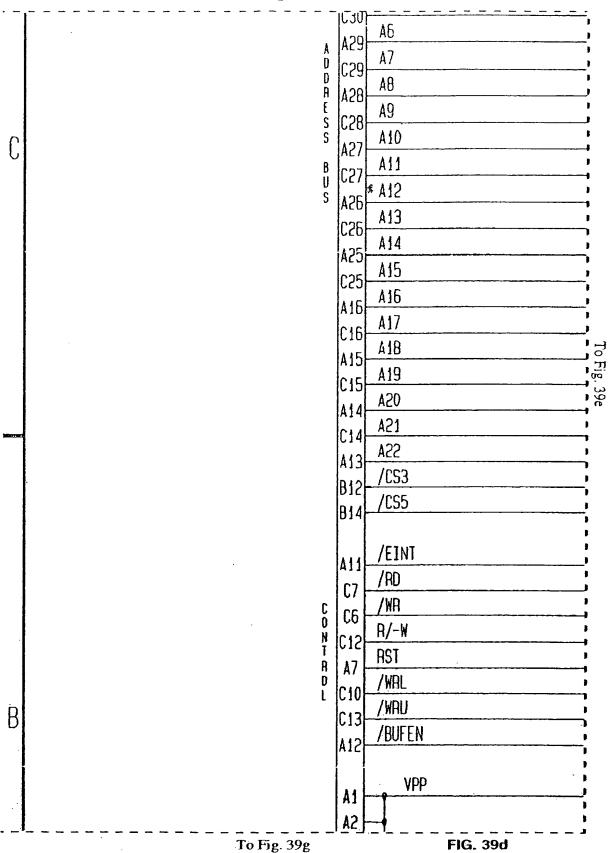
Inventor(s): Ronaio G. 100.
Attorney Docket No. 6270/28 Serial No. 10/068431-





Inventor(s): Ronald G. Hart
Attorney Docket No. 6270/76 Serial No. 10/068,437613,70)

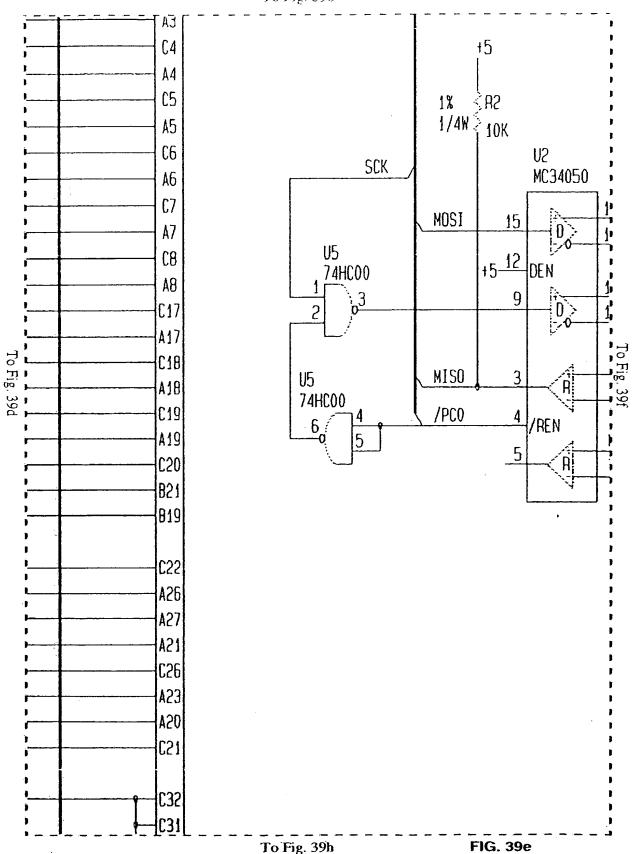
To Fig. 39a



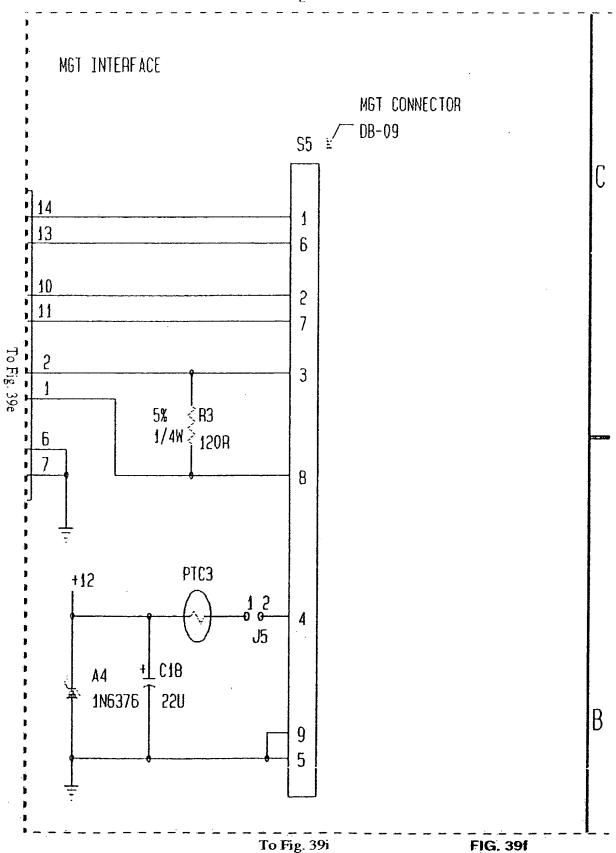
Inventor(s): Ronald G. Hart

Attorney Docket No. 6270/76, Serial No. 10/968,431-613.76

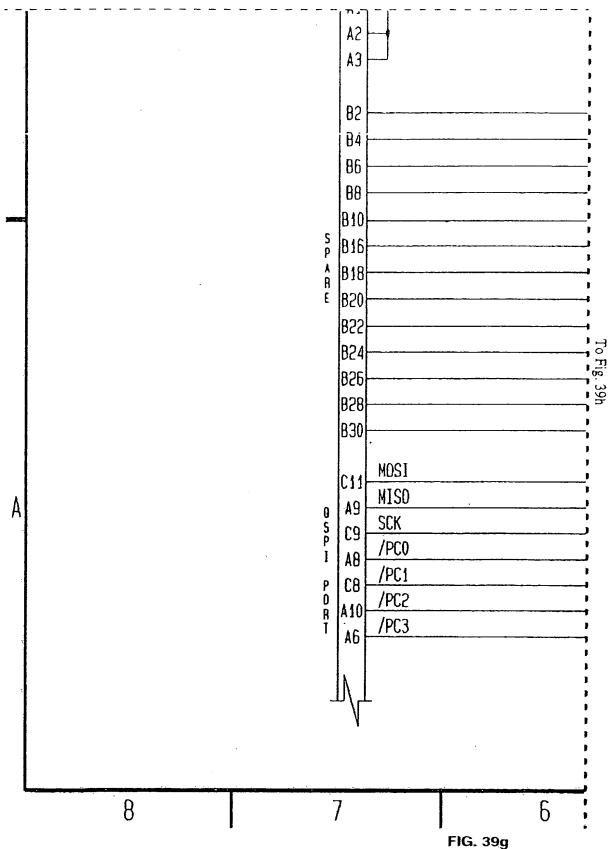
To Fig. 39b



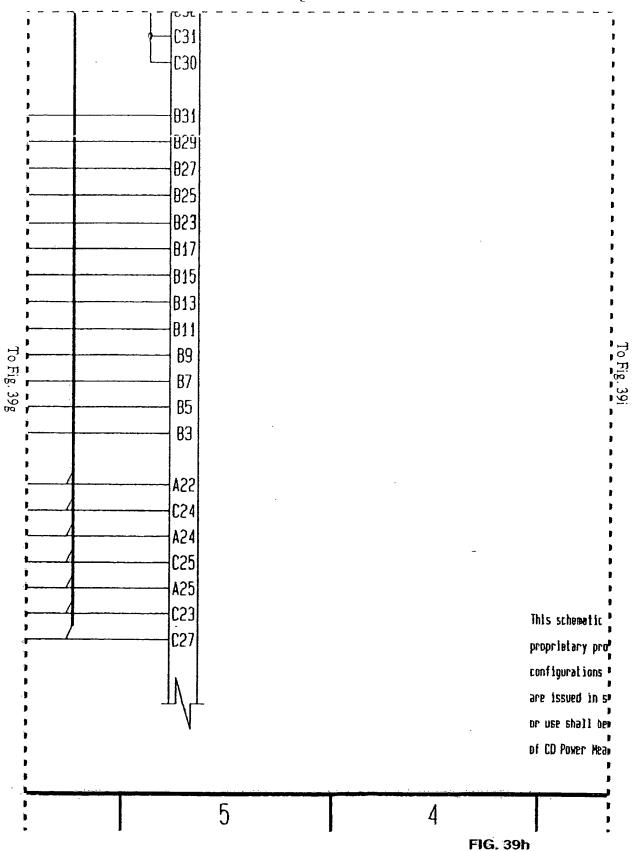
To Fig. 39c



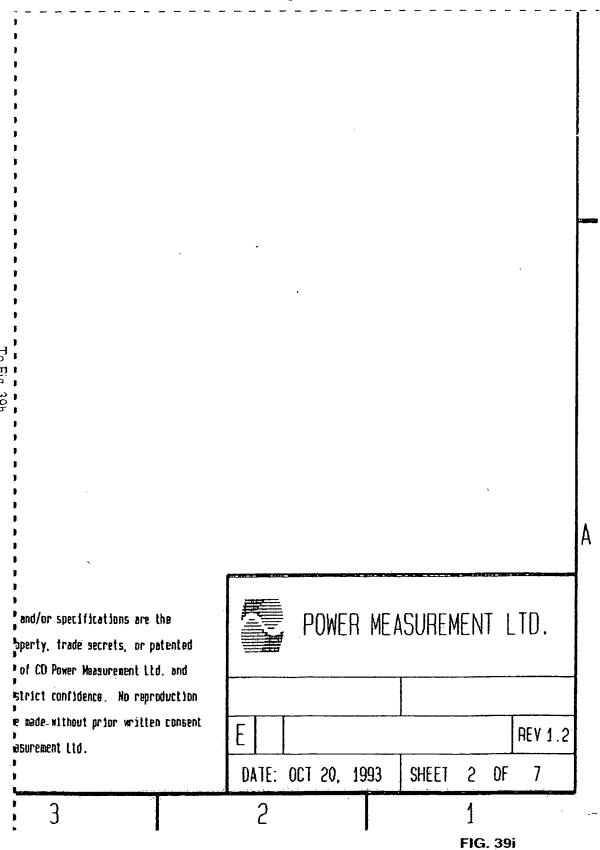


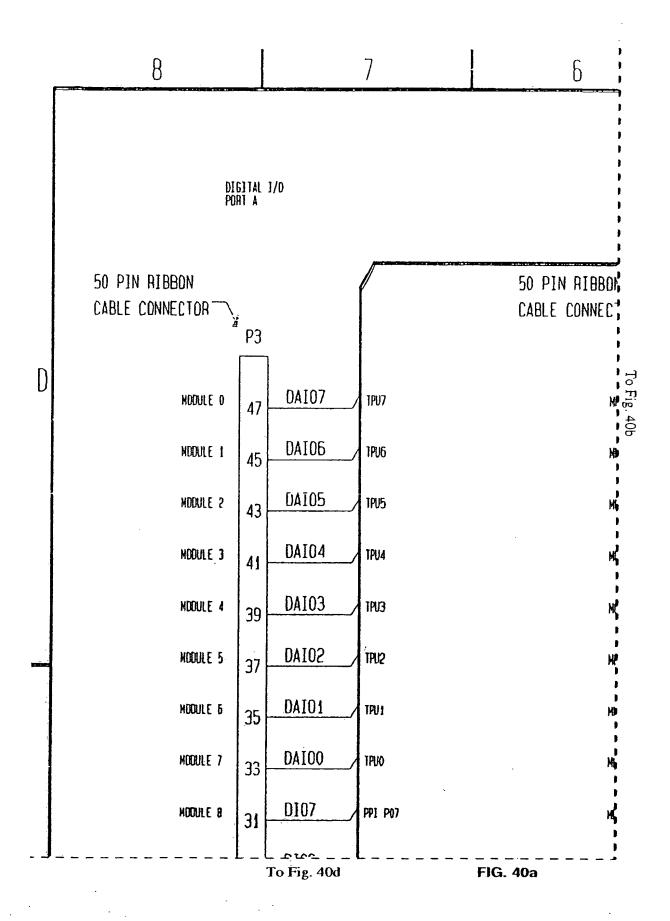


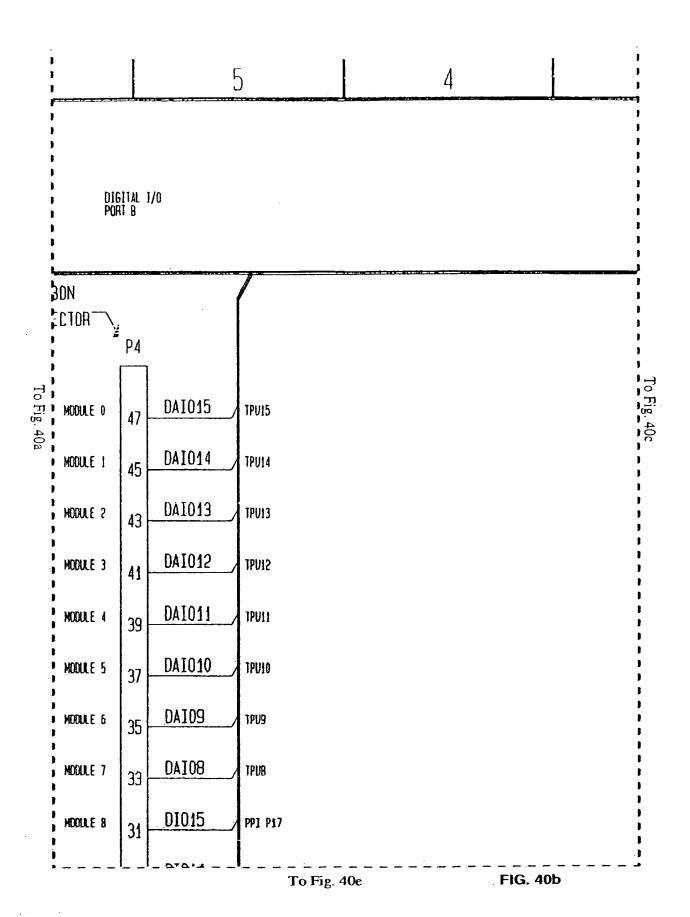
To Fig. 39e

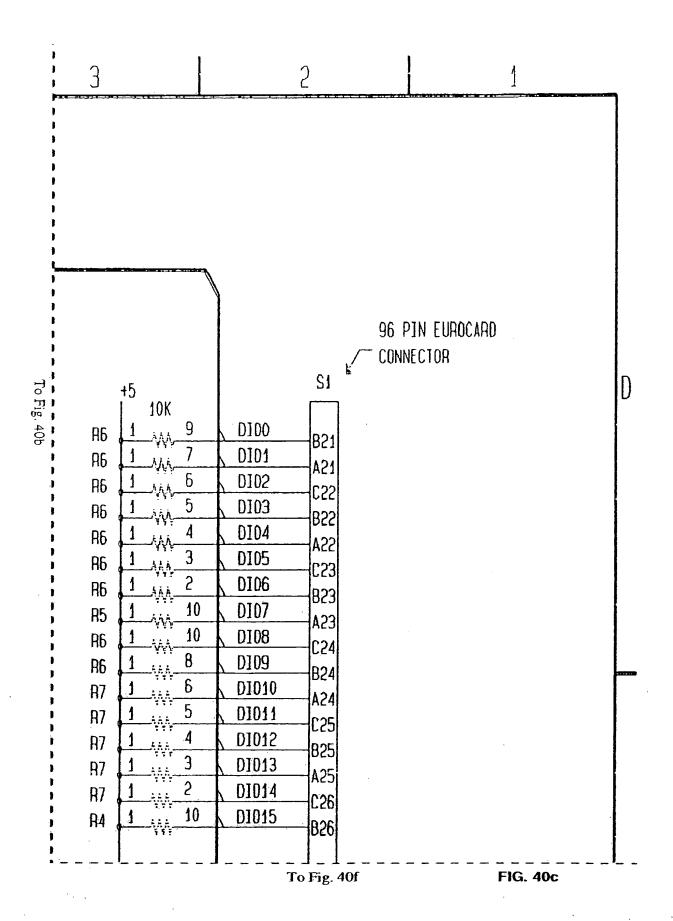


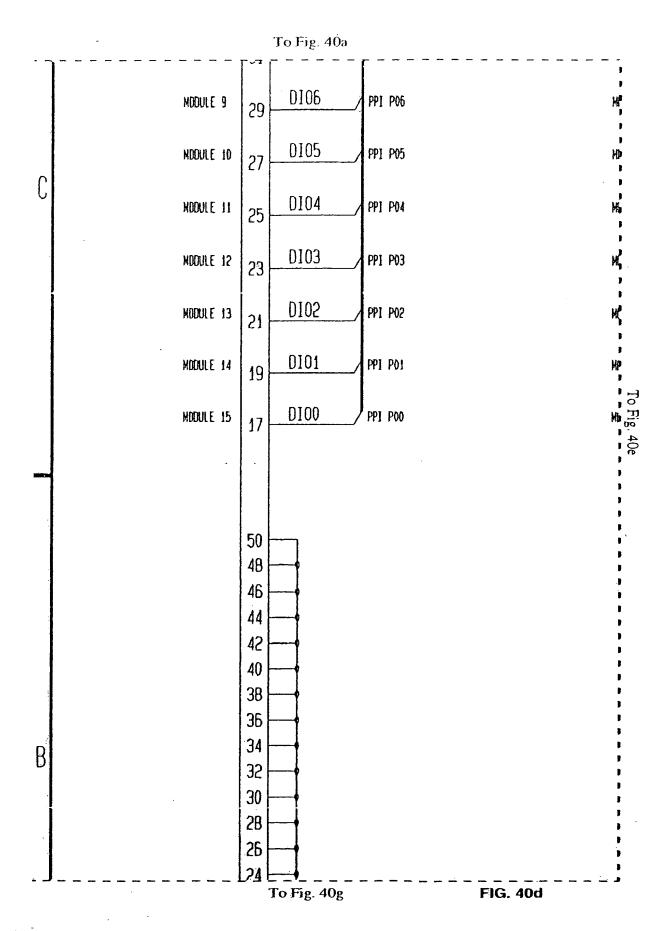




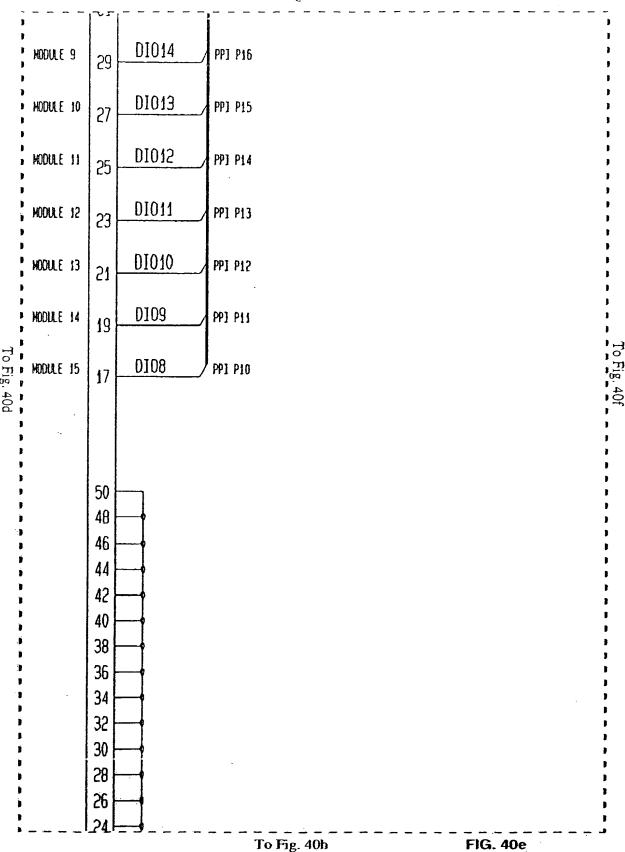


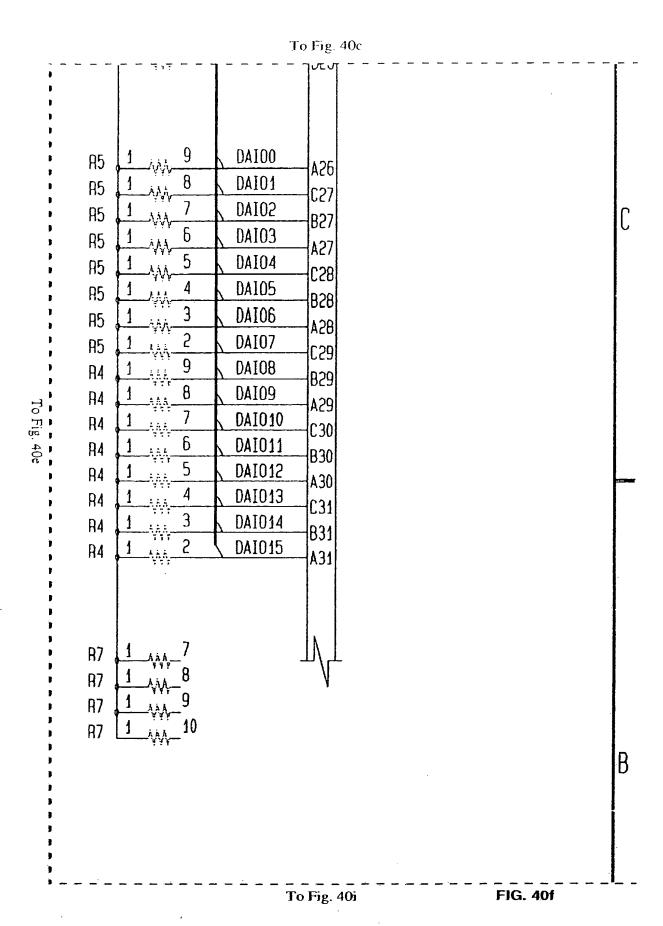


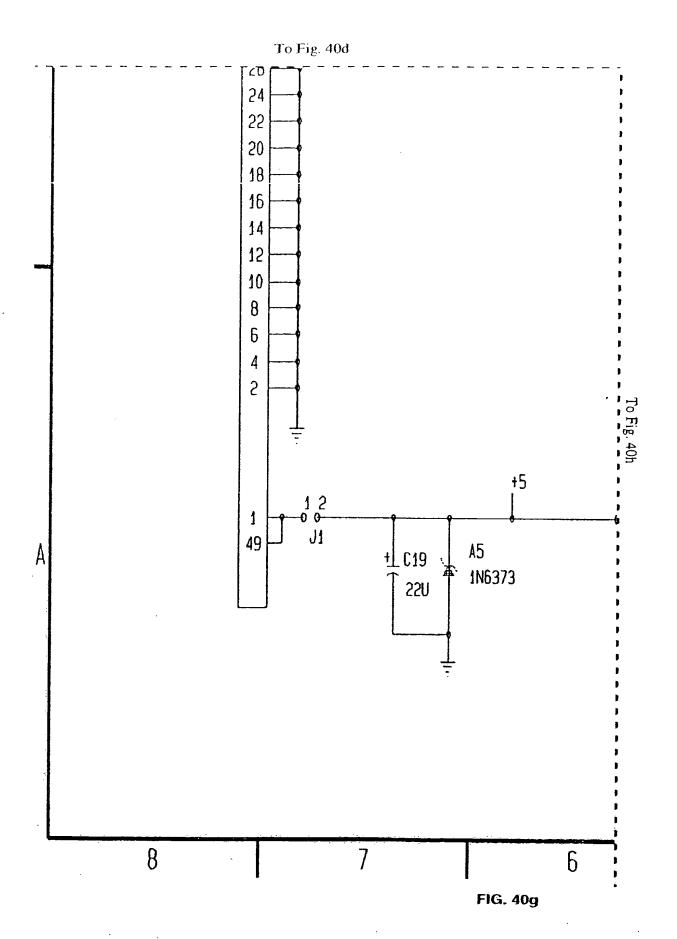




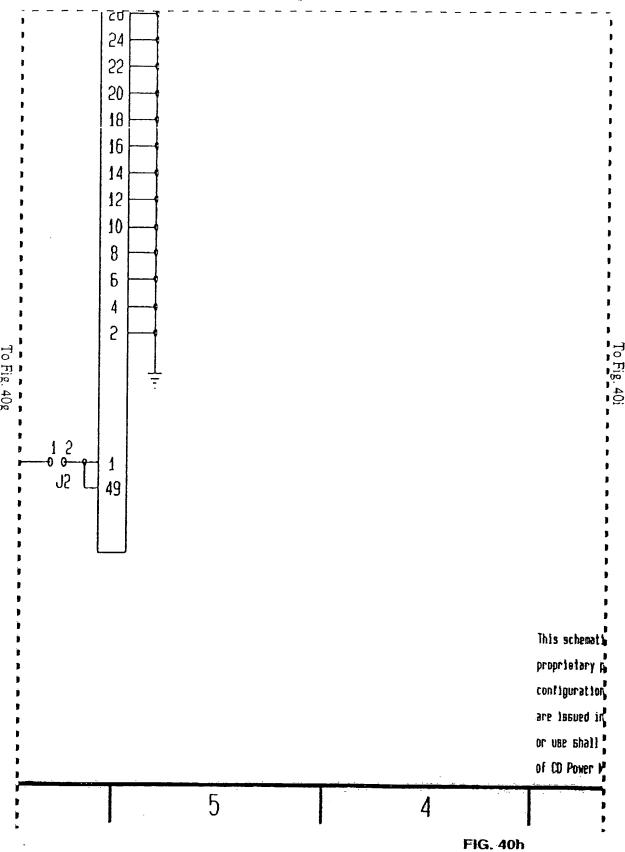
To Fig. 40b











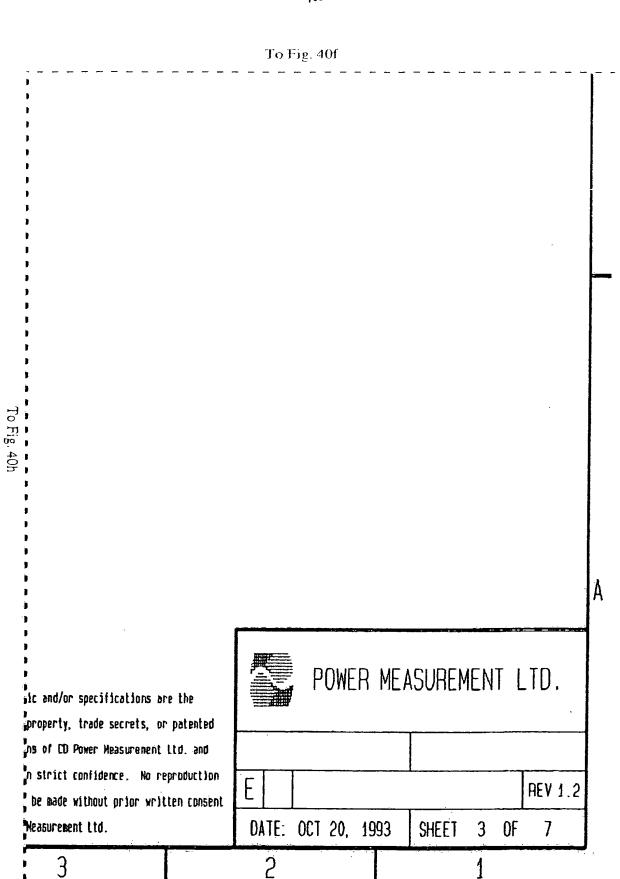
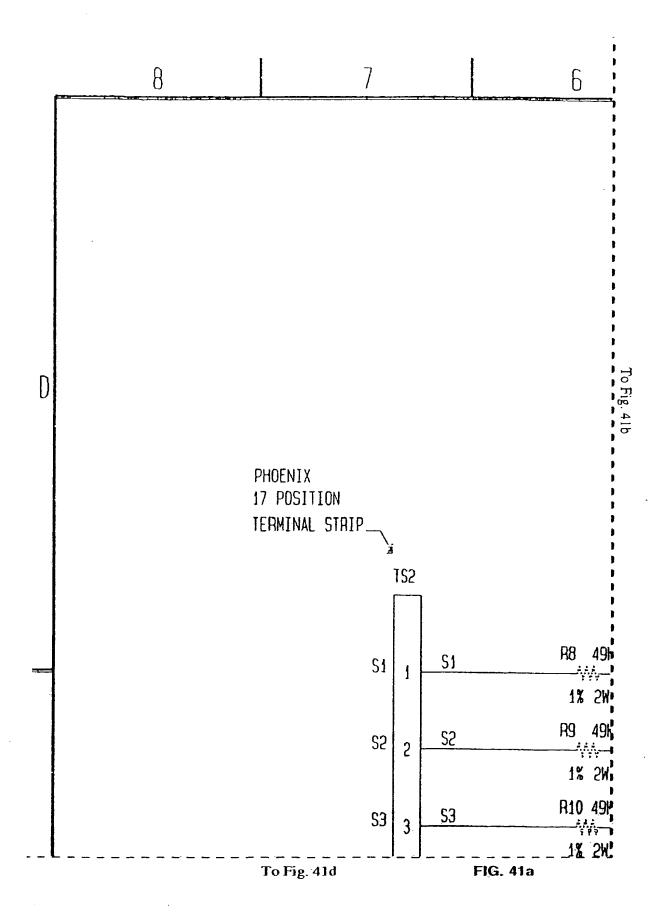
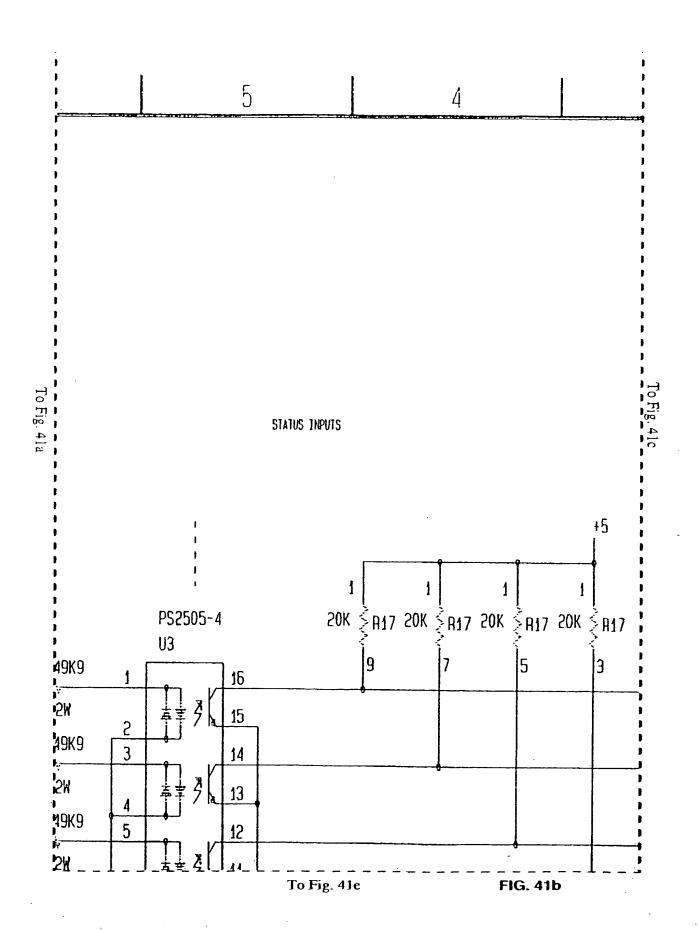
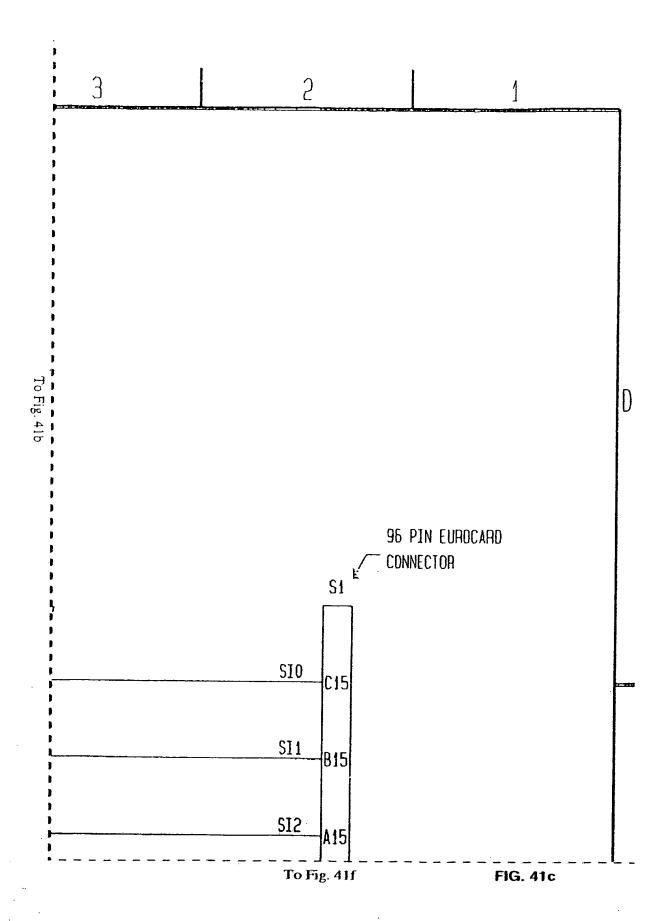


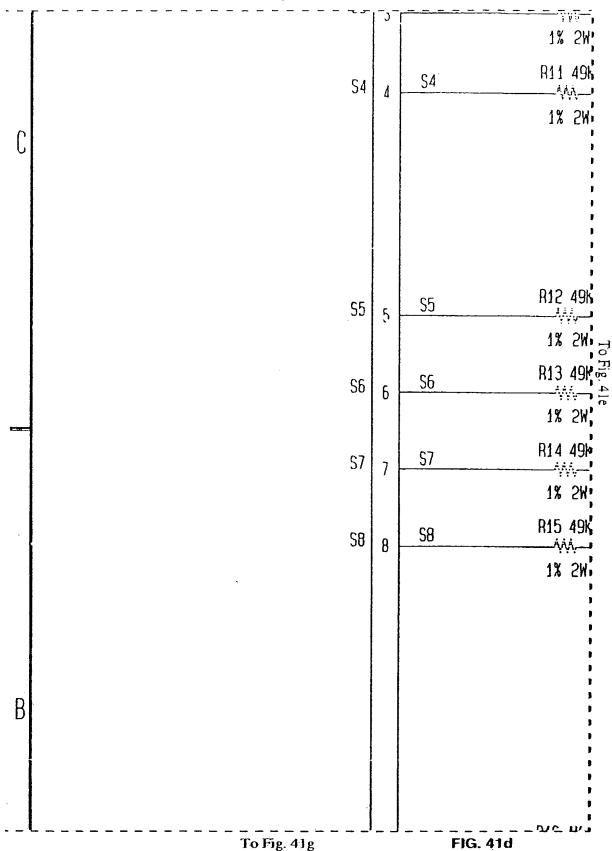
FIG. 40i

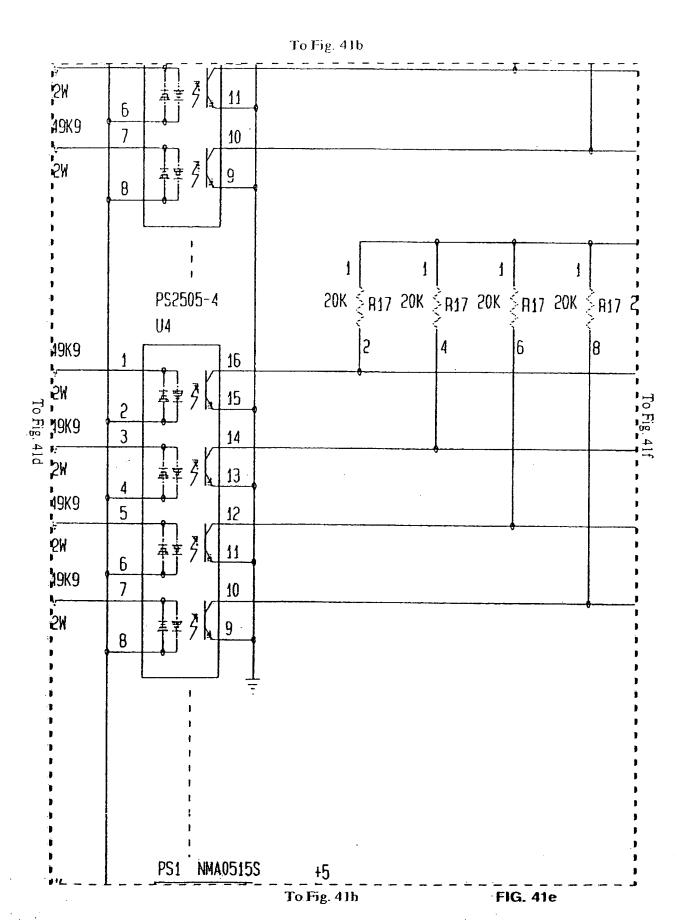


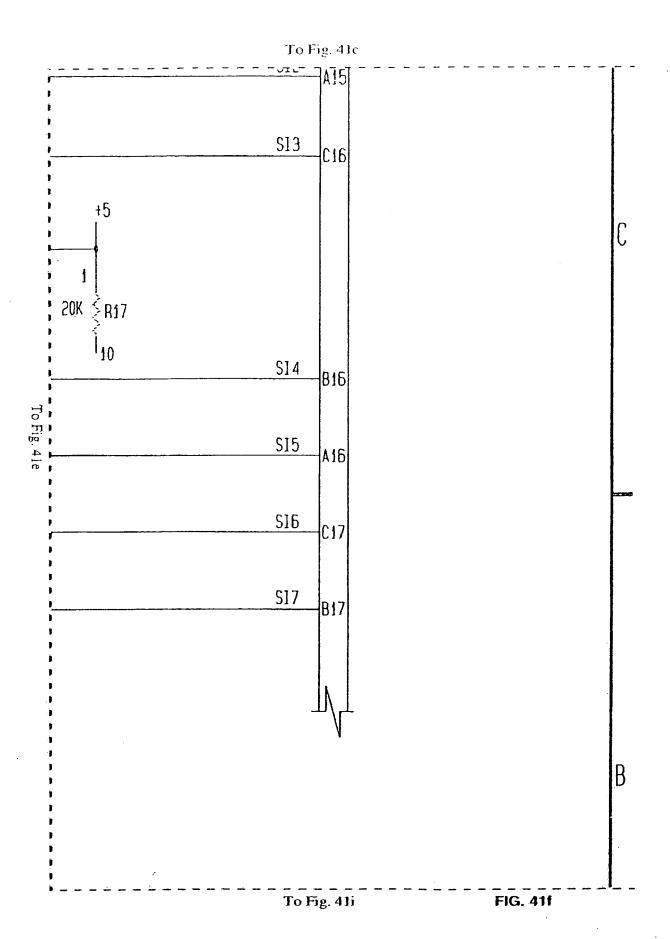


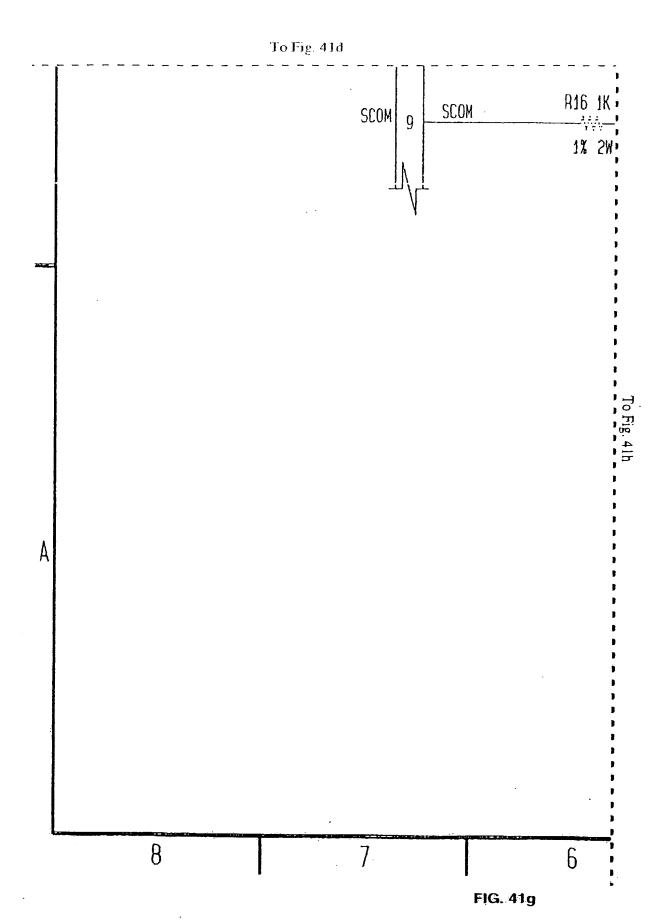












To Fig. 41e

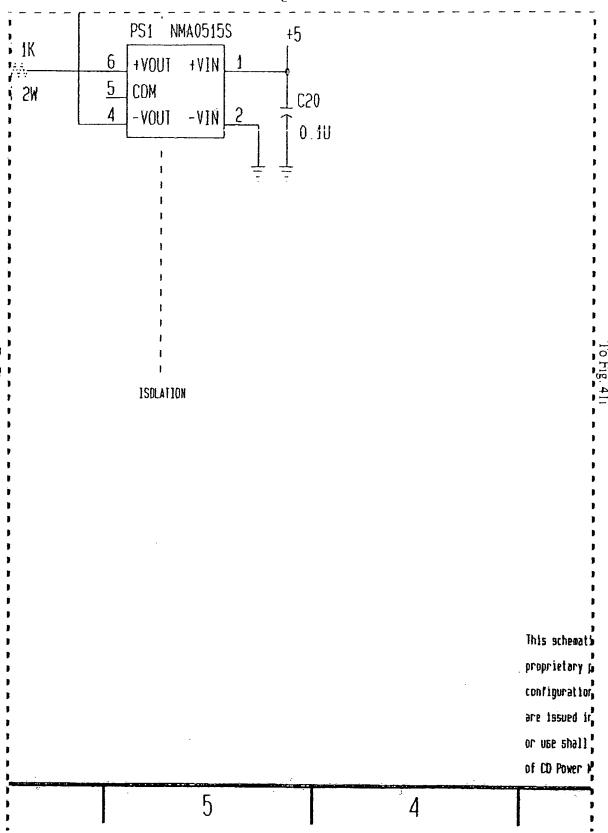
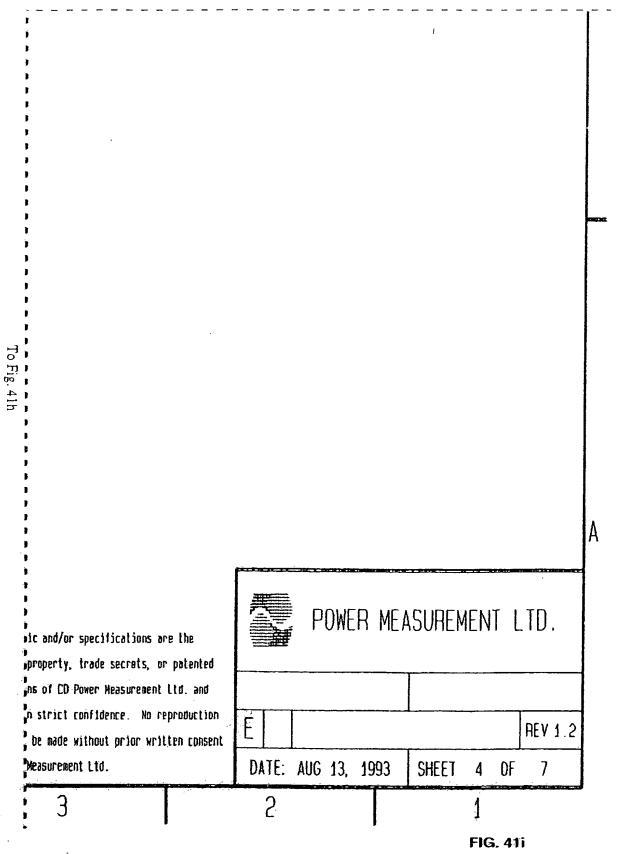
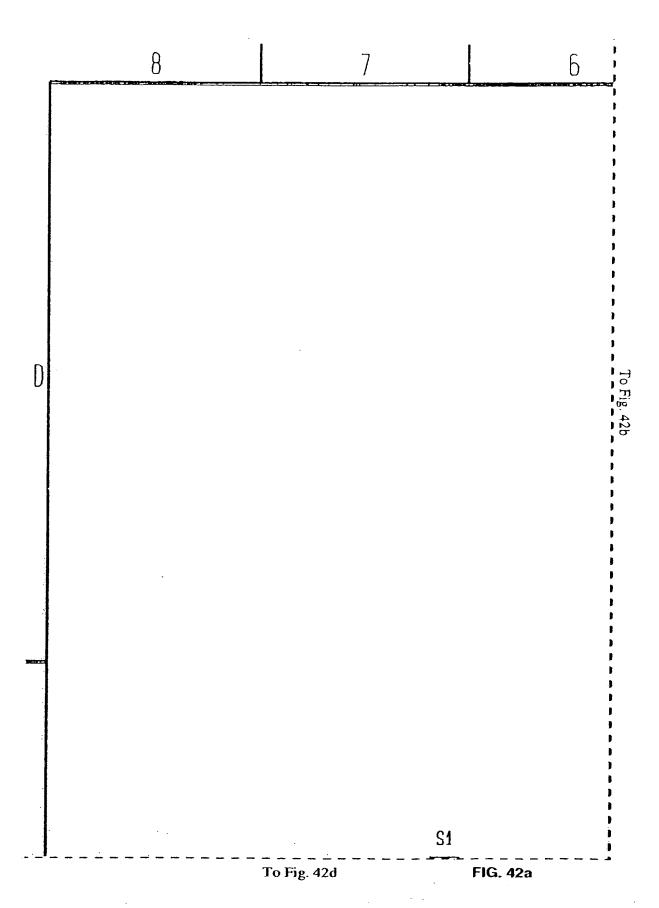
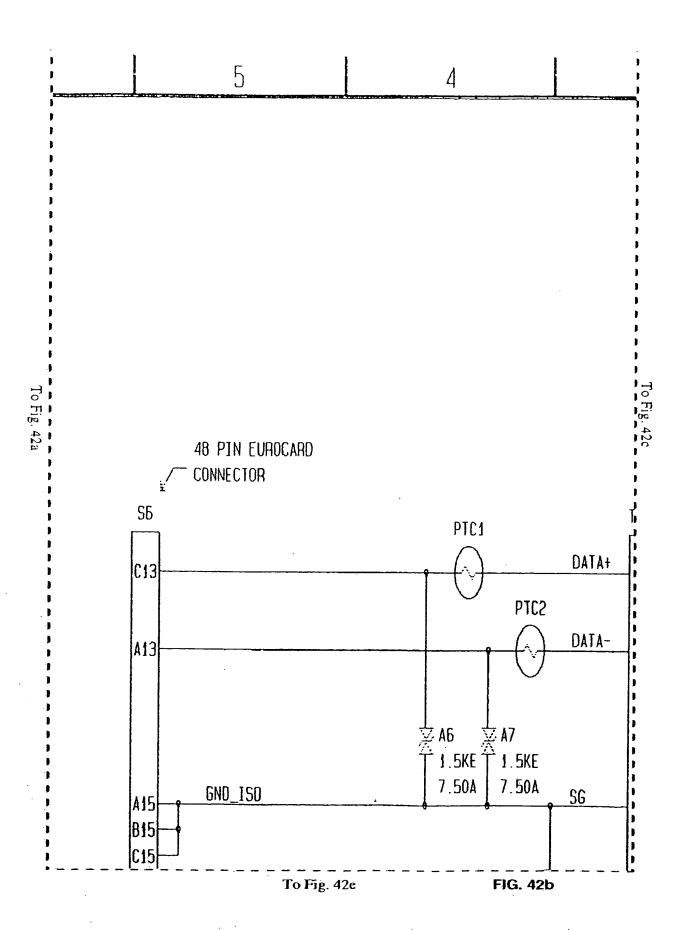


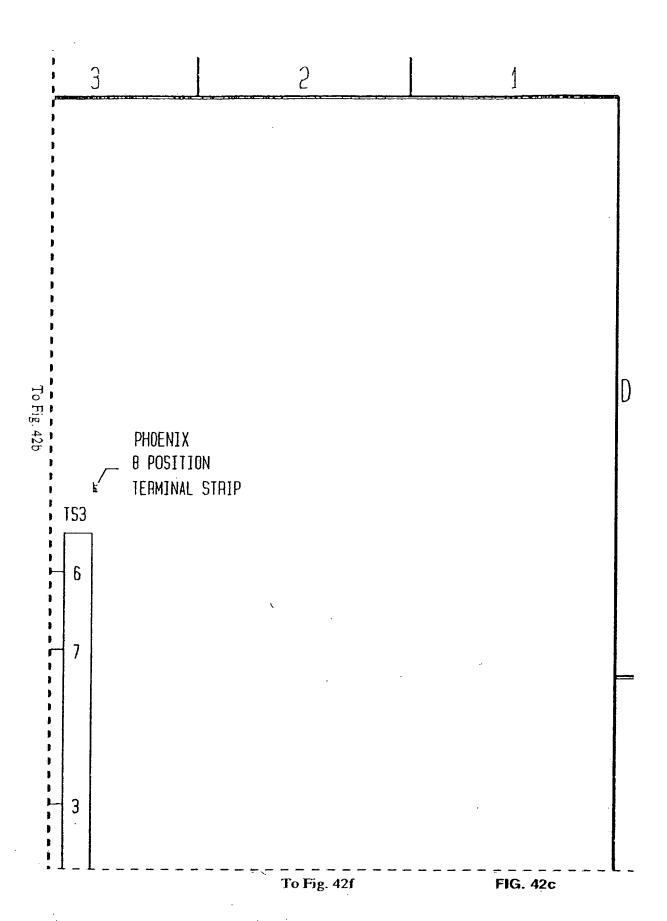
FIG. 41h



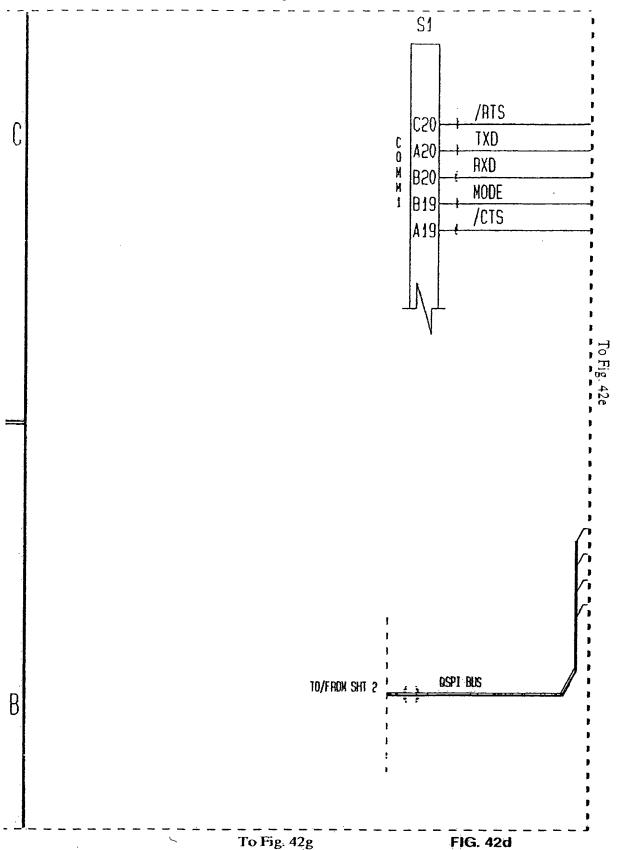




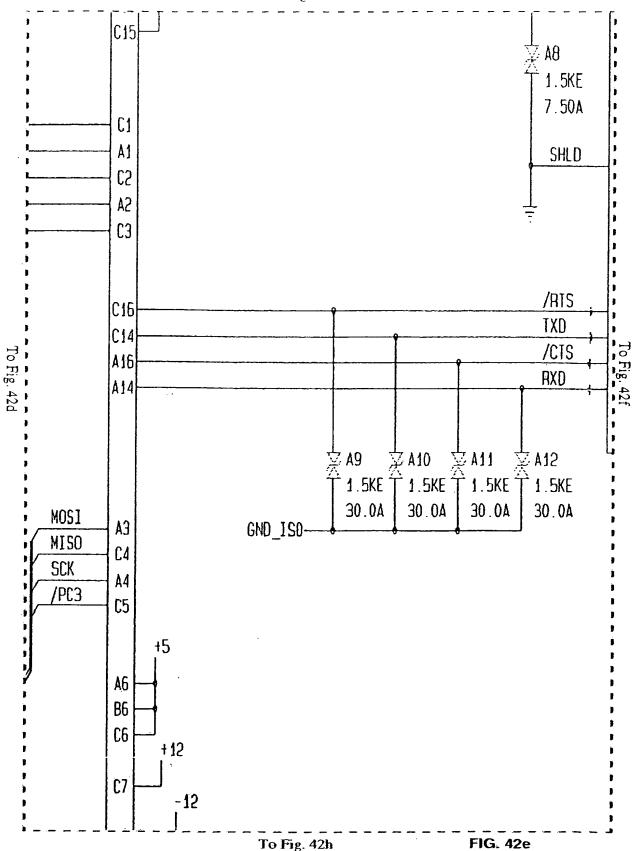




To Fig. 42a



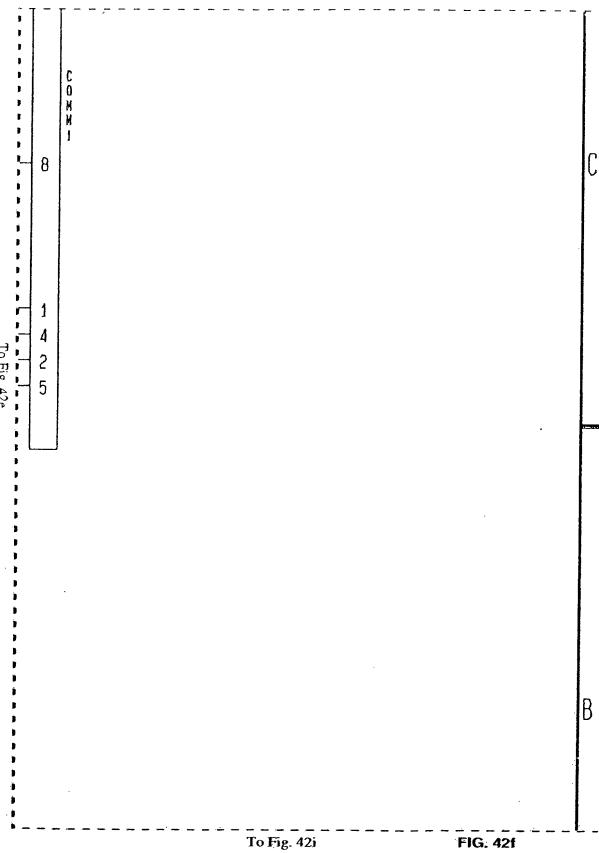


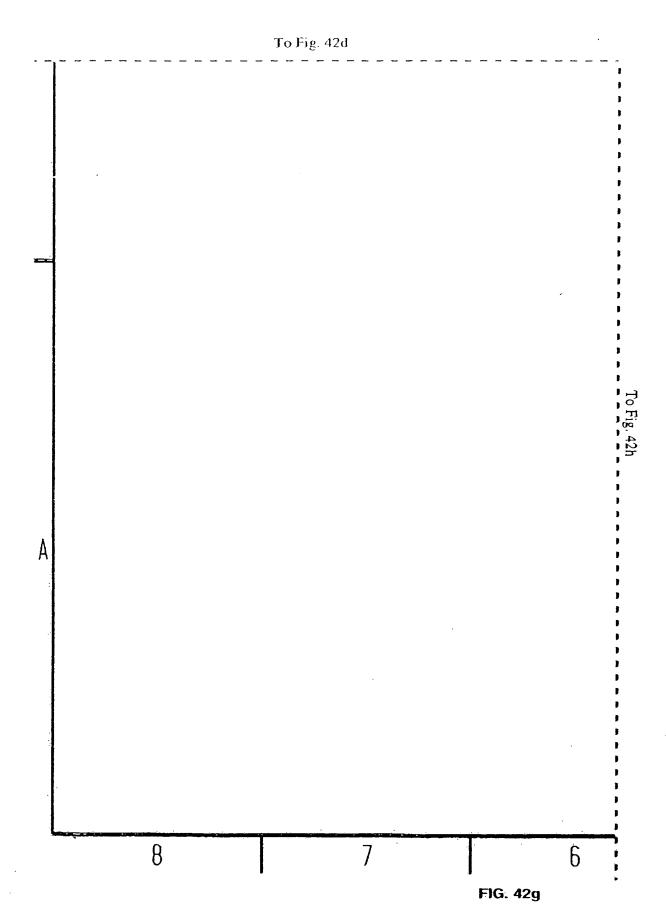


Patent Application for: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS Inventor(s): Ronald G. Hart

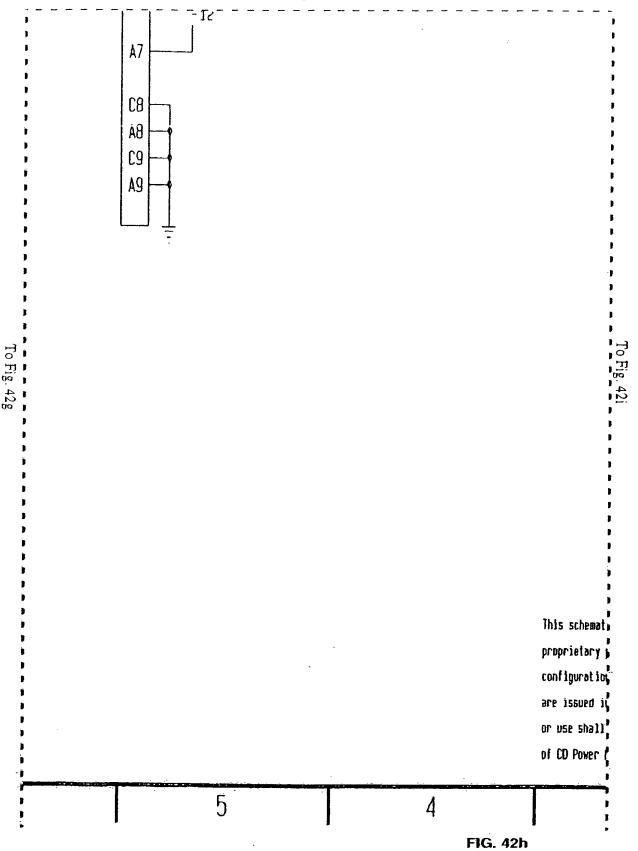
Attorney Docket No. 6270/76, Serial No. 10/968,434 613,701

To Fig. 42c

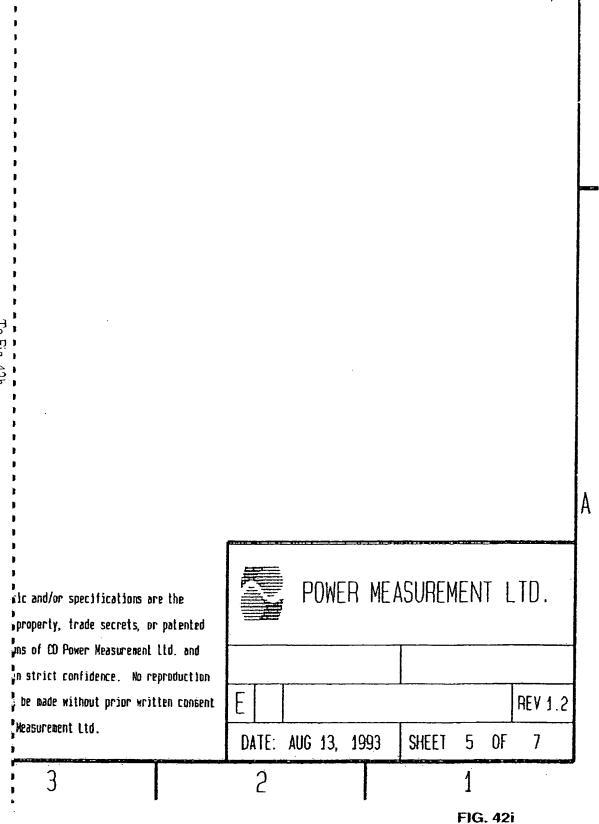


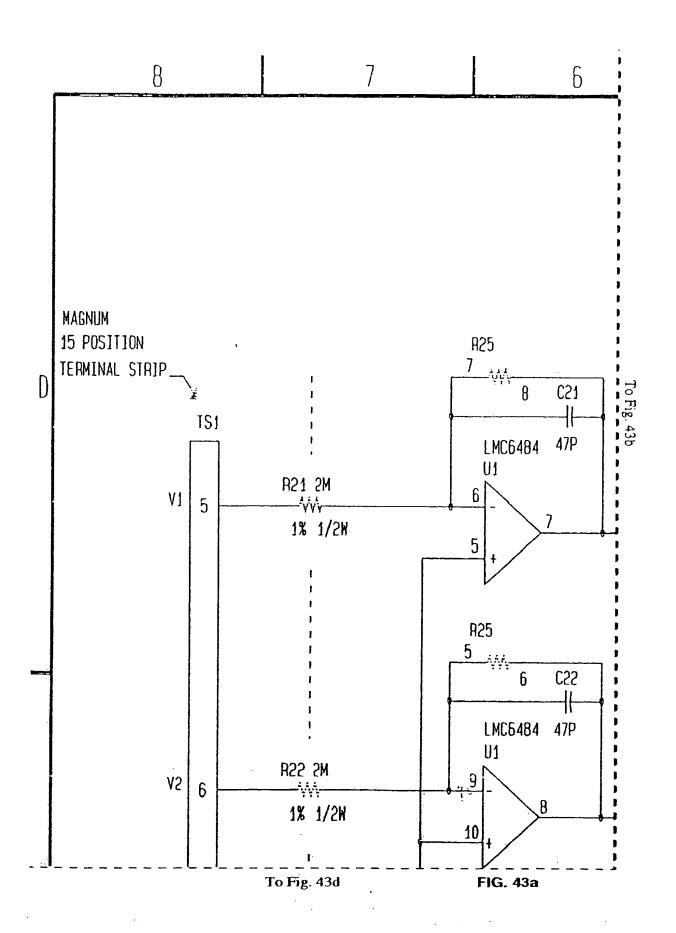


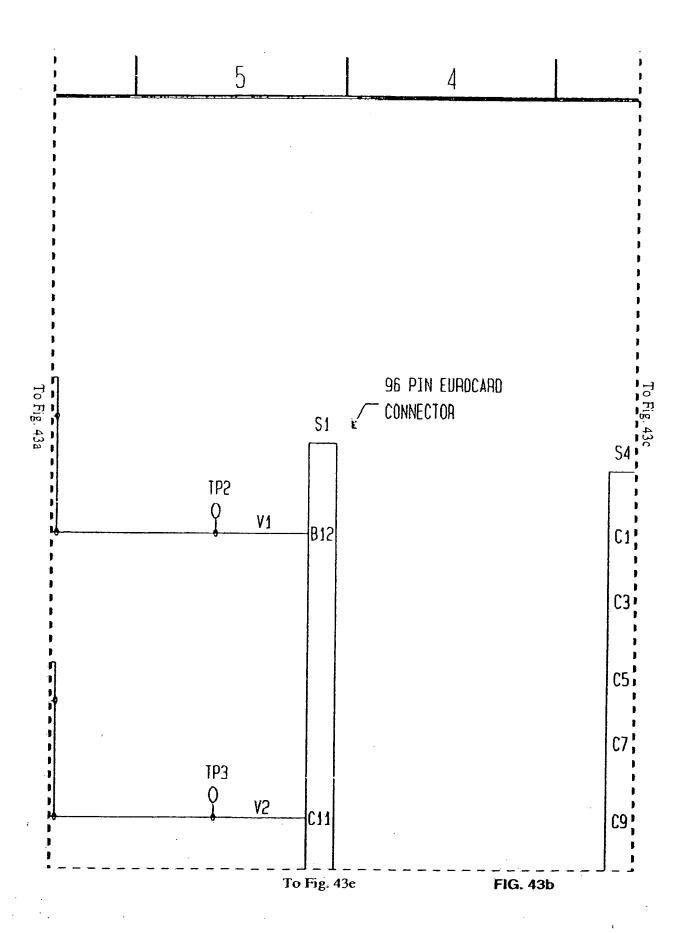


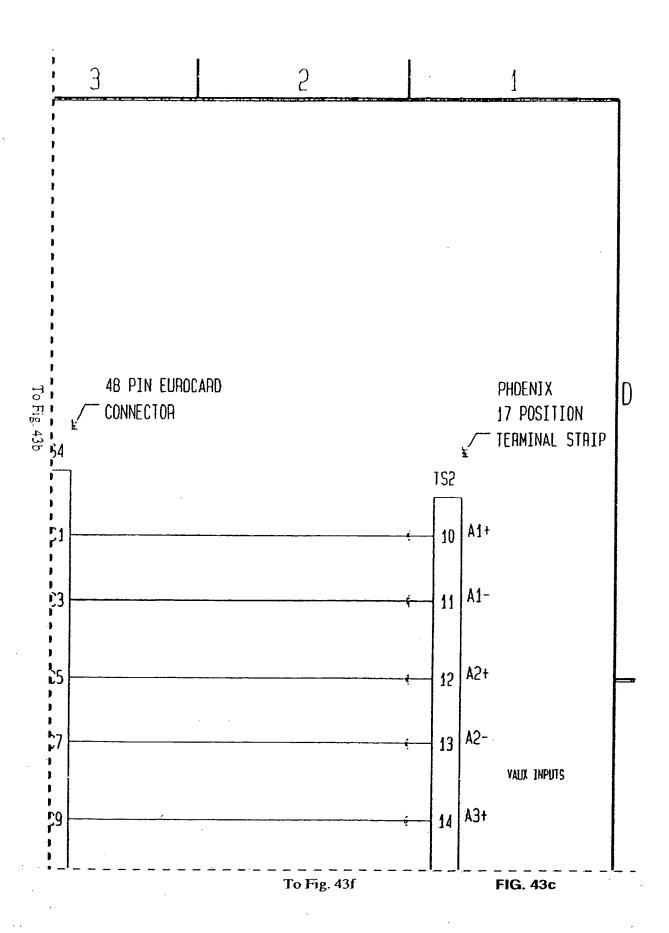


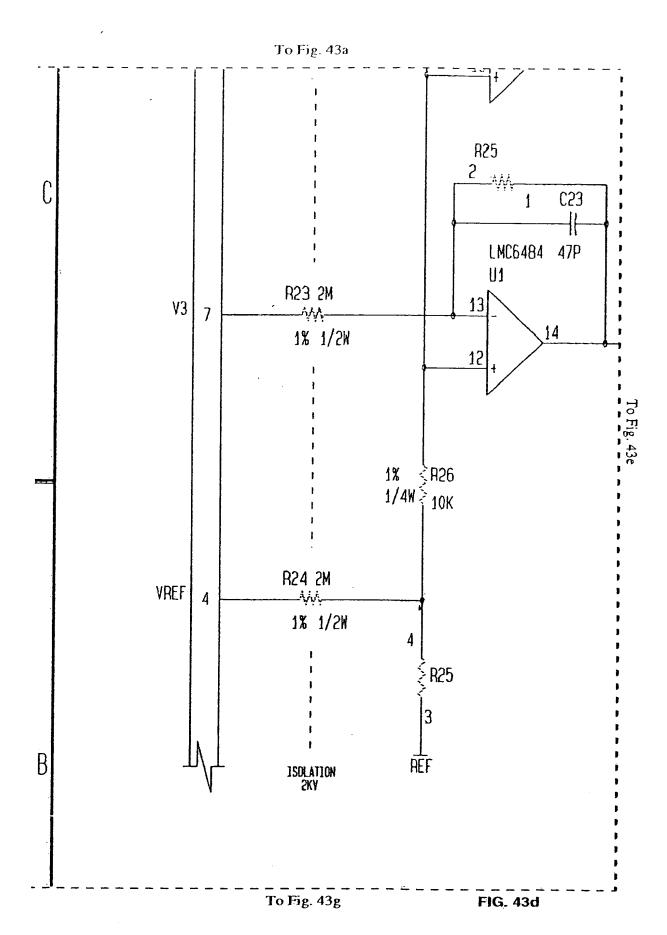
To Fig. 42f

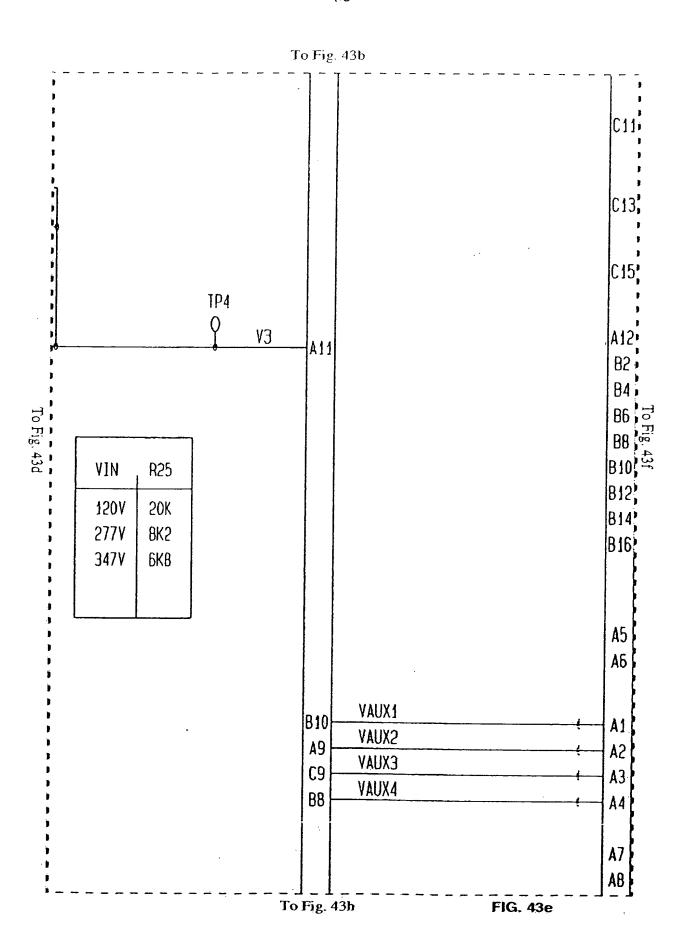




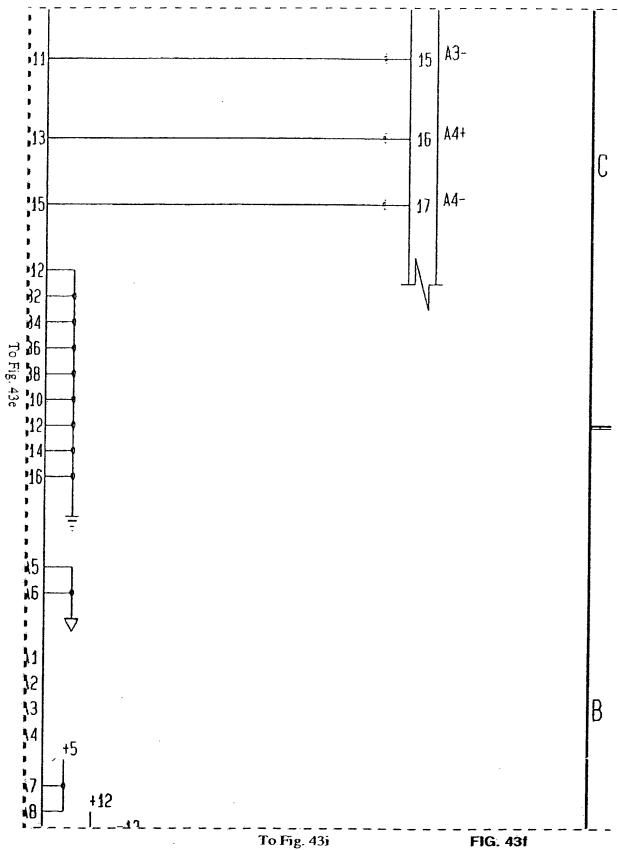


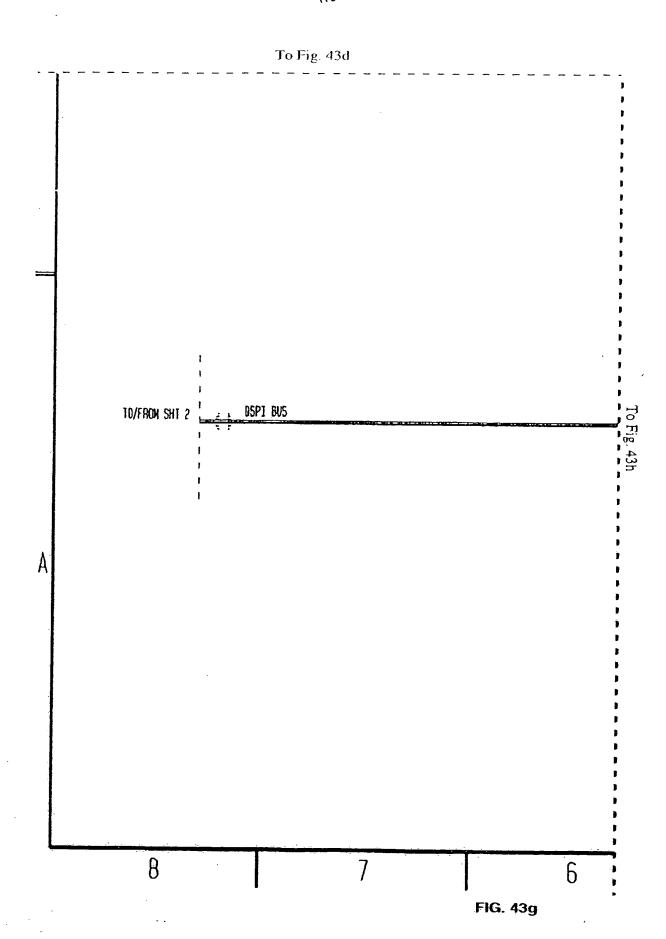


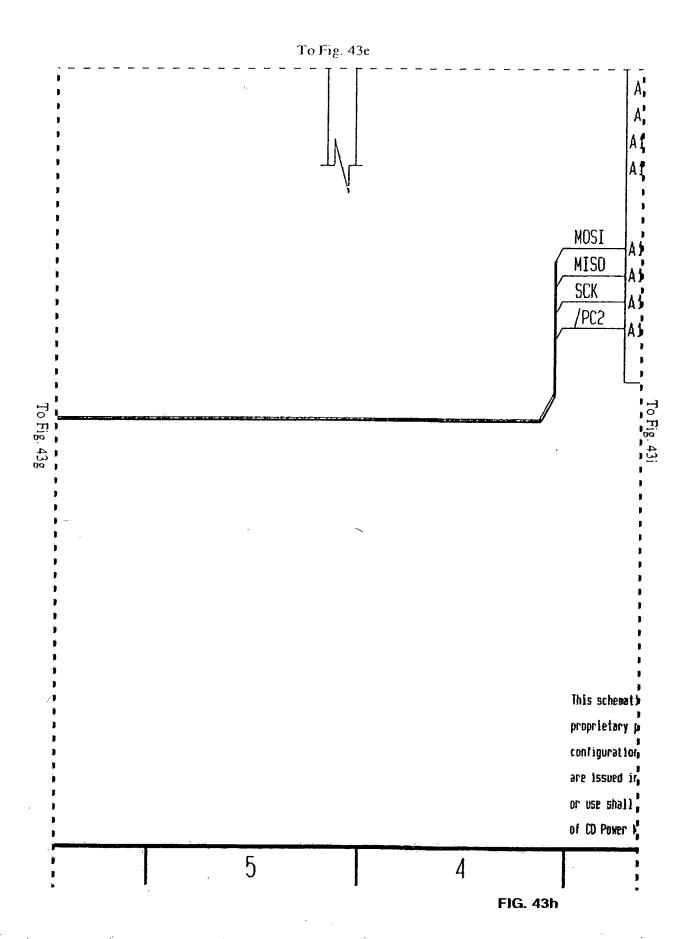












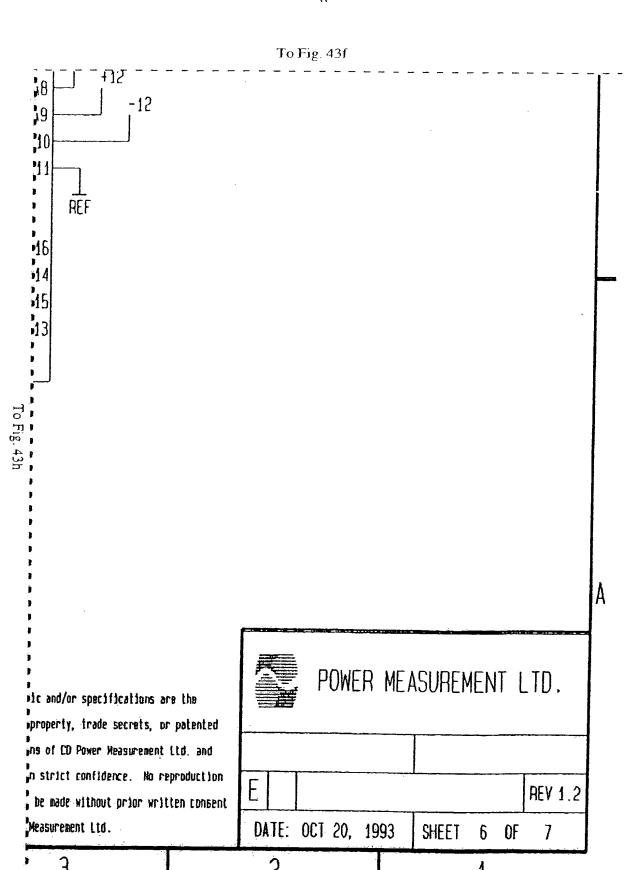
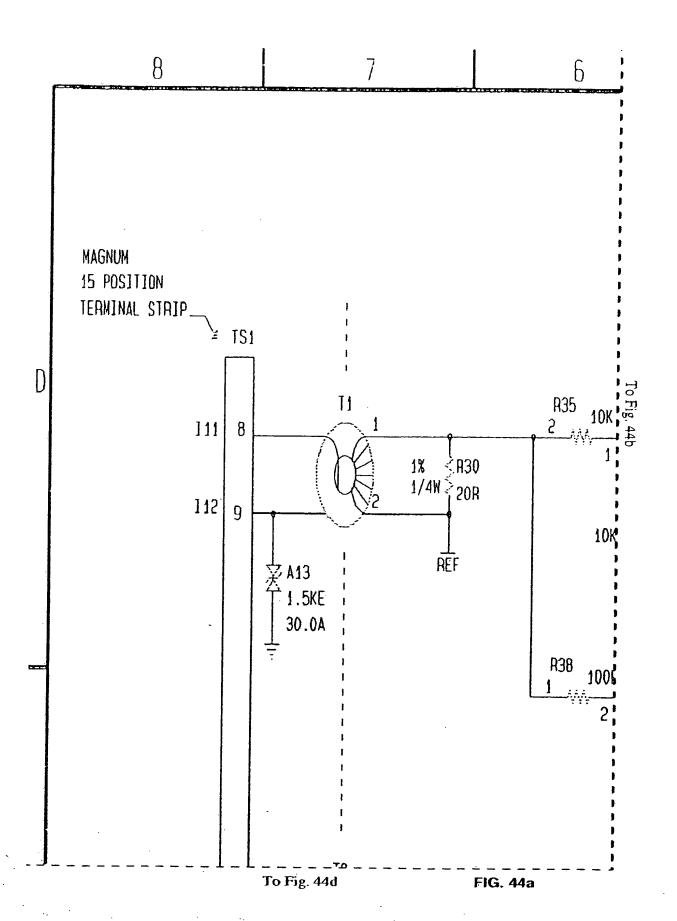
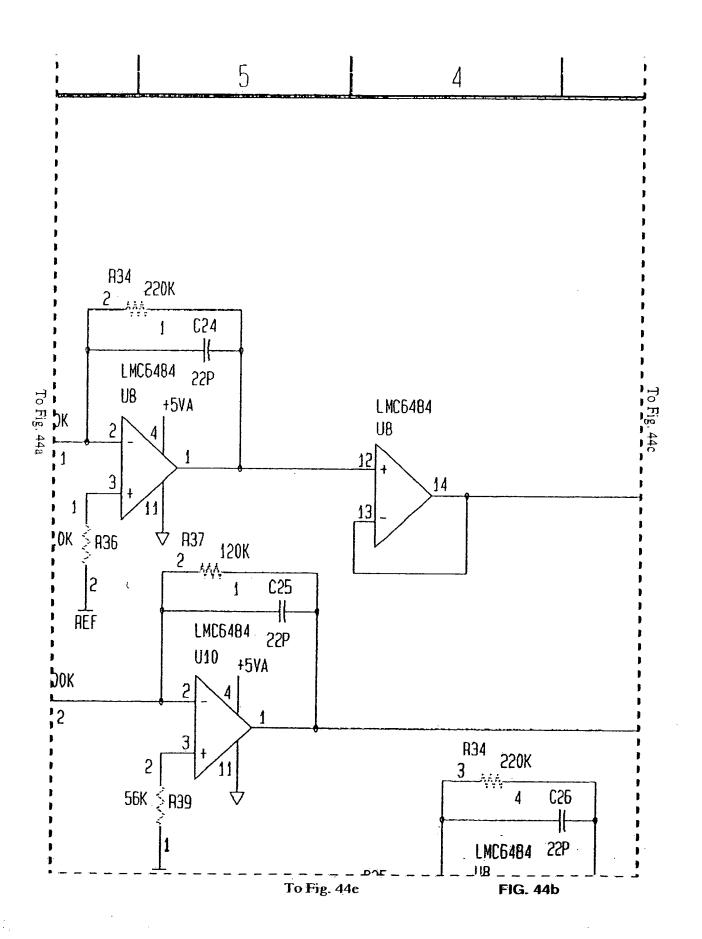
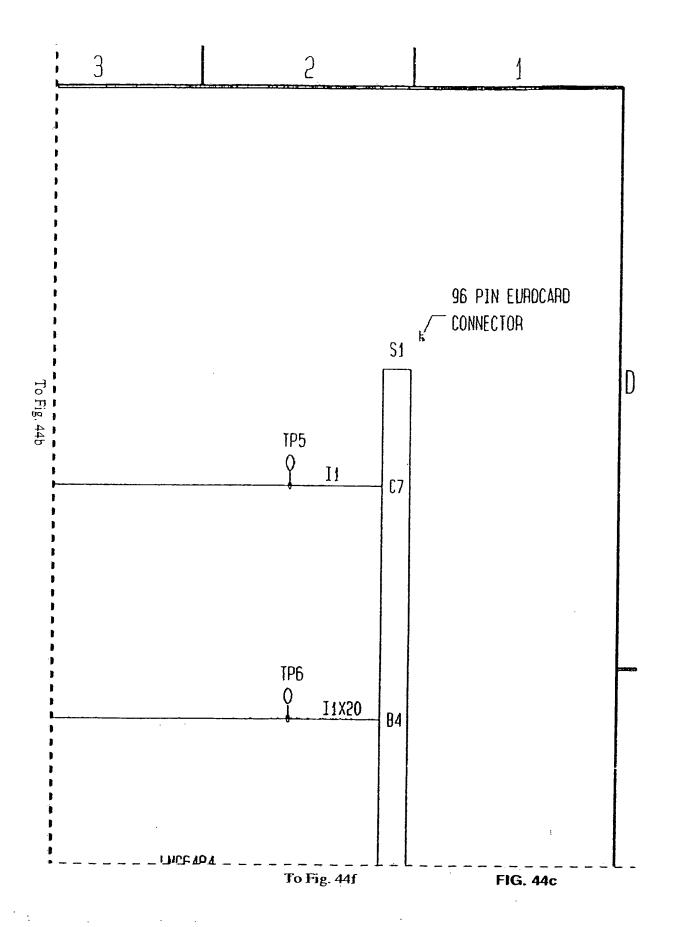


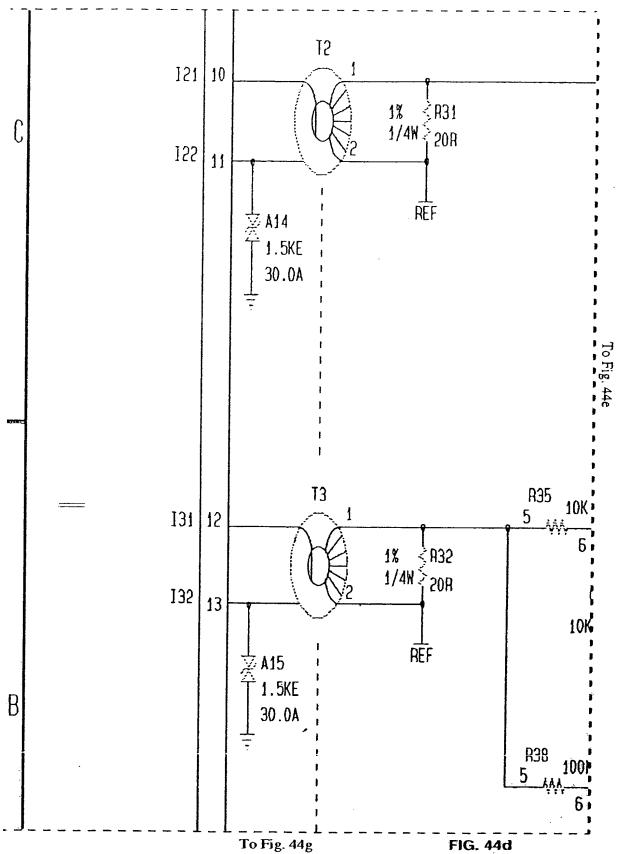
FIG. 43i

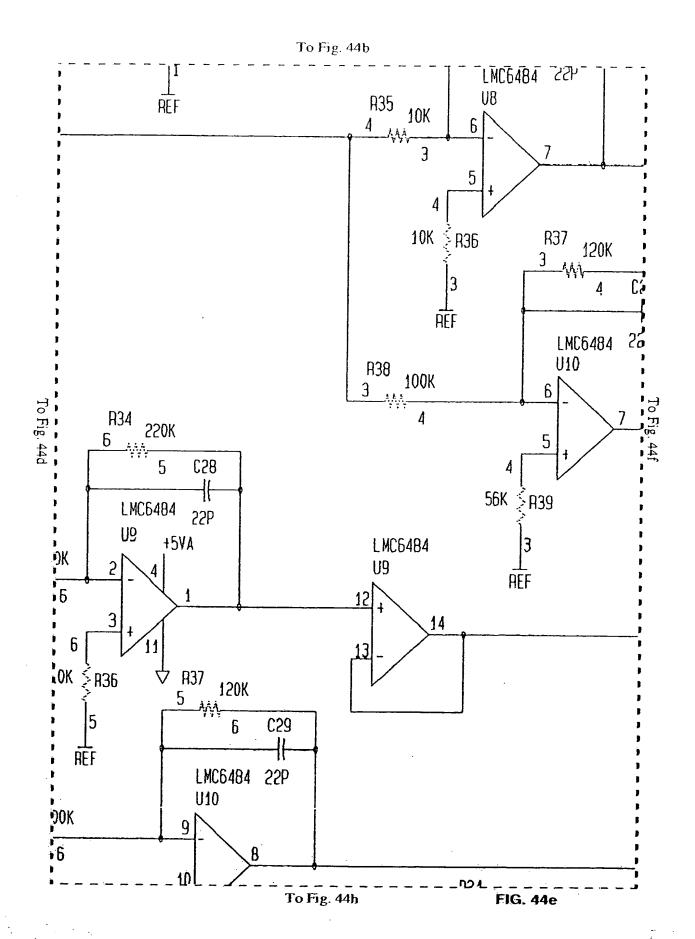




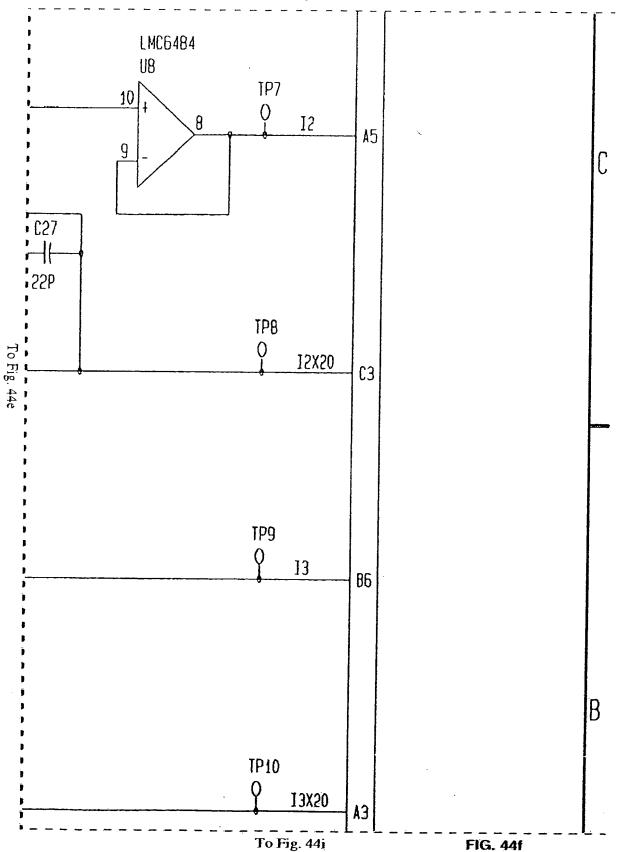


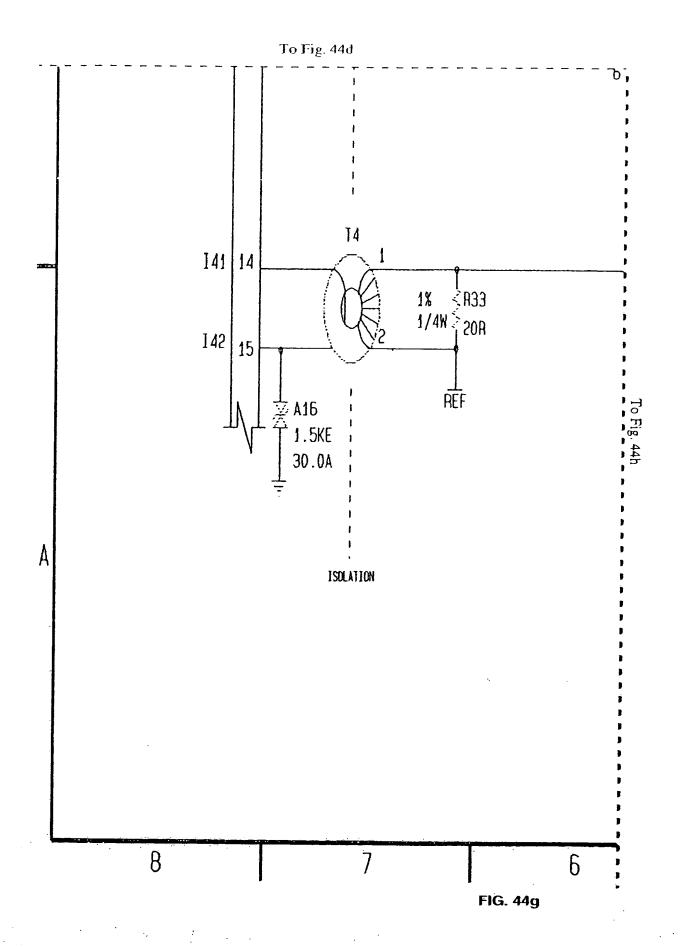


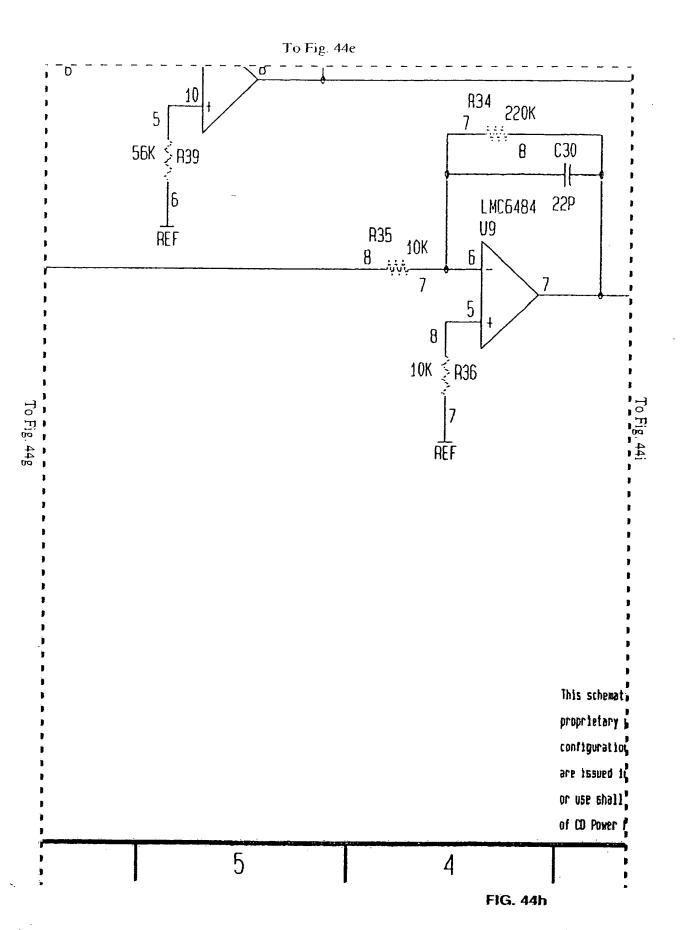




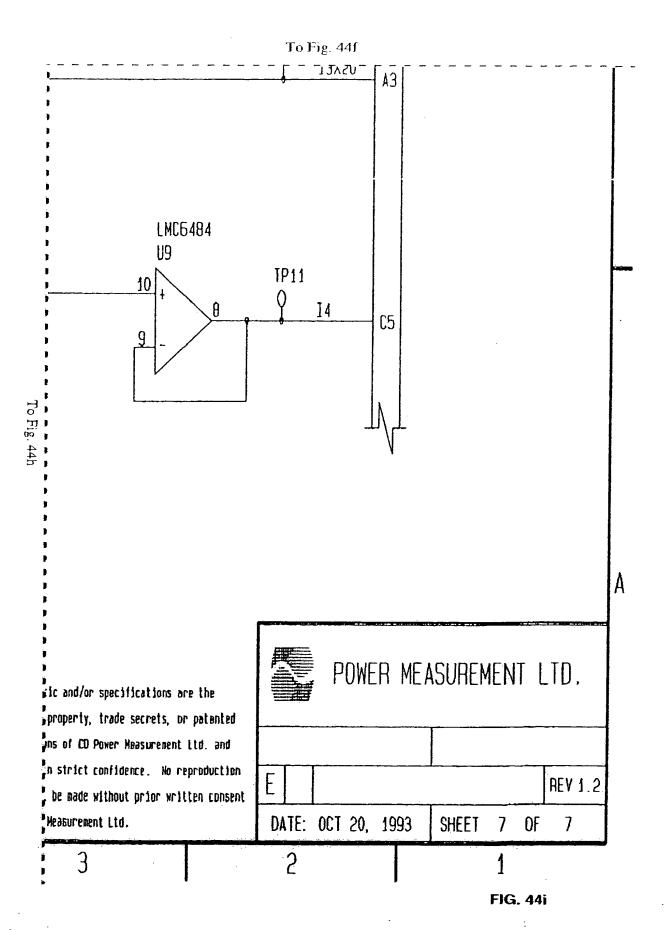


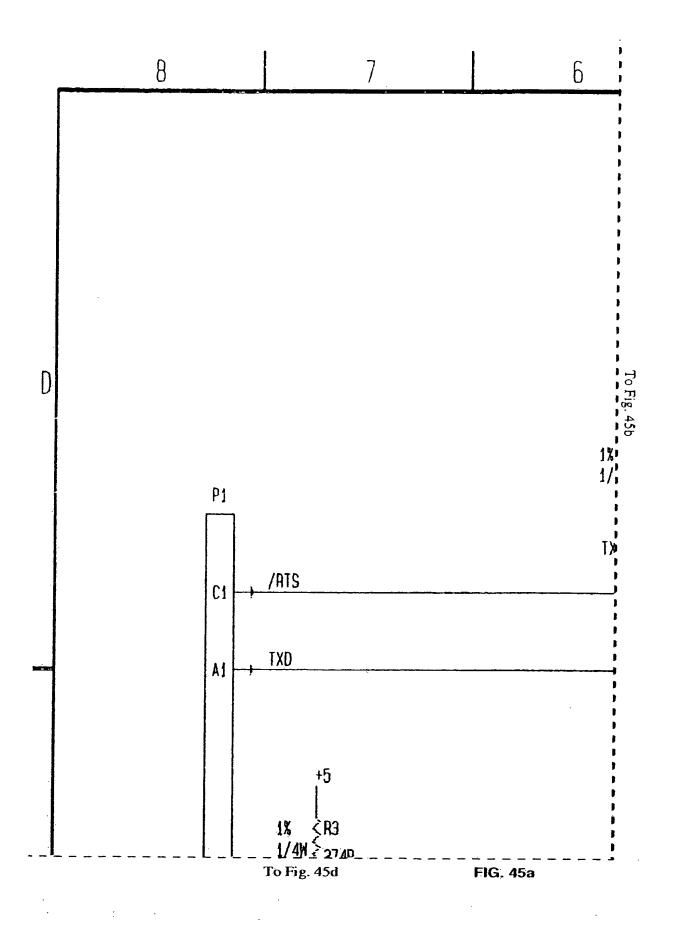


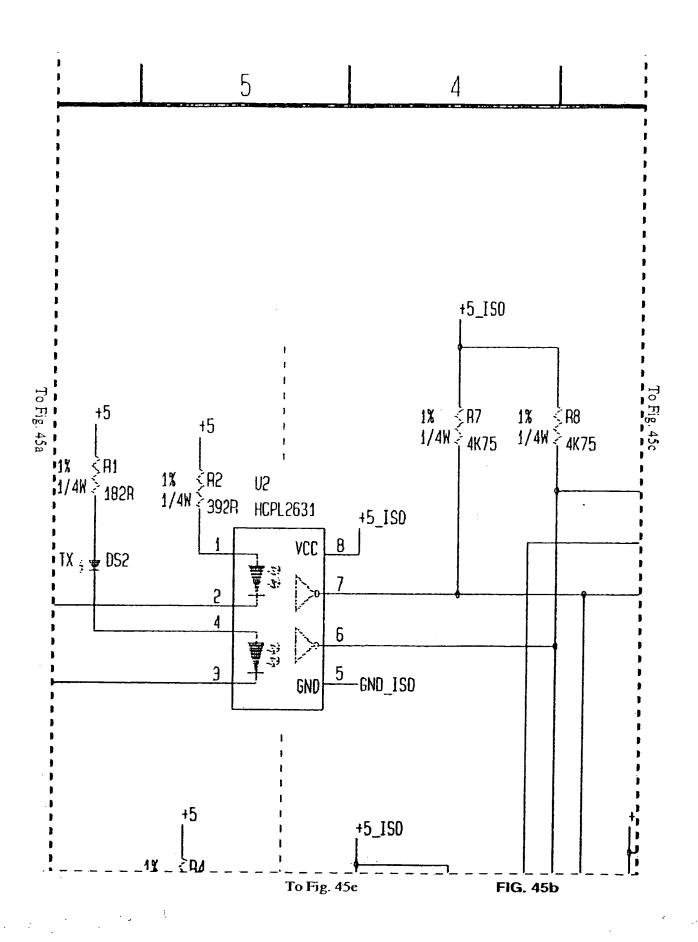


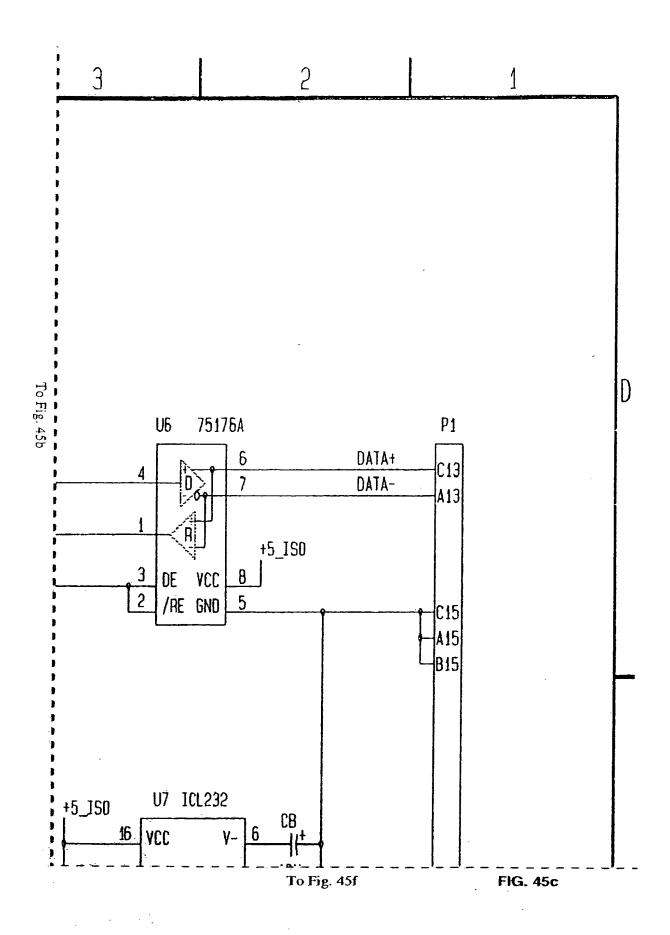


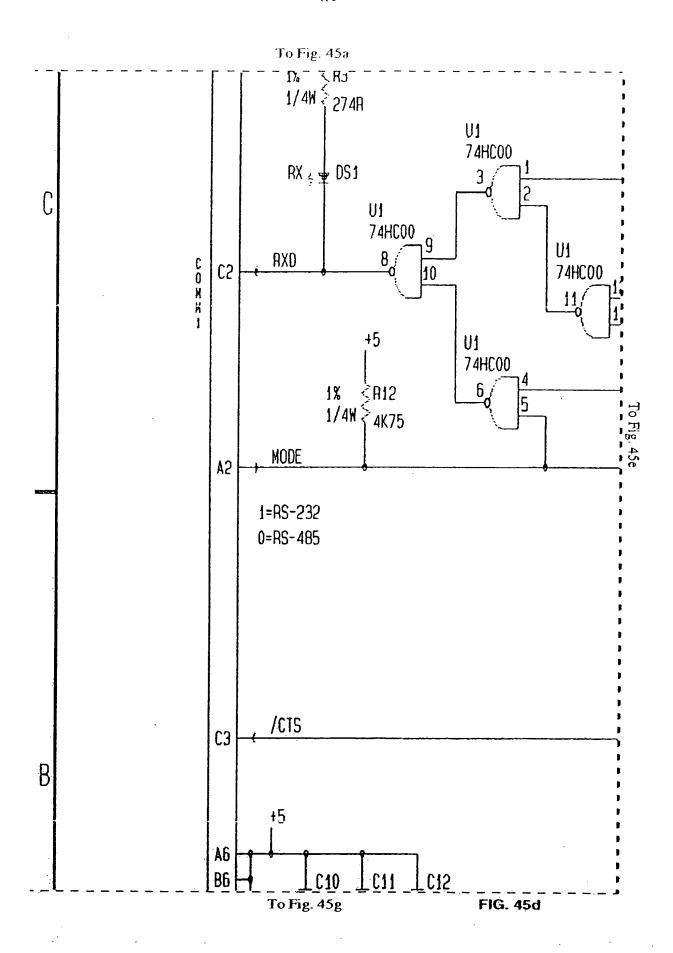
Attorney Docket No. 6270776, Serial No. 10/068,431- (43,70)

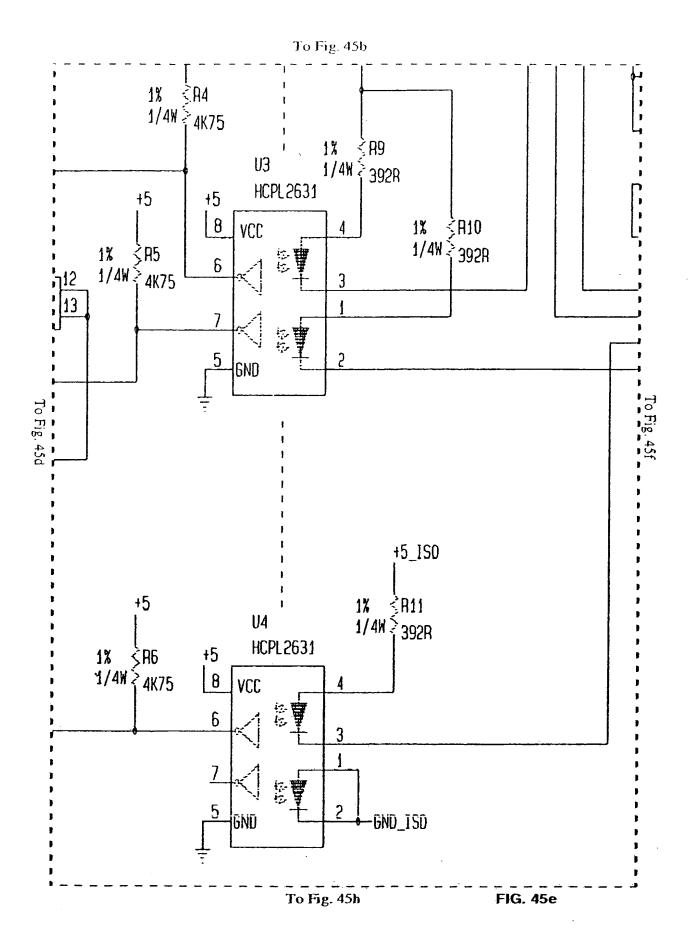


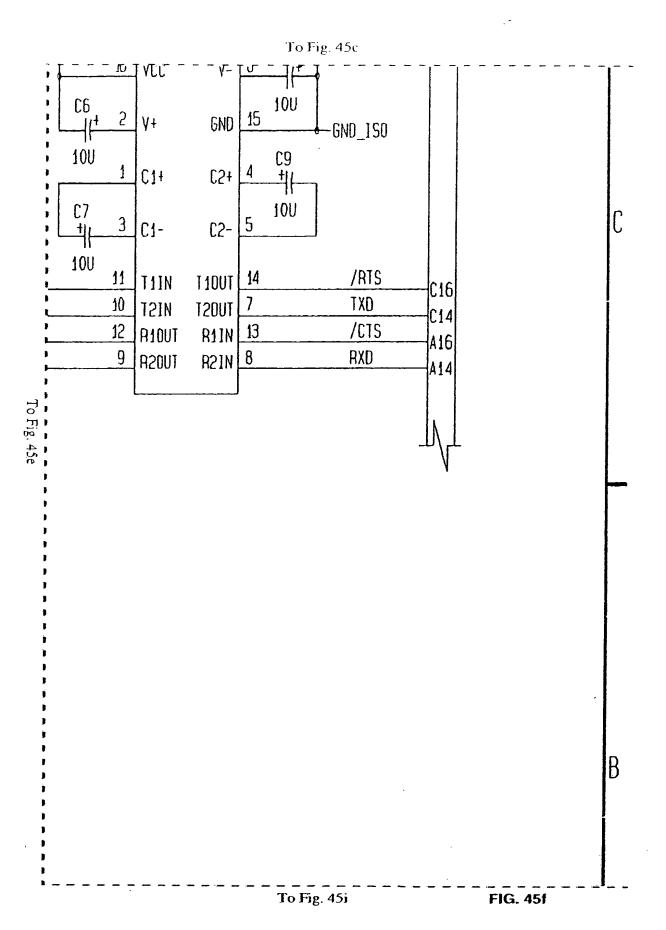


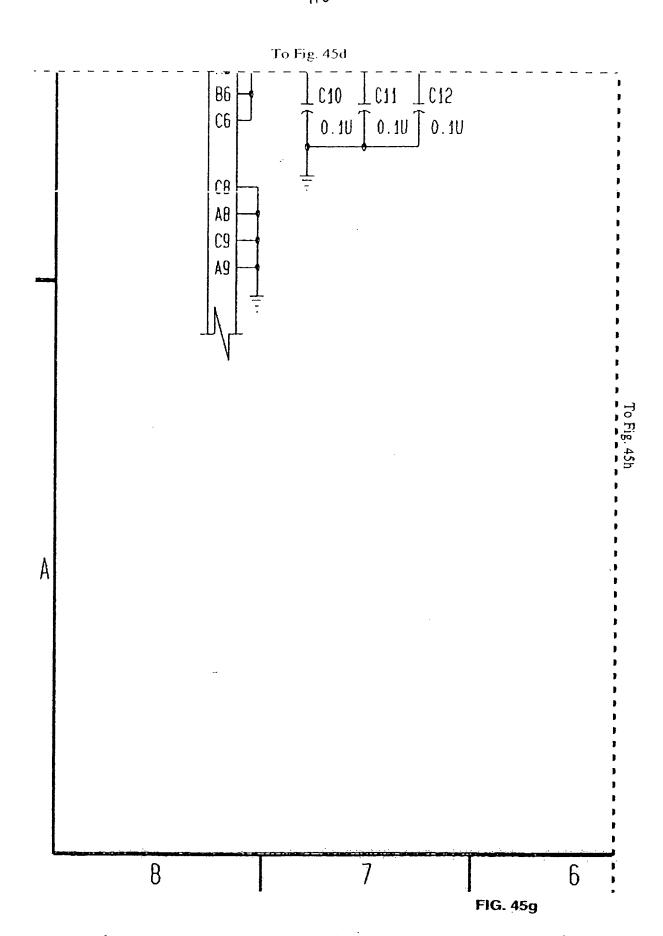


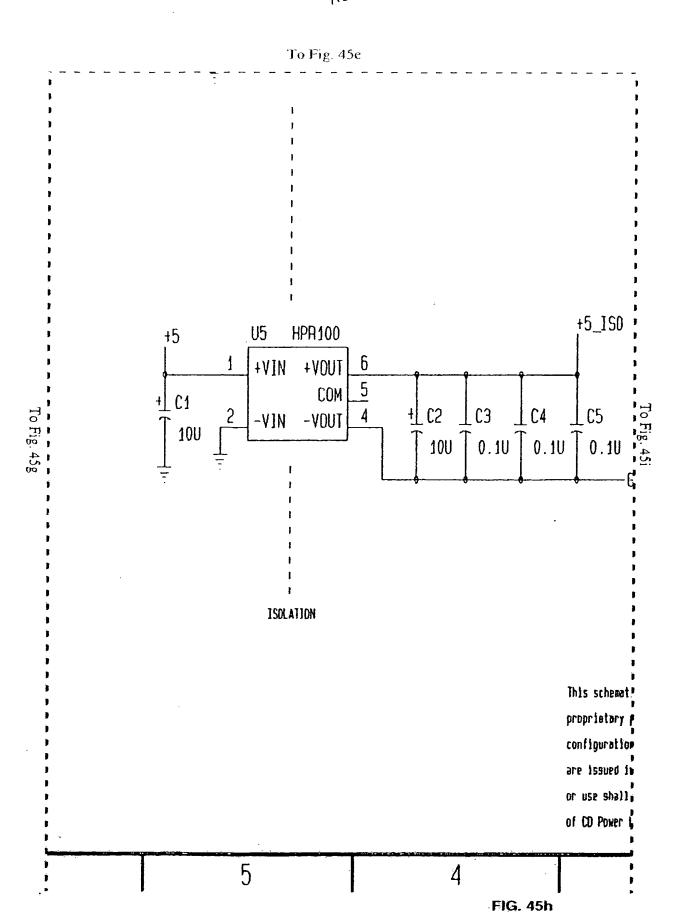






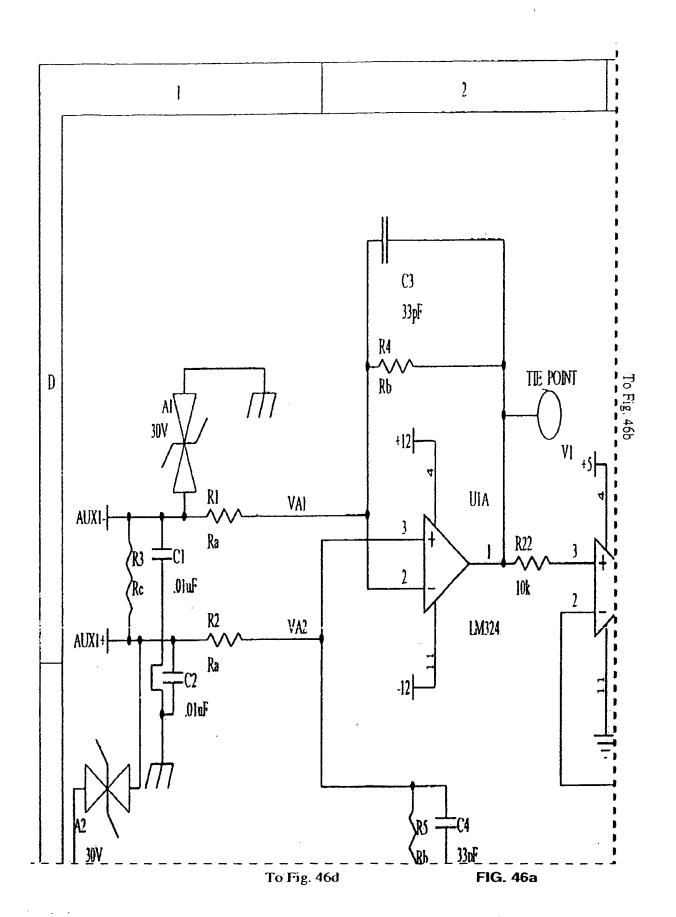


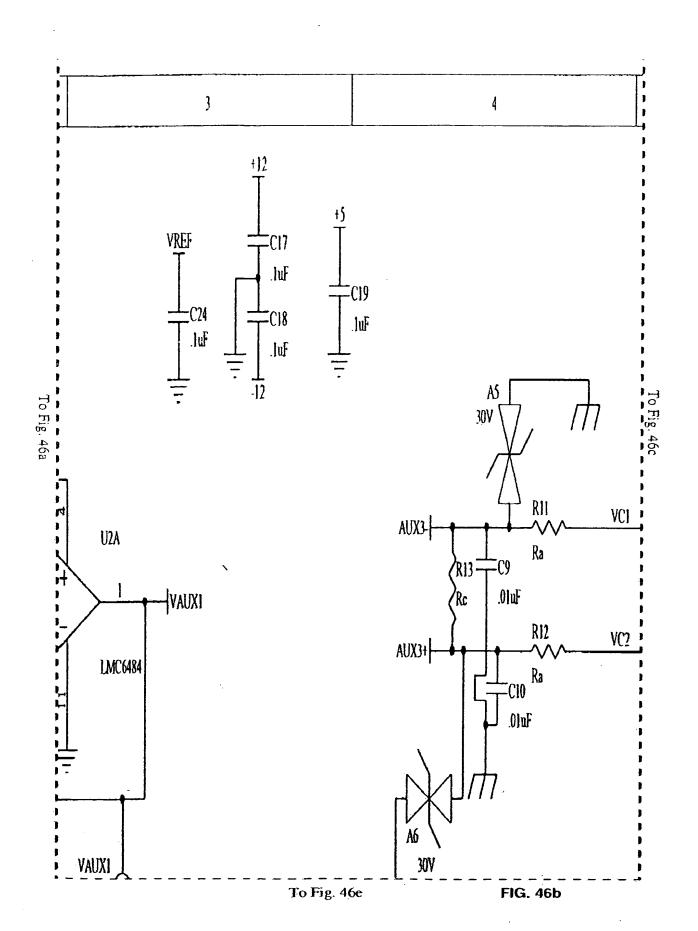


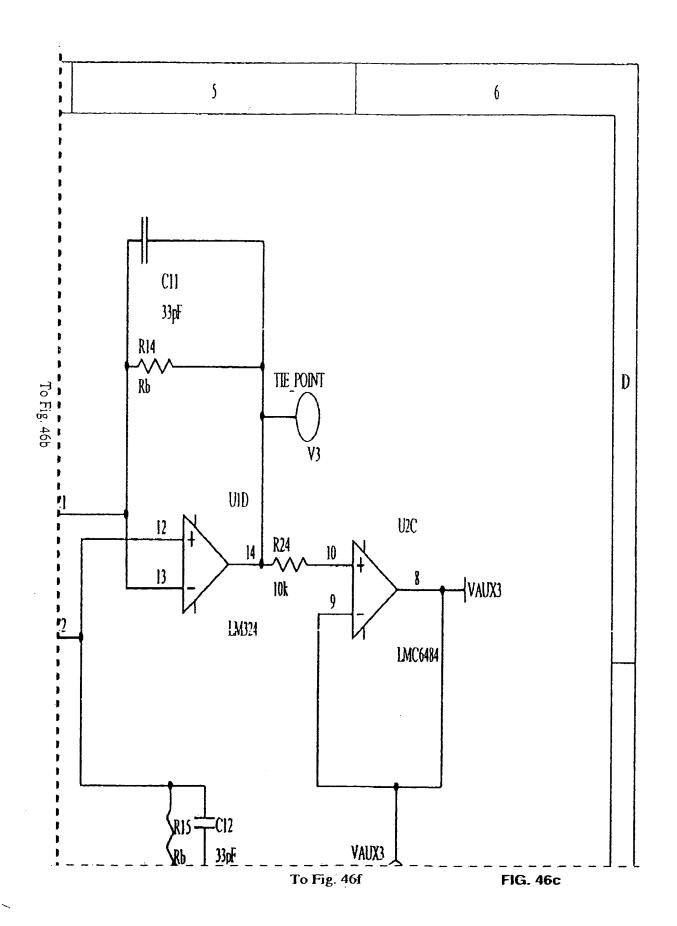


To Fig. 45f

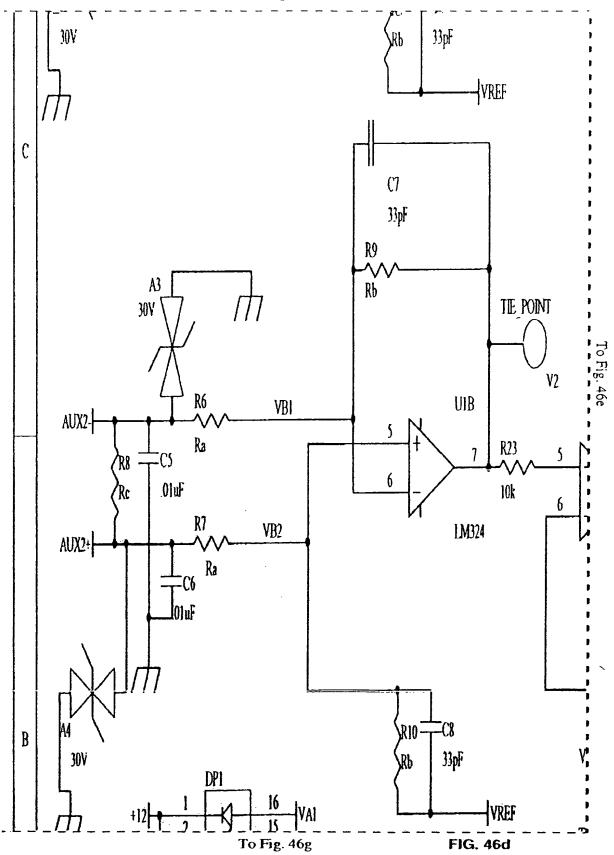
SND_ISO POWER MEASUREMENT LTD. Mic and/or specifications are the DWG# 14500-0023 *property, trade secrets, or patented mns of CD Power Measurement Ltd. and in strict confidence. Ho reproduction **REV 1.1** be made without prior written consent Measurement Ltd. DATE: JAN 7, 1993 SHEET OF 1 FIG. 45i







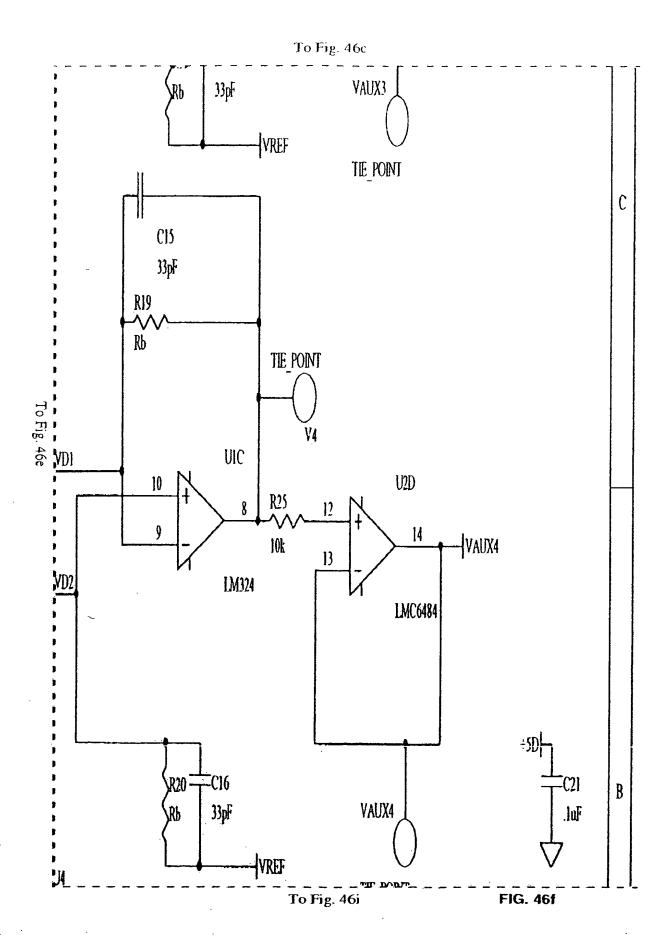
To Fig. 46a



To Fig. 46b VAUXI 30V TIE POINT 30**V** To Fig. 46d **R16** AUX4-U2B Ra C13 R18 — .OluF VAUX2 **R17** AUX41 LMC6484 Ra C14 .OleF **30V** VAUX2

To Fig. 46h

FIG. 46e



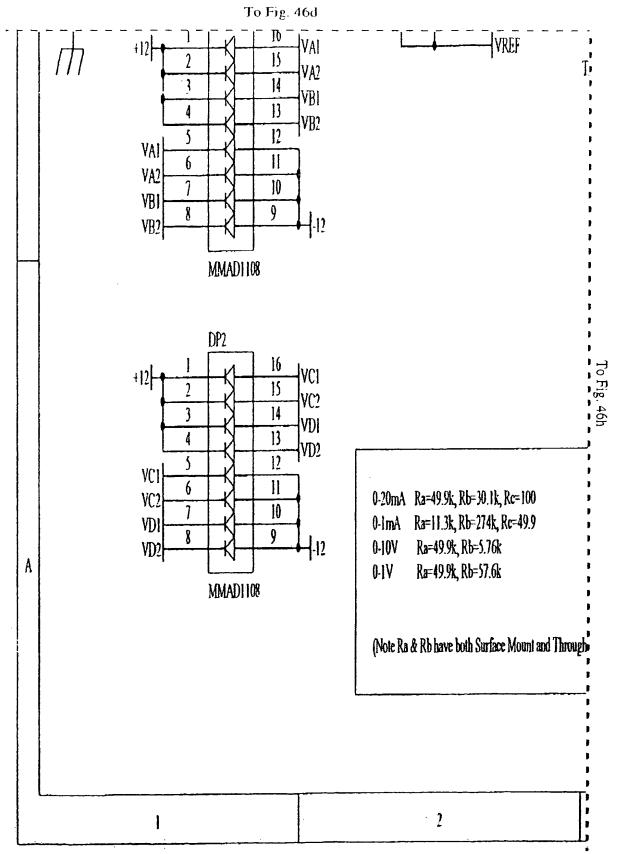
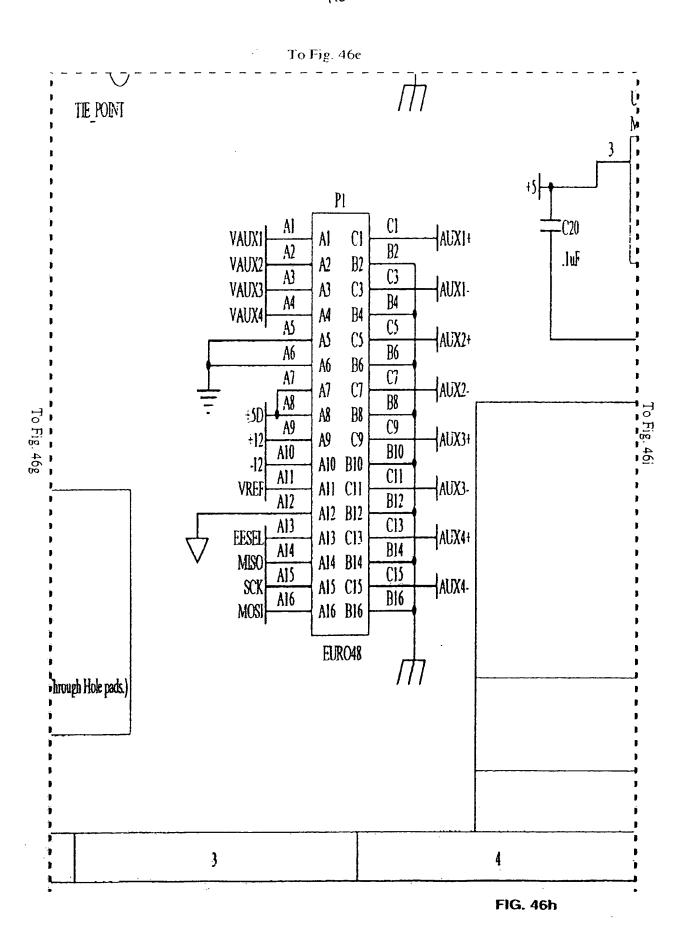


FIG. 46g



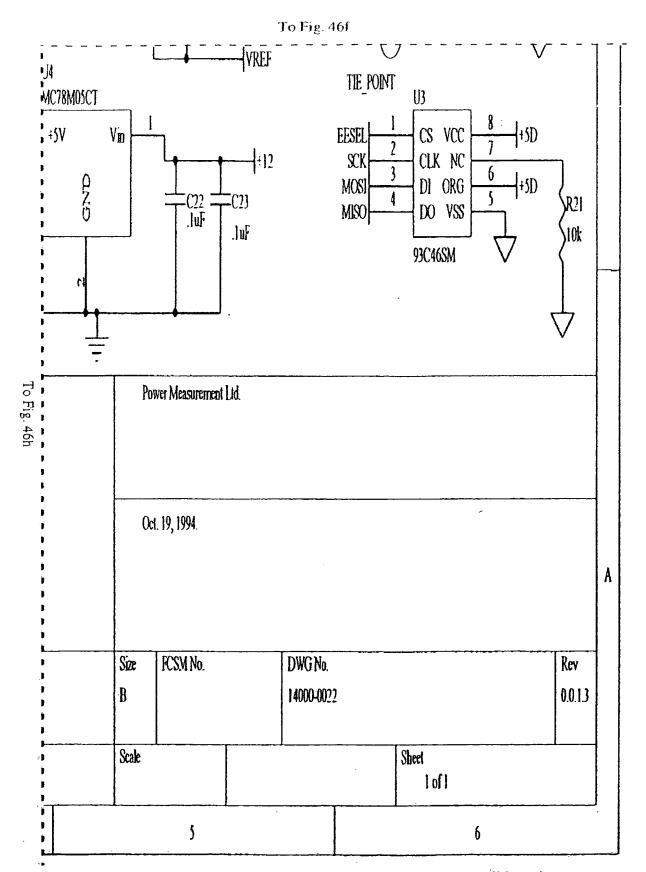


FIG. 46i